

# BZX84Cx Zener Voltage Regulator Diodes in SOT-23

## **1** Features

- Total power dissipation: 430mW (max)
- Low I/O capacitance: 80pF (max)
- Low leakage current: 0.6µA (max)
- Tolerance: ±5%
- Temperature range: -55°C to +150°C
- Leaded package used for automatic optical • inspection (AOI)

# 2 Applications

- Voltage regulation
- Over-voltage protection

# **3 Description**

The BZX84Cx is a family of voltage regulating diodes in a SOT-23 package. The diodes are available in Zener voltages ranging from 8.2V to 39V.

PART NUMBER	PACKAGE <sup>(1)</sup>	PACKAGE SIZE <sup>(2)</sup>		
BZX84Cx	DBZ (SOT-23, 3)	2.92mm × 2.37mm		

For more information, see Section 8. (1)

(2)The package size (length × width) is a nominal value and includes pins, where applicable.

Cathode

Anode

#### **Functional Block Diagram**





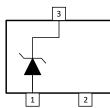
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# **4** Pin Configuration and Functions



### Figure 4-1. DBZ Package, 3-Pin SOT-23 (Top View)

#### Table 4-1. Pin Functions

PIN	DESCRIPTION				
NO.	DESCRIPTION				
1	Anode				
2	No Connect				
3	Cathode				

## **5** Specifications

### 5.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted) (1)

		MIN	MAX	UNIT
P <sub>D</sub> <sup>(2) (3)</sup>	Total Power Dissipation		430	mW
T <sub>A</sub>	Ambient Operating Temperature	-55	150	°C
T <sub>stg</sub>	Storage Temperature	-65	155	°C

(1) Operation outside the Absolute Maximum Ratings may cause permanent device damage. Absolute maximum ratings do not imply functional operation of the device at these or any other conditions beyond those listed under Recommended Operating Conditions. If briefly operating outside the Recommended Operating Conditions but within the Absolute Maximum Ratings, the device may not sustain damage, but it may not be fully functional. Operating the device in this manner may affect device reliability, functionality, performance, and shorten the device lifetime.

(2) FR-4 printed circuit board, single sided copper, standard footprint

(3) Measured at 25°C

## **5.2 Recommended Operating Conditions**

over operating free-air temperature range (unless otherwise noted)

			MIN	NOM	MAX	UNIT
V <sub>F</sub>	Forward Voltage	I <sub>F</sub> = 10mA			0.9	V
T <sub>A</sub>	Γ <sub>A</sub> Operating free-air temperature		-55		150	°C

### **5.3 Thermal Information**

		BZX84Cx	
	THERMAL METRIC <sup>(1)</sup>	DBZ (SOT-23)	UNIT
		3 PINS	
R <sub>0JA</sub>	Junction-to-ambient thermal resistance	285.5	°C/W
R <sub>0JC(top)</sub>	Junction-to-case (top) thermal resistance	197.5	°C/W
R <sub>θJB</sub>	Junction-to-board thermal resistance	118.5	°C/W
$\Psi_{JT}$	Junction-to-top characterization parameter	90.6	°C/W
$\Psi_{JB}$	Junction-to-board characterization parameter	117.8	°C/W
R <sub>0JC(bot)</sub>	Junction-to-case (bottom) thermal resistance	N/A	°C/W

(1) For more information about traditional and new thermal metrics, see the Semiconductor and IC Package Thermal Metrics application note.



## **5.4 Electrical Characteristics**

At T<sub>A</sub> = 25°C (unless otherwise noted)

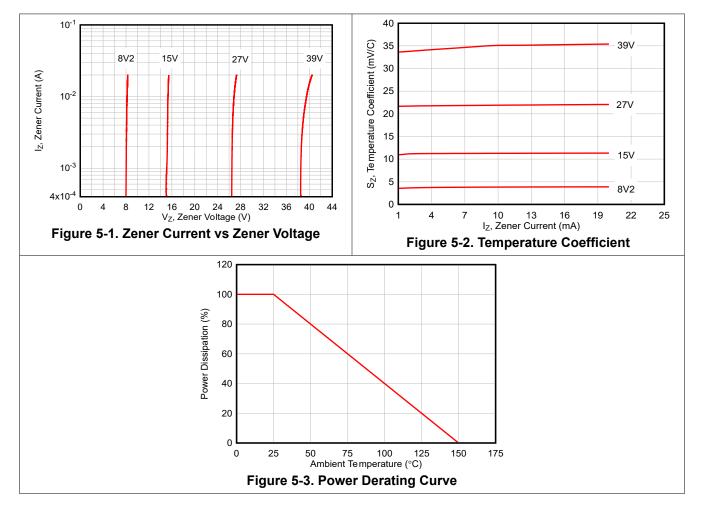
Part Number	Zener Voltage V <sub>Z</sub> (V) at I <sub>Z</sub>		Zener Impedance Z <sub>ZT</sub> (Ω)		Reverse Leakage Current Ι <sub>R</sub> (μΑ)		Temperature Coefficient S <sub>Z</sub> (mV/C) at I <sub>Z</sub>		Capacitance $C_D (pF)^{(1)}$		
	MIN	TYP	MAX	I <sub>Z</sub> (mA)	MAX	l <sub>Z</sub> (mA)	MAX	V <sub>R</sub> (V)	MAX	I <sub>Z</sub> (mA)	MAX
BZX84C5V6 (2)	5.2	5.6	6	5	40	5	1	2	2.5	5	300
BZX84C6V8 (2)	6.4	6.8	7.2	5	15	5	2	4	4.5	5	200
BZX84C8V2	7.79	8.2	8.61	5	15	5	0.6	5.75	6.2	5	80
BZX84C9V1 (2)	8.5	9.1	9.6	5	15	5	0.5	6	7	5	150
BZX84C15V	14.25	15	15.75	5	30	5	0.03	10.5	13	5	50
BZX84C16V (2)	15.3	16	17.1	5	40	5	0.05	11.2	14	5	75
BZX84C18V (2)	16.8	18	19.1	5	45	5	0.05	12.6	16	5	70
BZX84C27V	25.65	27	28.35	2	80	2	0.03	18.9	25.3	2	35
BZX84C39V	37.05	39	40.95	2	130	2	0.03	27.3	41.2	2	25

(1)  $f = 1MHz, V_R = 0$ 

(2) Preview Information (not Production Data)



## **5.5 Typical Characteristics**



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Product Folder Links: BZX84C8V2 BZX84C15V BZX84C27V BZX84C39V



## 6 Device and Documentation Support

#### 6.1 Documentation Support

#### 6.1.1 Related Documentation

For related documentation, see the following:

- Texas Instruments, Diodes Packaging and Layout Guide
- Texas Instruments, Diodes Layout Guide User's Guide
- Texas Instruments, Generic Evaluation Module User's Guide

### 6.2 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on ti.com. Click on *Notifications* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

#### 6.3 Support Resources

TI E2E<sup>™</sup> support forums are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

Linked content is provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's Terms of Use.

#### 6.4 Trademarks

TI E2E<sup>™</sup> is a trademark of Texas Instruments.

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#### 6.5 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### 6.6 Glossary

TI Glossary This glossary lists and explains terms, acronyms, and definitions.

## 7 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

С	Changes from Revision * (December 2024) to Revision A (August 2025)						
•	Updated power dissipation from 250mW to 430mW (max)	1					
•	Updated Electrical Characteristics to include 15V and 39V devices	5					

DATE	REVISION	NOTES
December 2024	*	Initial Release

## 8 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

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### **PACKAGING INFORMATION**

Orderable part number	Status	Material type	Package   Pins	Package qty   Carrier	RoHS	Lead finish/	MSL rating/	Op temp (°C)	Part marking
	(1)	(2)			(3)	Ball material	Peak reflow		(6)
						(4)	(5)		
BZX84C15VDBZR	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	SN	Level-1-260C-UNLIM	-55 to 150	309G
BZX84C15VDBZR.B	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	SN	Level-1-260C-UNLIM	-55 to 150	309G
BZX84C27VDBZR	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	SN	Level-1-260C-UNLIM	-55 to 150	3MXG
BZX84C27VDBZR.B	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	SN	Level-1-260C-UNLIM	-55 to 150	3MXG
BZX84C39VDBZR	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	SN	Level-1-260C-UNLIM	-55 to 150	3IVG
BZX84C39VDBZR.B	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	SN	Level-1-260C-UNLIM	-55 to 150	3IVG
BZX84C8V2DBZR	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	SN	Level-1-260C-UNLIM	-55 to 150	3ITG
BZX84C8V2DBZR.B	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	SN	Level-1-260C-UNLIM	-55 to 150	3ITG

<sup>(1)</sup> Status: For more details on status, see our product life cycle.

(2) Material type: When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

<sup>(3)</sup> RoHS values: Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.

<sup>(4)</sup> Lead finish/Ball material: Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

<sup>(5)</sup> MSL rating/Peak reflow: The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

<sup>(6)</sup> Part marking: There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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#### OTHER QUALIFIED VERSIONS OF BZX84C15V, BZX84C27V, BZX84C39V, BZX84C8V2 :

• Automotive : BZX84C15V-Q1, BZX84C27V-Q1, BZX84C39V-Q1, BZX84C8V2-Q1

NOTE: Qualified Version Definitions:

• Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects

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\*All dimensions are nominal

STRUMENTS

### TAPE AND REEL INFORMATION





#### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



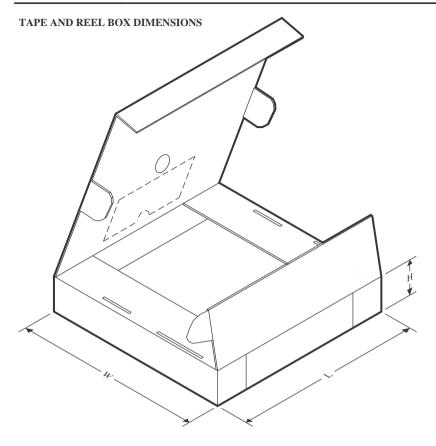
Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
BZX84C15VDBZR	SOT-23	DBZ	3	3000	180.0	8.4	3.2	2.85	1.3	4.0	8.0	Q3
BZX84C27VDBZR	SOT-23	DBZ	3	3000	180.0	8.4	3.2	2.85	1.3	4.0	8.0	Q3
BZX84C39VDBZR	SOT-23	DBZ	3	3000	180.0	8.4	3.2	2.85	1.3	4.0	8.0	Q3
BZX84C8V2DBZR	SOT-23	DBZ	3	3000	180.0	8.4	3.2	2.85	1.3	4.0	8.0	Q3



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# PACKAGE MATERIALS INFORMATION

2-Jul-2025



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
BZX84C15VDBZR	SOT-23	DBZ	3	3000	210.0	185.0	35.0
BZX84C27VDBZR	SOT-23	DBZ	3	3000	210.0	185.0	35.0
BZX84C39VDBZR	SOT-23	DBZ	3	3000	210.0	185.0	35.0
BZX84C8V2DBZR	SOT-23	DBZ	3	3000	210.0	185.0	35.0

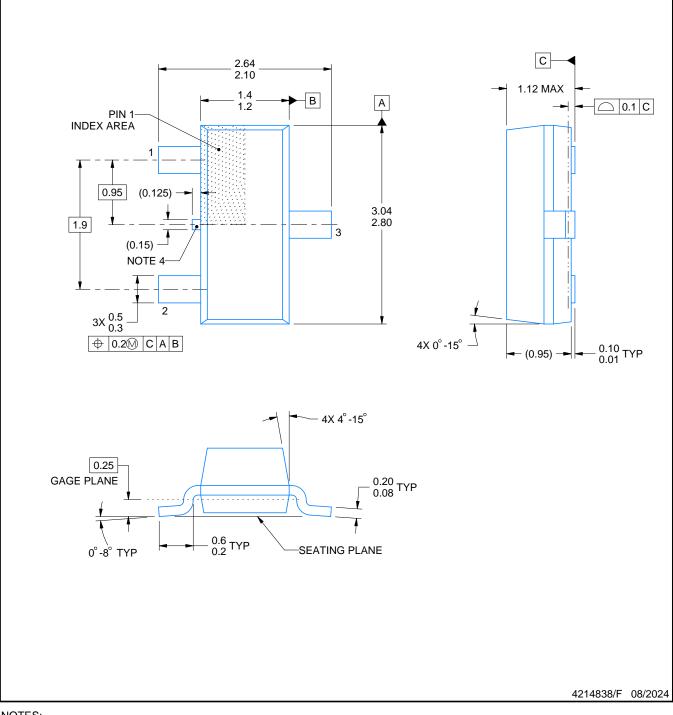
# **DBZ0003A**



# **PACKAGE OUTLINE**

# SOT-23 - 1.12 mm max height

SMALL OUTLINE TRANSISTOR



NOTES:

- All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
  This drawing is subject to change without notice.
  Reference JEDEC registration TO-236, except minimum foot length.

- 4. Support pin may differ or may not be present.
- 5. Body dimensions do not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.25mm per side

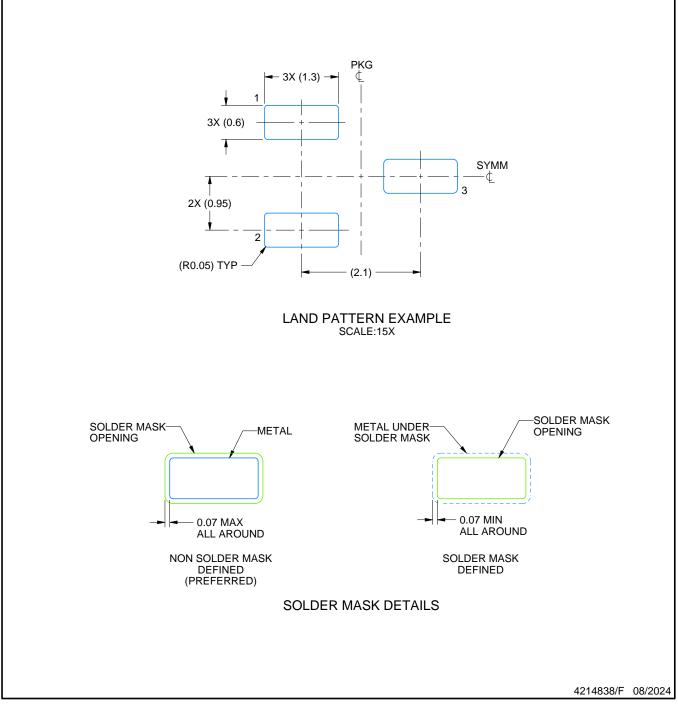


# **DBZ0003A**

# **EXAMPLE BOARD LAYOUT**

# SOT-23 - 1.12 mm max height

SMALL OUTLINE TRANSISTOR



NOTES: (continued)

5. Publication IPC-7351 may have alternate designs.6. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

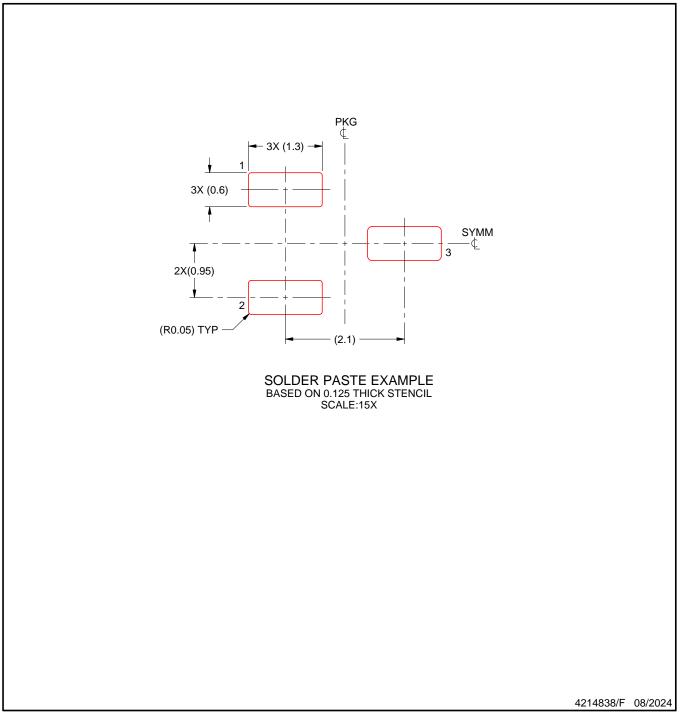


# DBZ0003A

# **EXAMPLE STENCIL DESIGN**

# SOT-23 - 1.12 mm max height

SMALL OUTLINE TRANSISTOR



NOTES: (continued)

7. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

8. Board assembly site may have different recommendations for stencil design.



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