

Revision History					
Rev	ECN #	Approved Date	Approved by	Notes	
N/A	N/A	N/A	N/A	N/A	

A

B

C

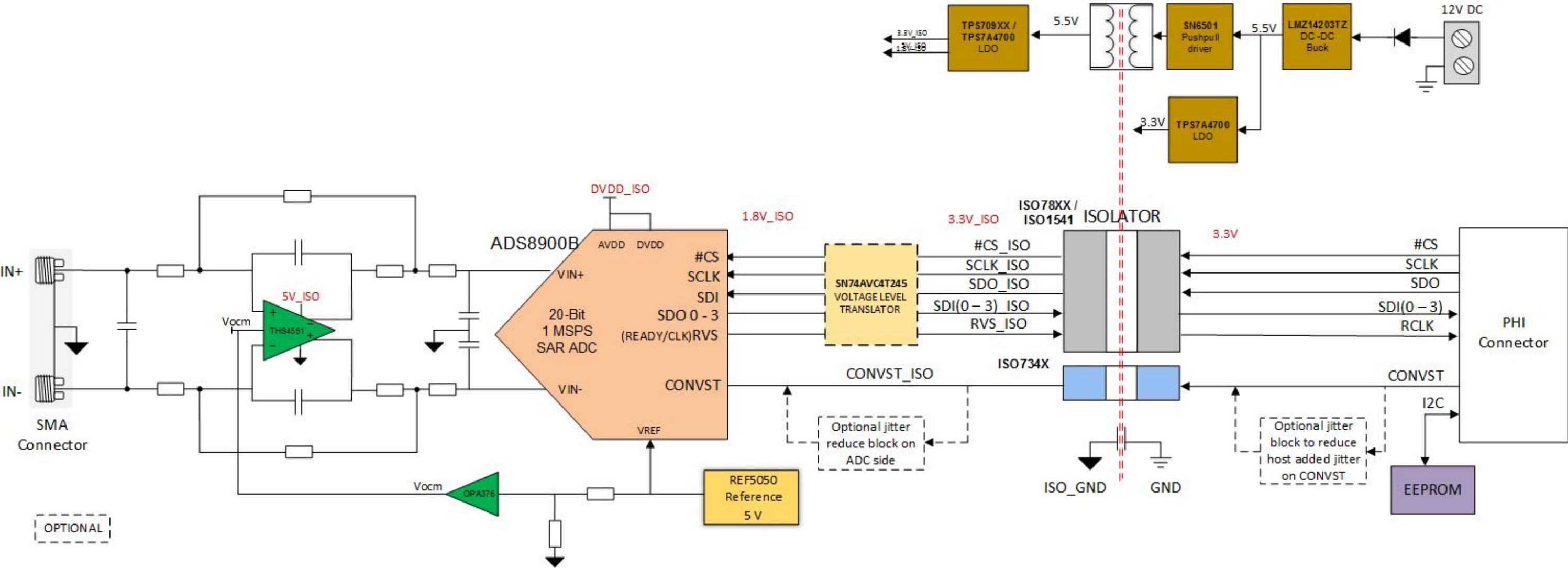
D

A

B

C

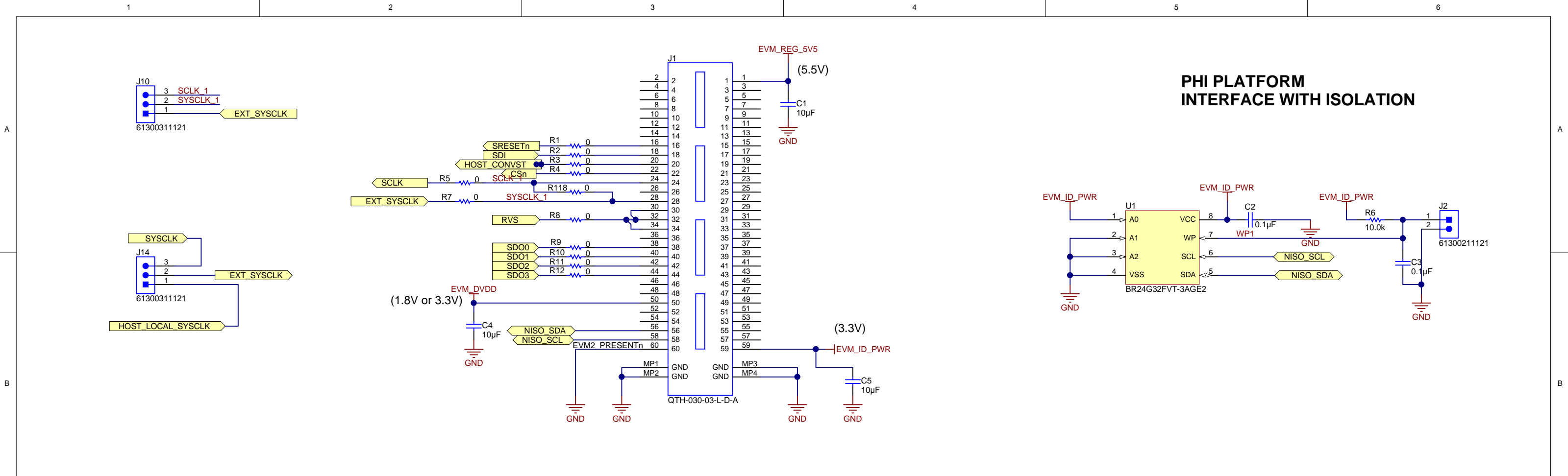
D



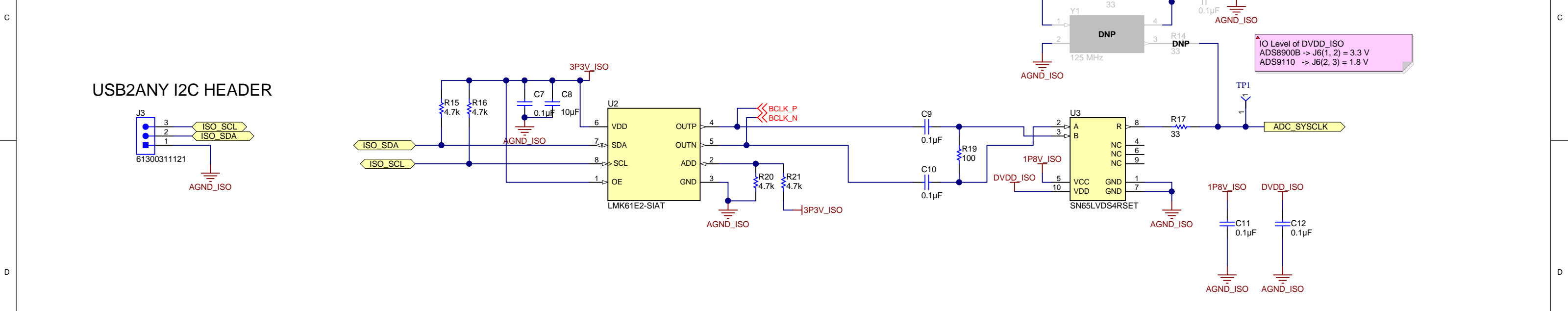
Copyright©2016, Texas Instruments Incorporated

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

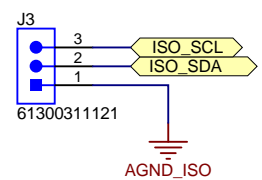
Orderable: N/A	Designed for: Public Release	Mod. Date: 12/7/2016
TID #: 01037	Project Title: 20-bit, 1-MSPS Isolator Optimized Data Acquisition	
Number: TIDA-01037	Rev: E2	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: ADS8900B	Sheet: 1 of 7
Drawn By:	File: TIDA-01037-E2_CoverSheet.SchDoc	Size: B
Engineer: ANBU MANI / Harsh	Contact: http://www.ti.com/support	



PROG MASTER CLOCK GENERATION

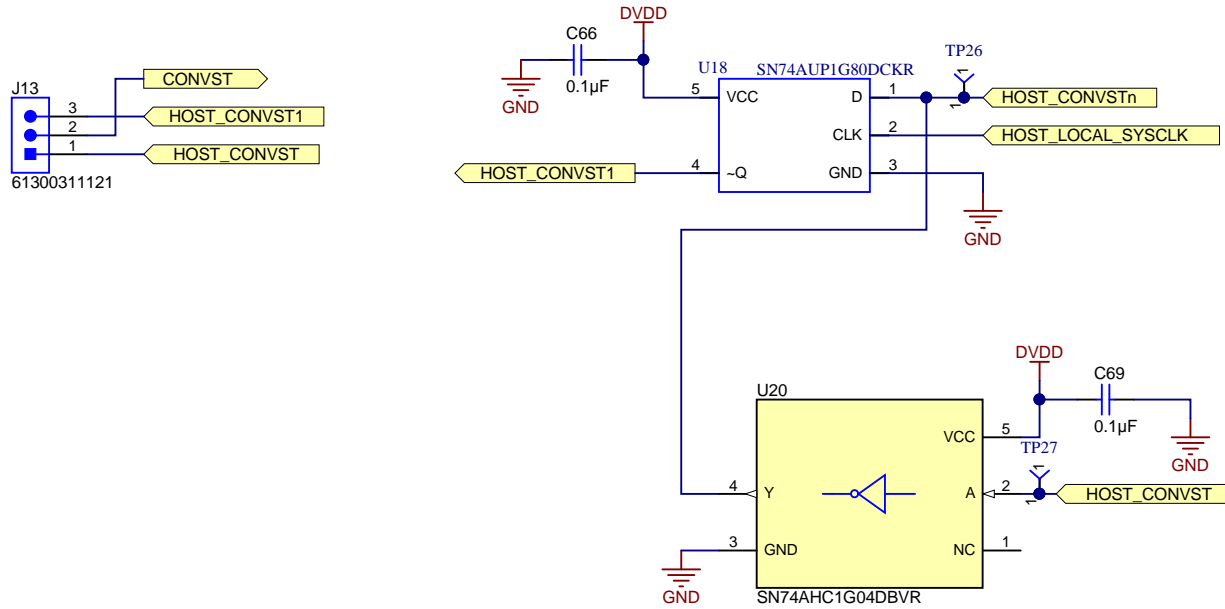
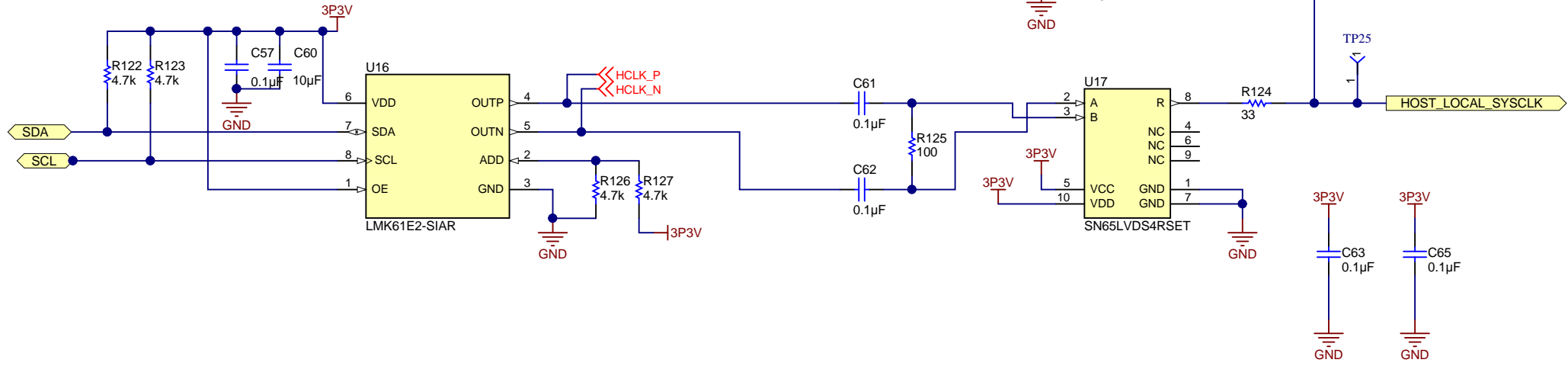
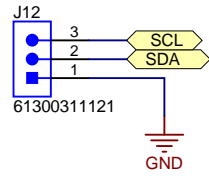


USB2ANY I2C HEADER



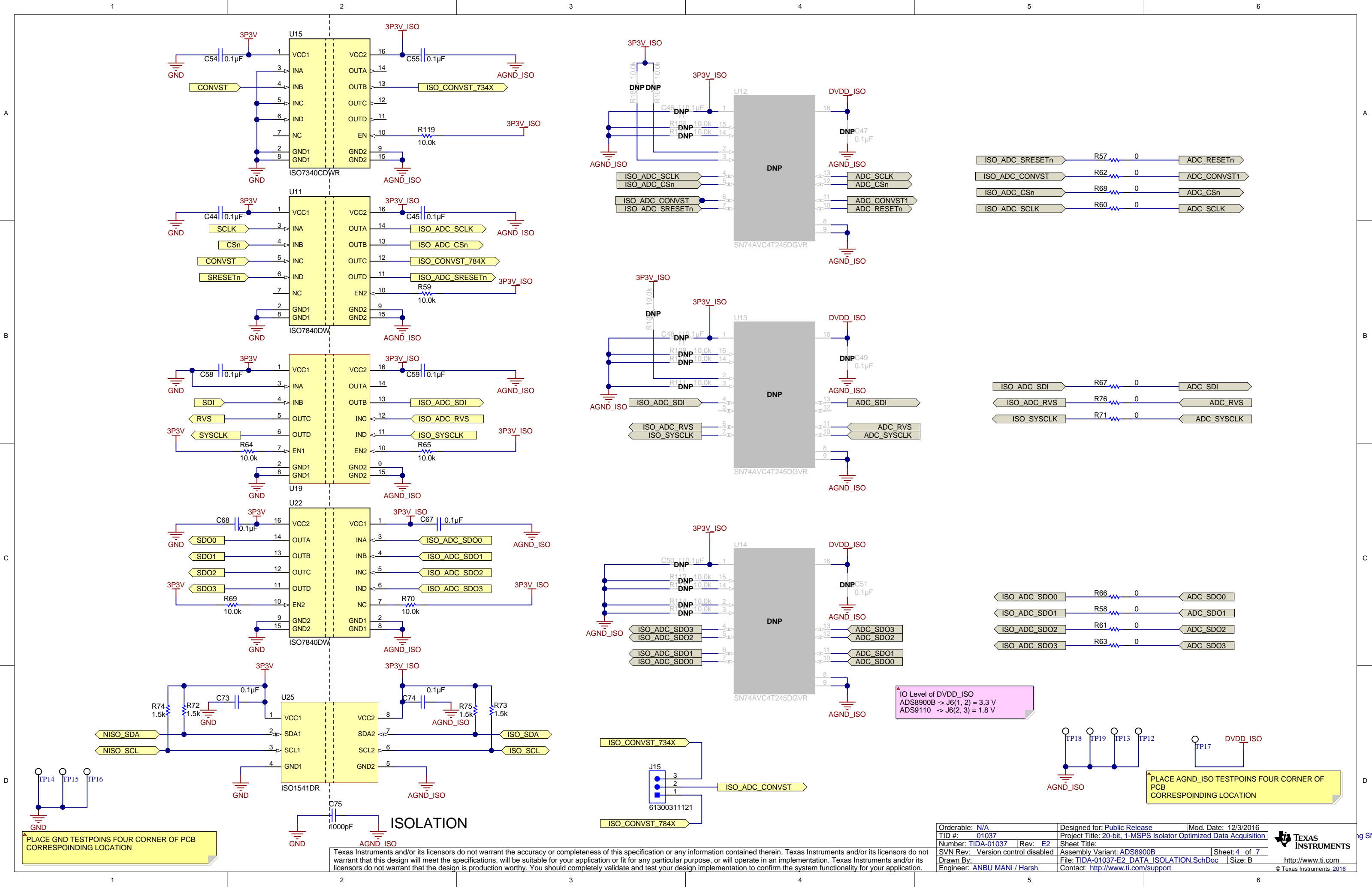
PROG MASTER CLOCK GENERATION

USB2ANY I2C HEADER



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: N/A	Designed for: Public Release	Mod. Date: 12/3/2016
TID #: 01037	Project Title: 20-bit, 1-MSPS Isolator Optimized Data Acquisition	
Number: TIDA-01037	Rev: E2	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: ADS8900B	Sheet: 3 of 7
Drawn By:	File: TIDA-01037-E2_CLKGENERATION.SchDoc	Size: B
Engineer: ANBU MANI / Harsh	Contact: http://www.ti.com/support	



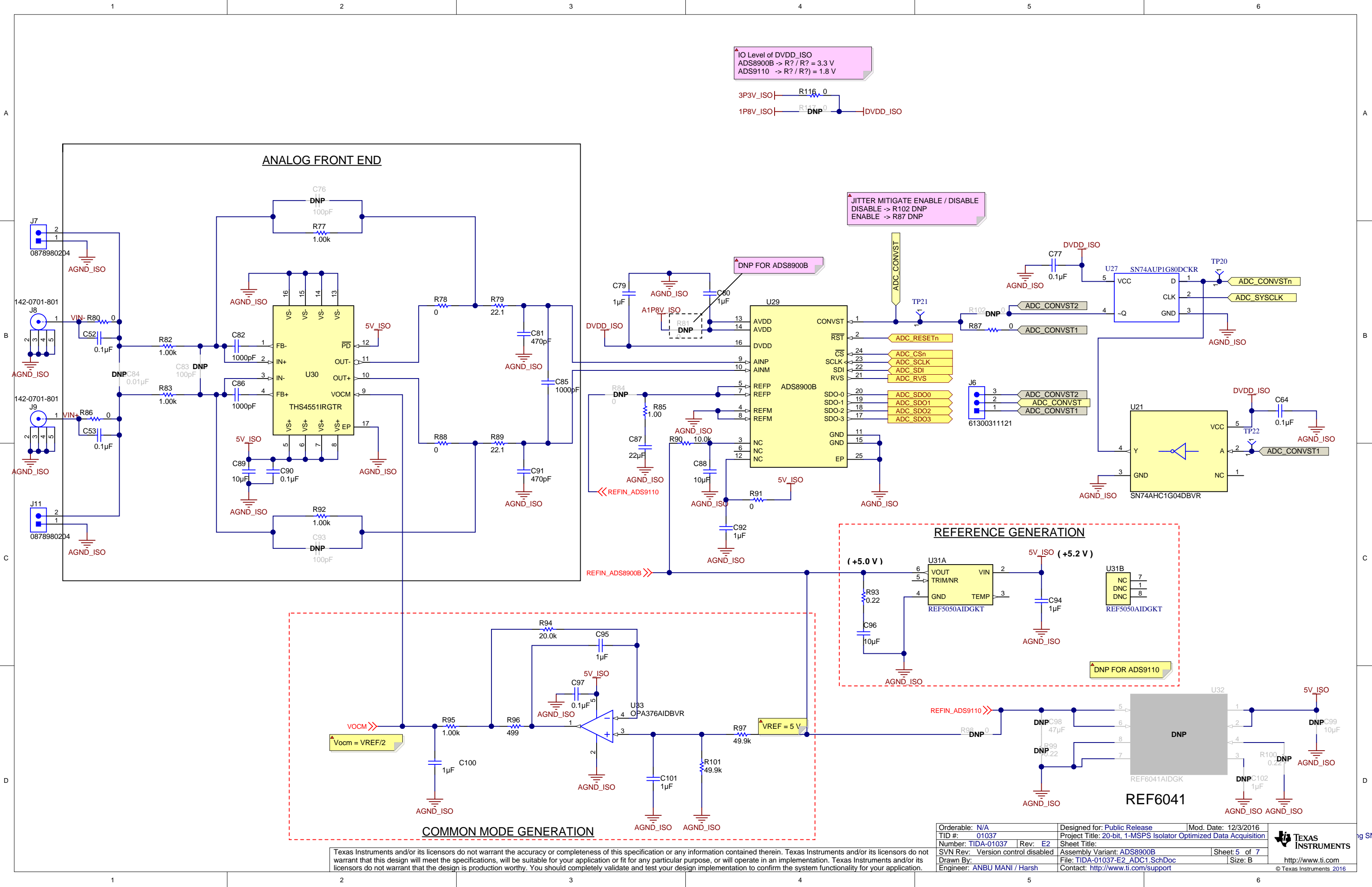
PLACE GND TESTPOINTS FOUR CORNER OF PCB CORRESPONDING LOCATION

PLACE AGND_ISO TESTPOINTS FOUR CORNER OF PCB CORRESPONDING LOCATION

IO Level of DVDD_ISO
ADS8900B -> J6(1, 2) = 3.3 V
ADS9110 -> J6(2, 3) = 1.8 V

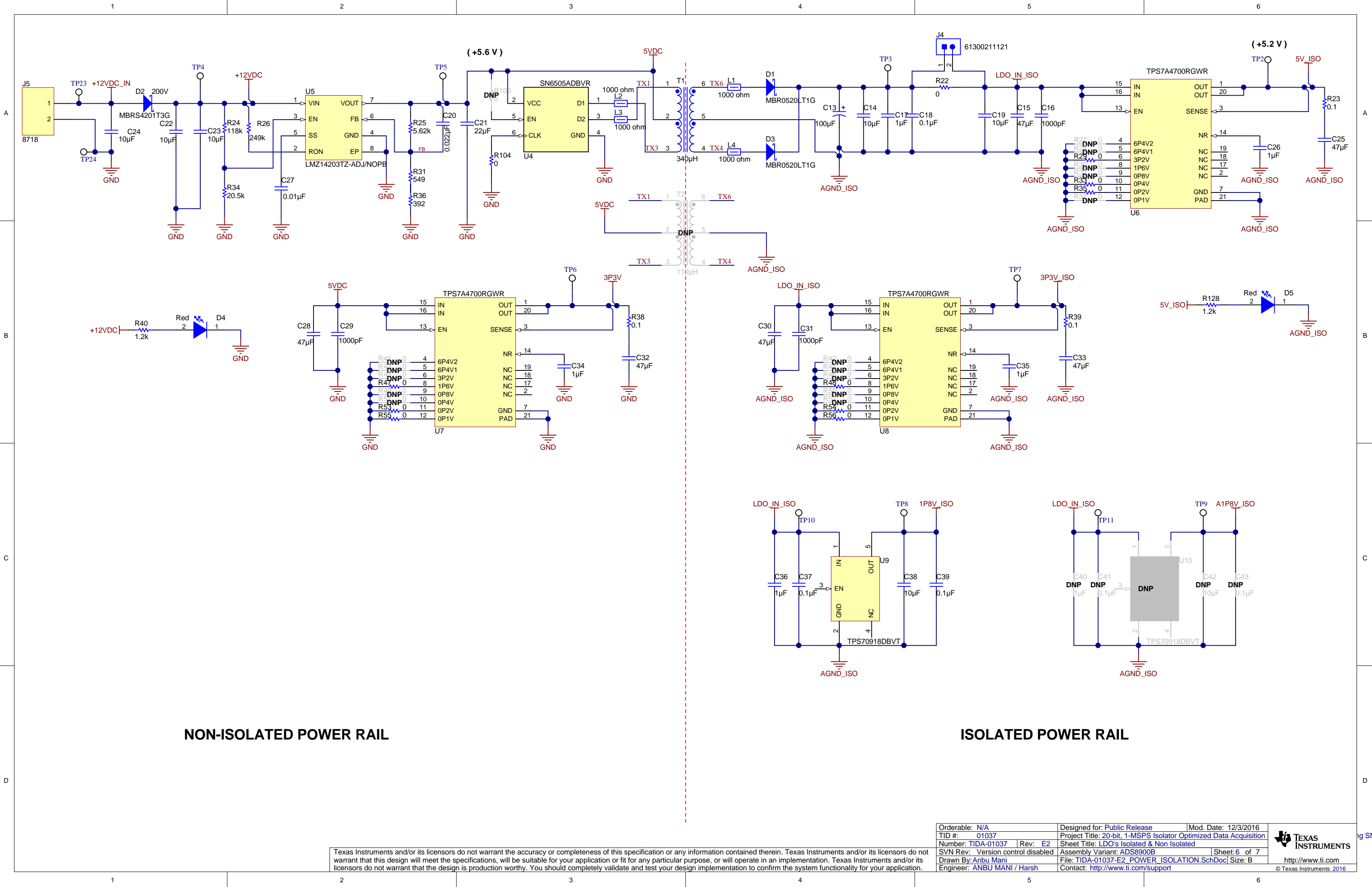
Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: N/A	Designed for: Public Release	Mod. Date: 12/3/2016
TID #: 01037	Project Title: 20-bit, 1-MSPS Isolator Optimized Data Acquisition	
Number: TIDA-01037	Rev: E2	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: ADS8900B	Sheet: 4 of 7
Drawn By:	File: TIDA-01037-E2_DATA_ISOLATION.SchDoc	Size: B
Engineer: ANBU MANI / Harsh	Contact: http://www.ti.com/support	© Texas Instruments 2016



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: N/A	Designed for: Public Release	Mod. Date: 12/3/2016
TID #: 01037	Project Title: 20-bit, 1-MSPS Isolator Optimized Data Acquisition	
Number: TIDA-01037	Rev: E2	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: ADS8900B	Sheet: 5 of 7
Drawn By:	File: TIDA-01037-E2_ADC1.SchDoc	Size: B
Engineer: ANBU MANI / Harsh	Contact: http://www.ti.com/support	



NON-ISOLATED POWER RAIL

ISOLATED POWER RAIL

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: N/A	Designed for: Public Release	Mod. Date: 12/3/2016
TID #: 01037	Project Title: 20-bit, 1-MSPS Isolator Optimized Data Acquisition	
Number: TIDA-01037	Rev: E2	Sheet Title: LDO's Isolated & Non Isolated
SVN Rev: Version control disabled	Assembly Variant: ADS8900B	Sheet: 6 of 7
Drawn By: Anbu Mani	File: TIDA-01037-E2_POWER_ISOLATION.SchDoc	Size: B
Engineer: ANBU MANI / Harsh	Contact: http://www.ti.com/support	

