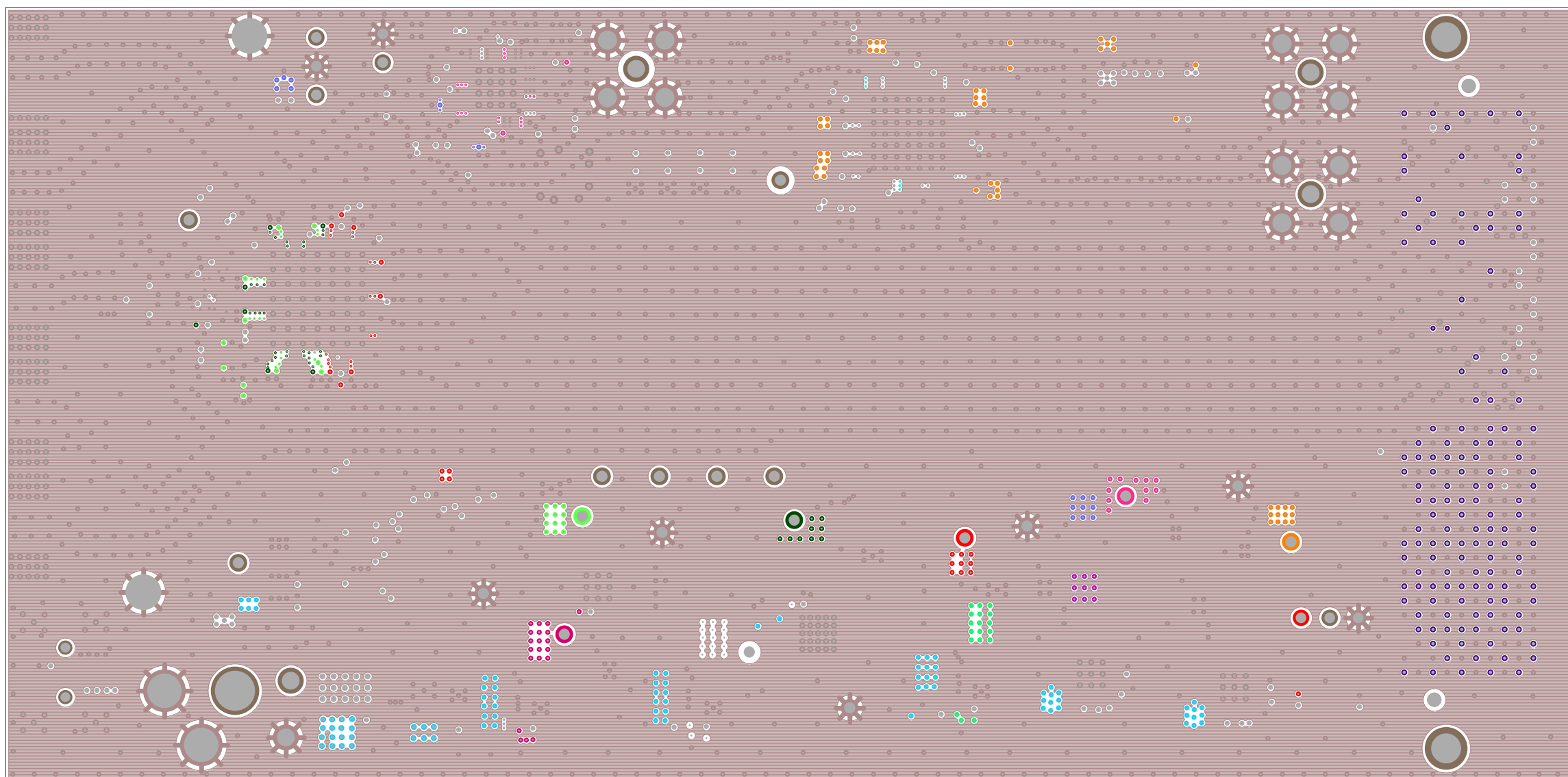
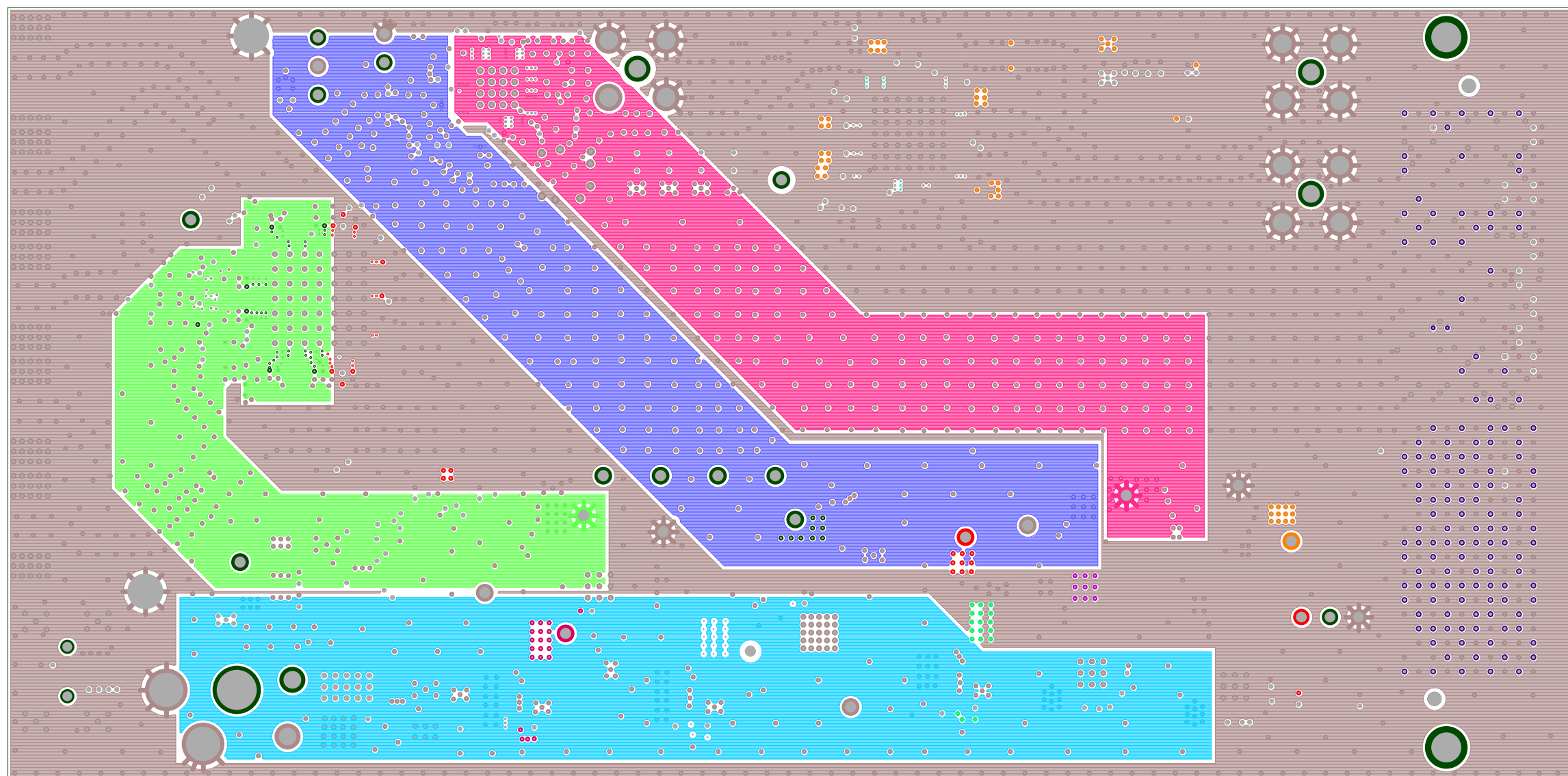


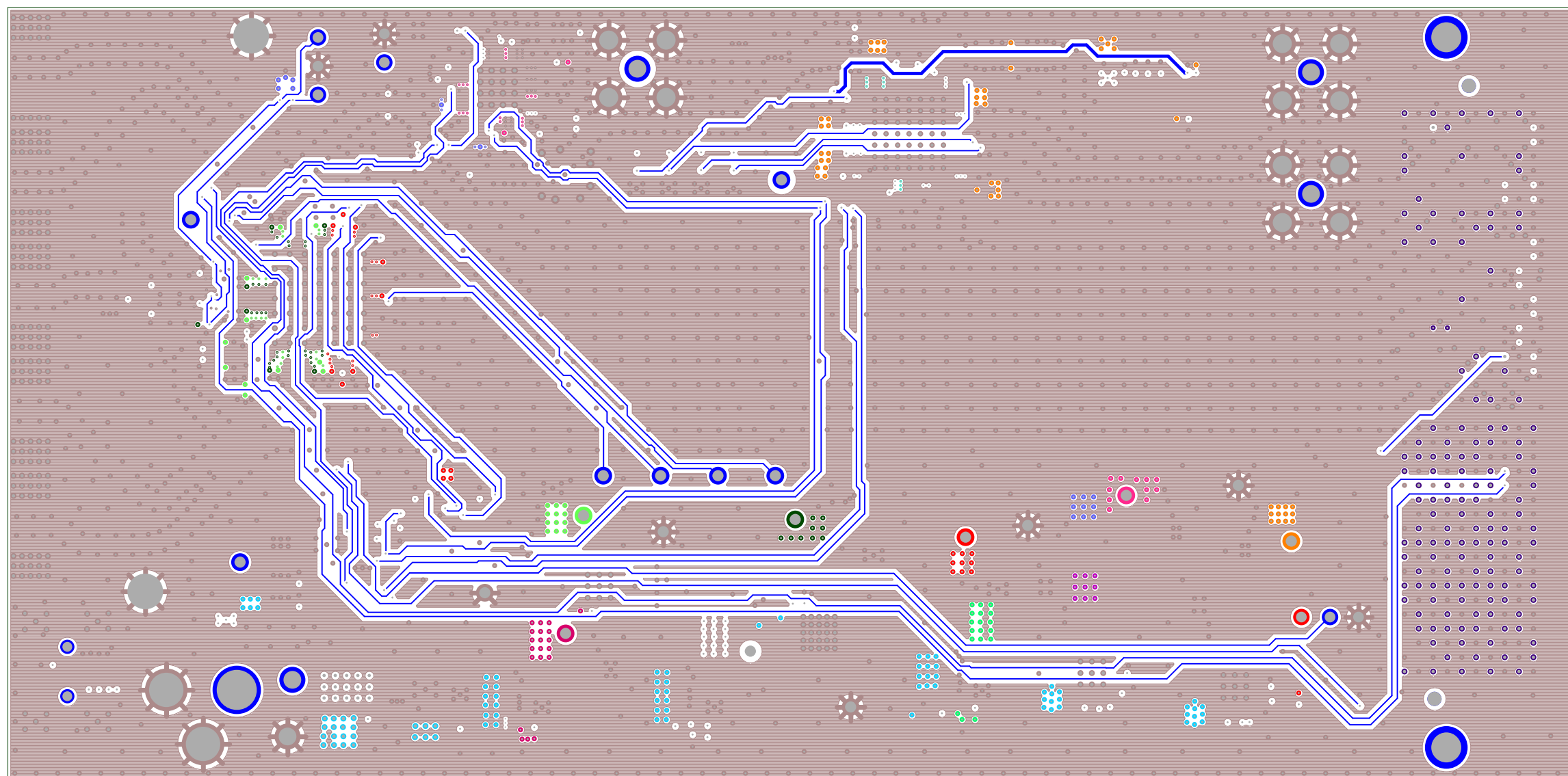
TEXAS INSTRUMENTS, INC.
ADC32RFXEVM
PCB REV D
SILEKSCREENSTOP



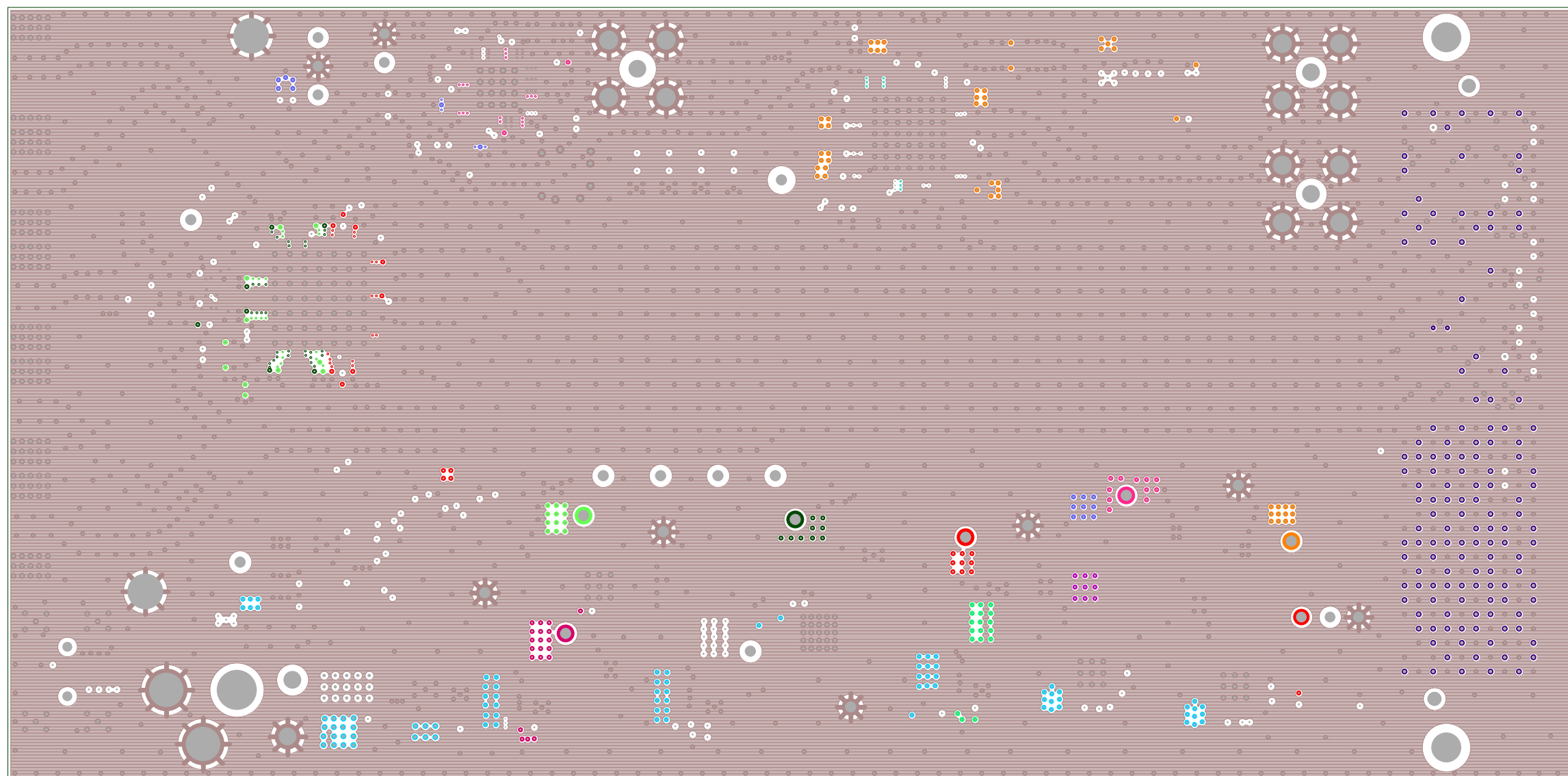
TEXAS INSTRUMENTS, INC.
ADC32RFXEVM
PCB REV D
LAYER 2 - GND



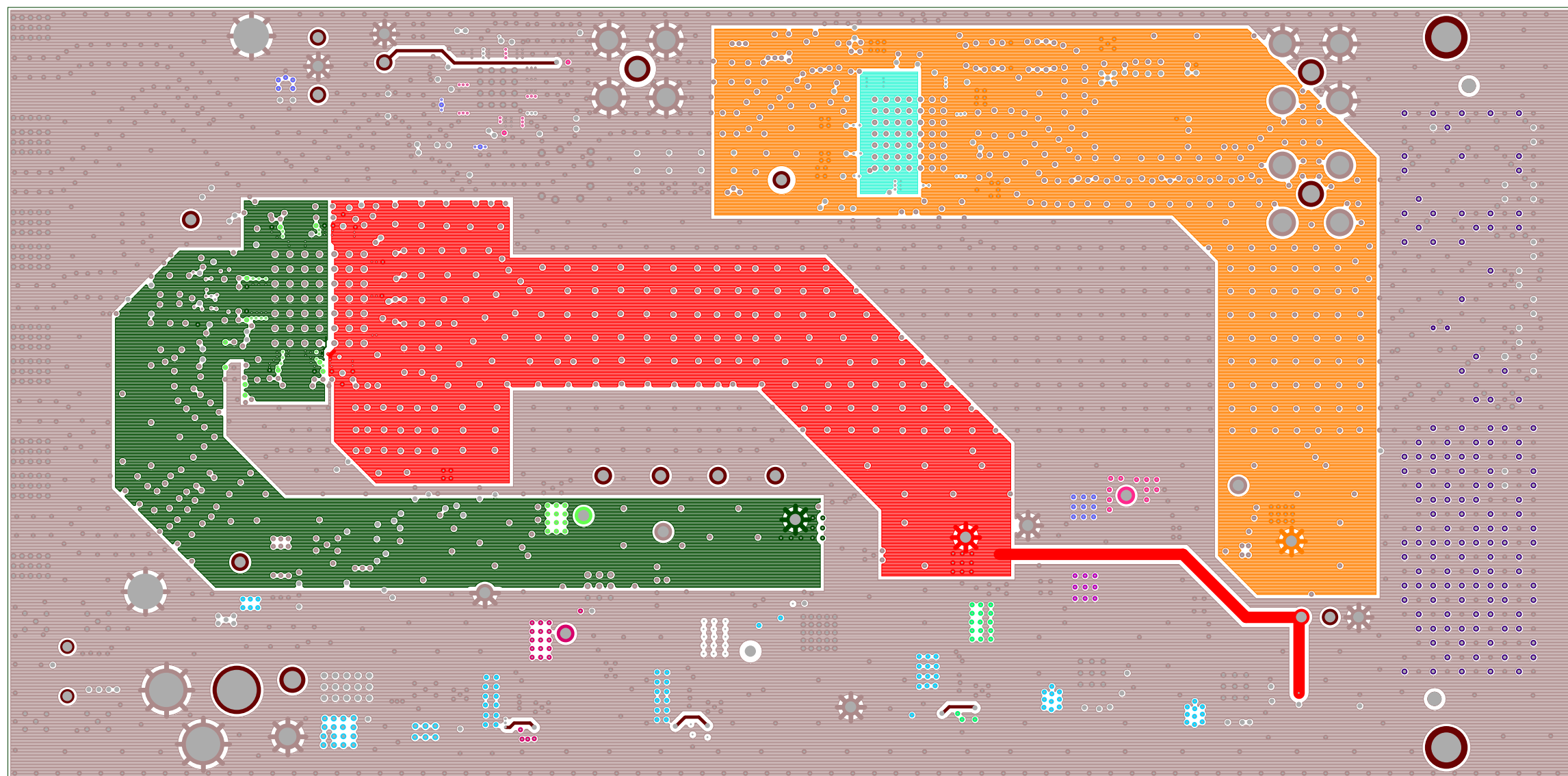
TEXAS INSTRUMENTS, INC.
ADC32RFXEVM
PCB REV D
LAYER 3 - POWER



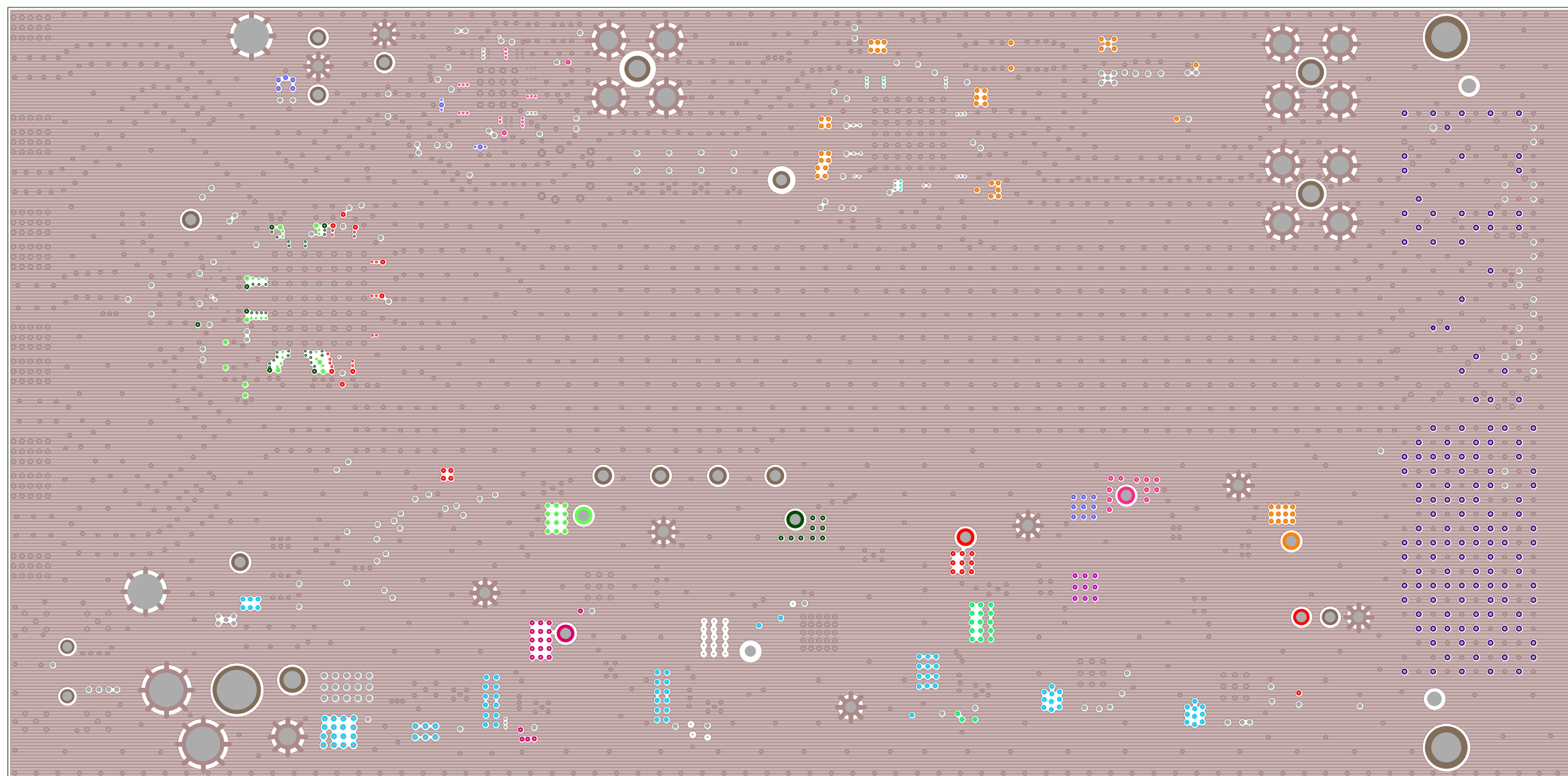
TEXAS INSTRUMENTS, INC.
ADC32RFXEVM
PCB REV D
LAYER 4 - GND



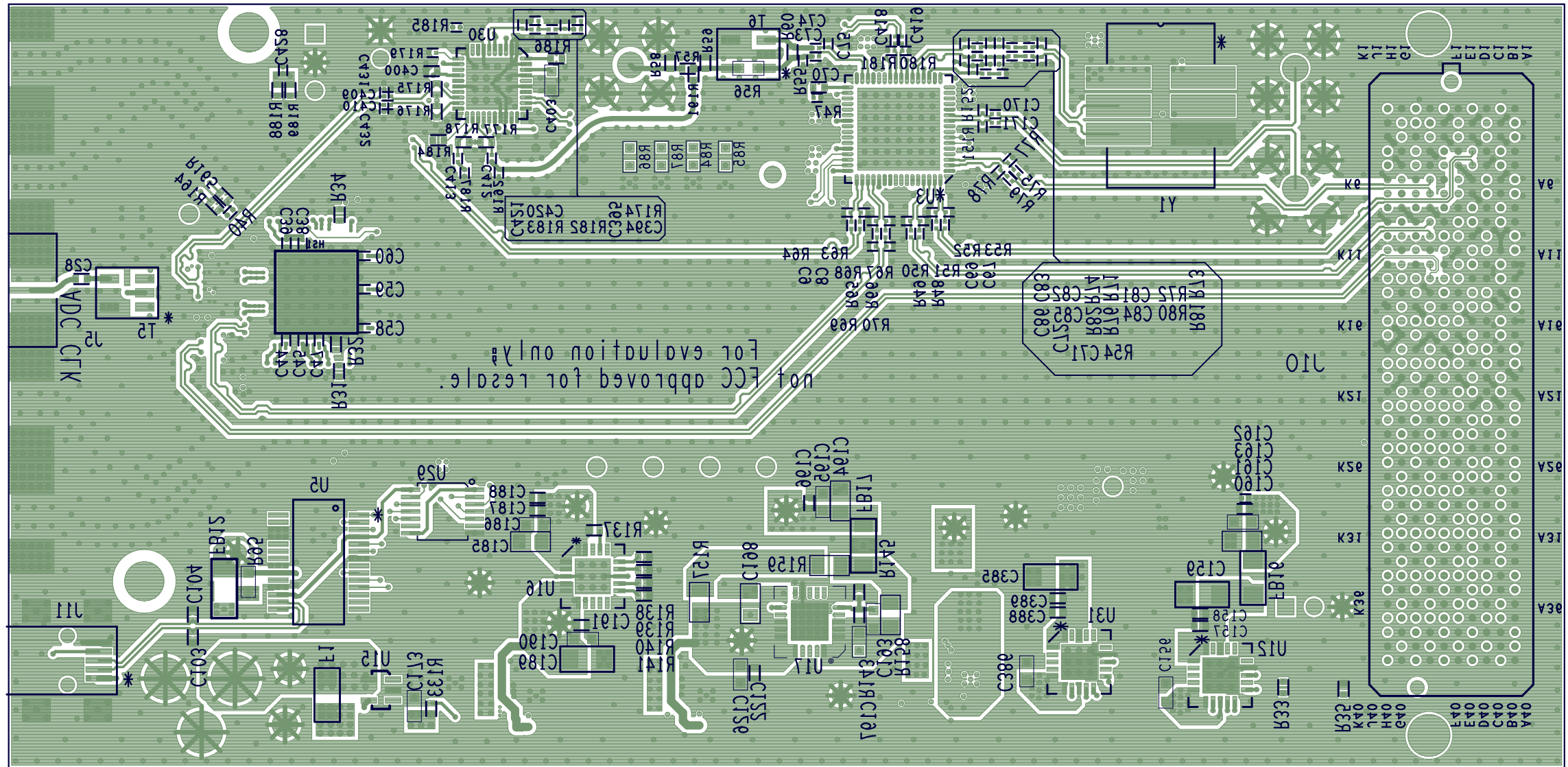
TEXAS INSTRUMENTS, INC.
ADC32RFXEVM
PCB REV D



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ADC32RFXEVM
PCB REV D
LAYER 6 - POWER/SIGNAL



TEXAS INSTRUMENTS, INC.
ADC32RFXEVM
PCB REV D
LAYER 7 - GROUND



TEXAS INSTRUMENTS, INC.
ADC32RFXEVM
PCB REV D
LAYER SCREEN BOTTOM

8 7 6 5 4 3 2 1

UNLESS OTHERWISE SPECIFIED. ALL NOTES ARE APPLICABLE.

1. APPLICATION DESIGN, MANUFACTURING AND INSPECTION DOCUMENTS.
IPC-2221A & IPC-2222 / DESIGN STANDARD FOR RIGID PRINTED CIRCUIT BOARDS AND RIGID PRINTED BOARD ASSEMBLIES.
IPC-6012B / QUALIFICATION AND PERFORMANCE SPECIFICATION FOR RIGID PRINTED BOARD, CLASS 2, CURRENT REVISION.
IPC-A-600G / ACCEPTABILITY OF PRINTED BOARDS, CLASS 2, CURRENT REVISION.

2. VIA 8 & 10MIL SIZES APPLY AFTER PLATING. TOLERANCE TO BE +.003/- .010.
HOLE SIZE APPLY AFTER PLATING. TOLERANCE TO BE +/- .003.

3. REGISTRATION TOLERANCE: ARTWORK +/- .002.
ALL HOLE CENTERS +/- .005 FROM DIMENSION DATUM.

4. MINIMUM COPPER WALL THICKNESS SHALL BE .001 INCH.
FOR ALL PLATED THROUGH HOLES. BREAKOUT NOT ALLOWED.

5. PROCESS AND MATERIAL MUST CONFORM TO UL 796. MATERIAL MUST MEET OR EXCEED UL FLAMMABILITY RATING 94V-0.
MATERIAL: MULTI-LAYER (SEE DETAIL 'A')
SEE LAYER STACKUP FOR ALL PRE-PREG & CORE THICKNESSES, COPPER OZ AND MATERIAL. FINISHED BOARD THICKNESS: .060 +/- 10%

6. MANUFACTURE'S UL MARKING, FLAMMABILITY RATING, LOGO AND DATE CODE TO BE PLACED IN SILKSCREEN ON BOTTOM SIDE OF THE BOARD.

7. SMOBC/IMMERSION GOLD: 2 - 8 uIN OVER 118-236 uIN NICKEL PLATING.

8. SOLDERMASK BOTH SIDES USING TAIYO (OR EQUIVALENT)
COLOR = RED (0.001 TO 002" THICK OVER METAL).

9. SILKSCREEN BOTH SIDES USING WHITE NPI LEADFREE.
REGISTRATION TOLERANCE TO BE +/- .005.
INK IS NOT ALLOWED ON EXPOSED PLATED AREA.

10. P.C. BOARD TO BE FREE OF DIRT, OIL, FINGER PRINTS, ETC.

11. BOARD WARPAGE: WARP AND TWIST SHALL NOT EXCEED .007 INCH PER INCH MEASURED AT ANY LOCATION OR DIRECTION ON THE BOARD.

12. BOARD MUST BE 100% ELECTRICALLY TESTED TO ENSURE NO SHORTS OR OPEN CIRCUITS AT 20V.

13. ALL OUTER LAYERS USING A 19MIL TRACE WIDTH SHALL BE 50 OHMS SINGLE ENDED +/- 10%.

14. 9MIL TRACE WIDTH AND 6MIL SPACING ON TOP SIDE OF BOARD SHALL BE 100 OHMS DIFFERENTIAL +/- 10%.
8MIL TRACE WIDTH AND 6MIL SPACING ON BOTTOM SIDE OF BOARD SHALL BE 100 OHMS DIFFERENTIAL +/- 10%.

15. MINIMUM COPPER CONDUCTOR WIDTH IS: 4MIL.
MINIMUM COPPER CONDUCTOR SPACING IS: 3.5MIL.

16. ALL INNER LAYER UNCONNECTED PADS SHALL BE REMOVED.

17. PWB MUST BE ROHS COMPLIANT AND SURVIVE LEAD FREE ASSEMBLY.
MAX REFLOW OF 260 DEGREES C (6 PASSES).

18. ALL THROUGH VIAS TO BE FILLED WITH NON-CONDUCTIVE MATERIAL.
FILLED VIAS TO BE PLATED AFTER PLUGGING TO PRESENT FLAT SURFACE TO DEVICE.
NO POTHOLE.

REVISIONS

| ZONE | LTR | DESCRIPTION | DATE | APPROVED |
|------|-----|-------------|------|----------|
|------|-----|-------------|------|----------|

TOP

LAYER 1

LAYER 2

R-5775K

Copper Foil 0.25oz / Plate to 0.5oz min Layer 1

Core 0.008" 0.5oz / 0.5oz Layer 1 & 2

R-1650V

Pre-preg 0.0043 (material)

LAYER 3

LAYER 4

R-1775V

Core 0.012" 0.5oz / 0.5oz Layer 3 & 4

R-1650V

Pre-preg 0.0042 (material)

LAYER 5

LAYER 6

R-1775V

Core 0.012" 0.5oz / 0.5oz Layer 5 & 6

R-1650V

Pre-preg 0.0042 (material)

LAYER 7

LAYER 8

R-1775V

Core 0.008" 0.5oz / 0.5oz Layer 5 & 6

Copper Foil 0.25oz / Plate to 0.5oz min Layer 6

BOTTOM

DRILL CHART: TOP to BOTTOM

ALL UNITS ARE IN MILS

| FIGURE | SIZE | PLATED | QTY |
|--------|-------|------------|------|
| - | 6.0 | PLATED | 169 |
| - | 8.0 | PLATED | 2223 |
| - | 10.0 | PLATED | 49 |
| * | 12.0 | PLATED | 479 |
| * | 13.0 | PLATED | 16 |
| * | 15.0 | PLATED | 8 |
| * | 38.0 | PLATED | 8 |
| * | 40.0 | PLATED | 19 |
| o | 62.0 | PLATED | 5 |
| o | 67.0 | PLATED | 12 |
| o | 106.0 | PLATED | 2 |
| o | 120.0 | PLATED | 2 |
| Y | 125.0 | PLATED | 2 |
| o | 140.0 | PLATED | 1 |
| * | 35.0 | NON-PLATED | 2 |
| v | 50.0 | NON-PLATED | 2 |

5.500

2.700

TEXAS INSTRUMENTS, INC.
ADC32RFxxEVM
PCB REV D

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ARE:
FRACTIONS DECIMALS ANGLES
+/- .XX +/- .01 +/-
+/- .XXX +/- .005 +/-

CONTRACT NO.

APPROVALS

DATE

DRAWN L. NGUYEN 10-18-15

ENG R. PRENTICE 10-18-15

MATERIAL

SEE NOTE 5

FINISH

SEE NOTE 7, 8, 9

DO NOT SCALE DRAWING

TEXAS INSTRUMENTS INC.

FABRICATION DRAWING

ADC32RFxxEVM

SIZE CODE IDENT NO. DRAWING NO. REV.

D

SCALE NONE

SHEET 1 OF 1

8 7 6 5 4 3 2 1