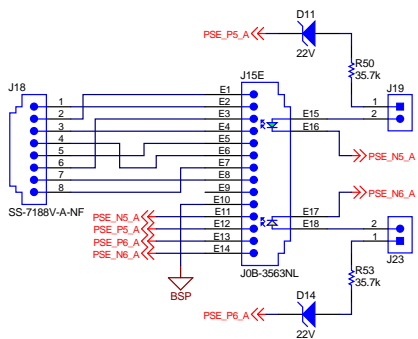
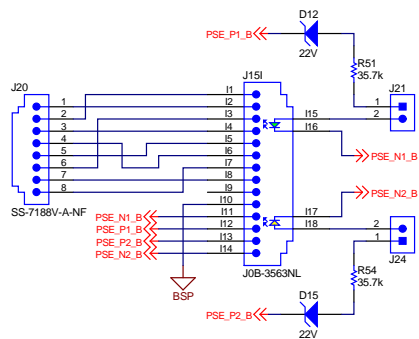


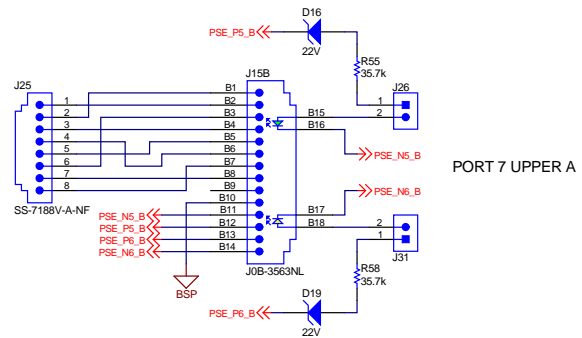
PORT 1 LOWER A



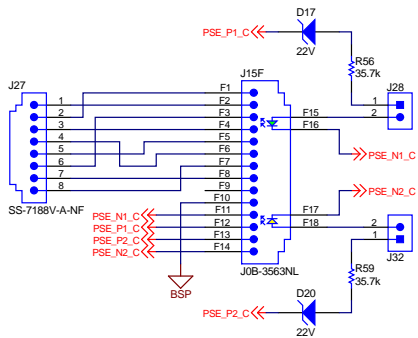
PORT 3 LOWER A



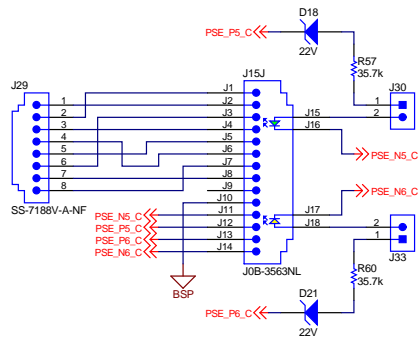
PORT 5 LOWER A



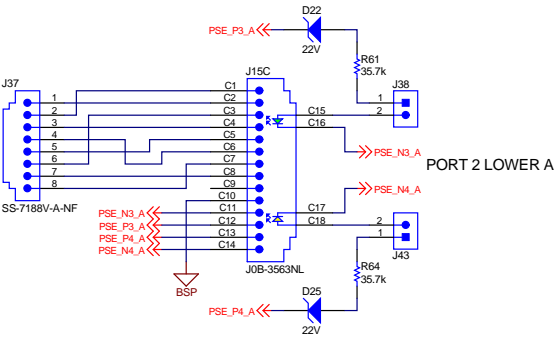
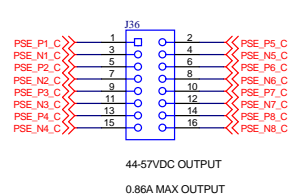
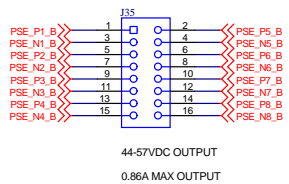
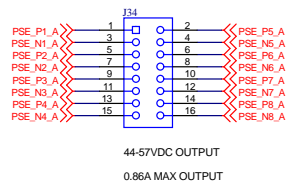
PORT 7 UPPER A



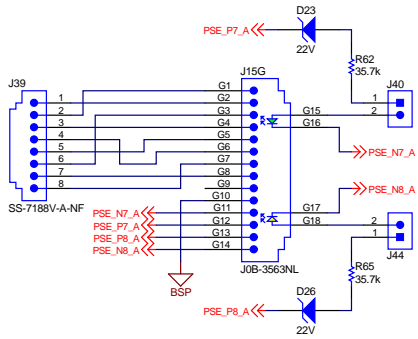
PORT 9 UPPER A



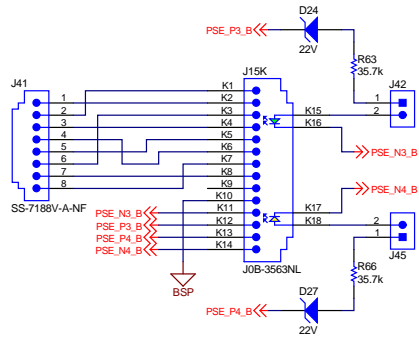
PORT 11 UPPER A



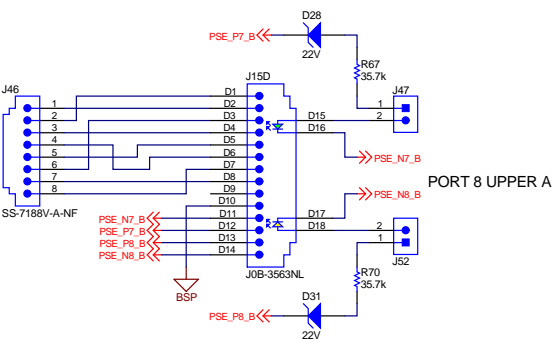
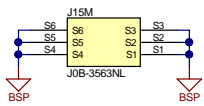
PORT 2 LOWER A



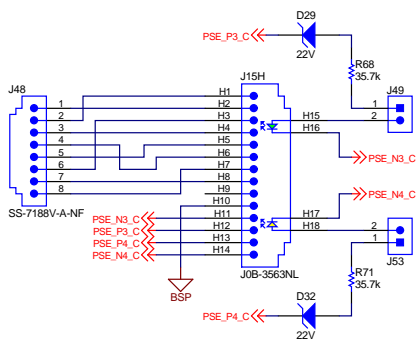
PORT 4 LOWER A



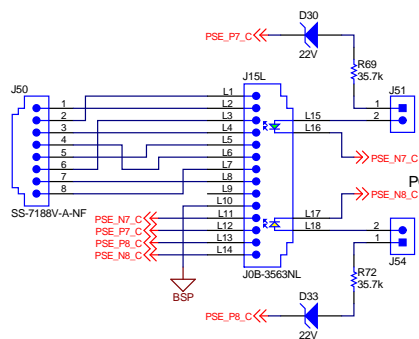
PORT 6 LOWER A



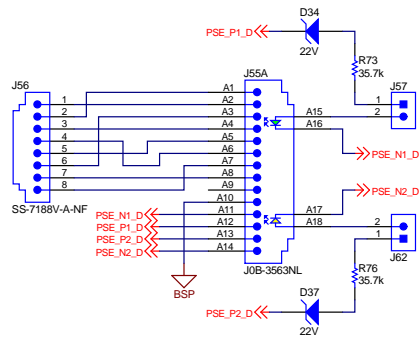
PORT 8 UPPER A



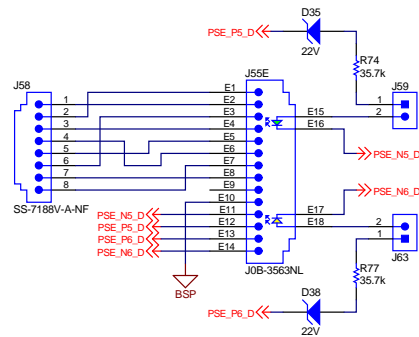
PORT 10 UPPER A



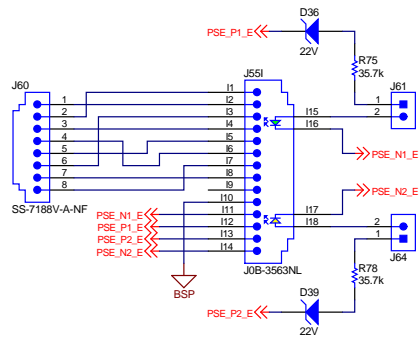
PORT 12 UPPER A



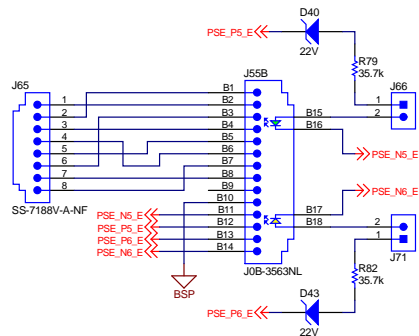
PORT 13 LOWER B



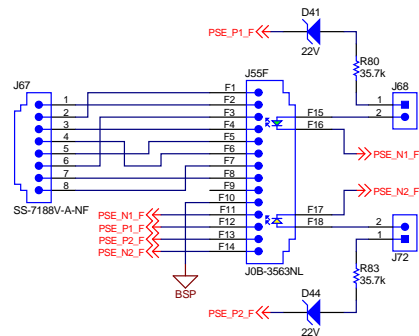
PORT 15 LOWER B



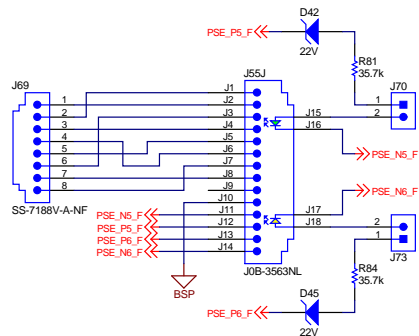
PORT 17 LOWER B



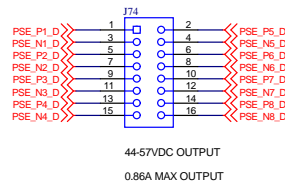
PORT 19 UPPER B



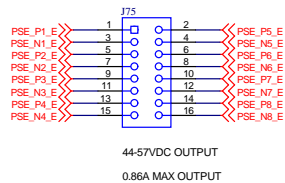
PORT 21 UPPER B



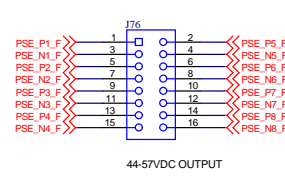
PORT 23 UPPER B



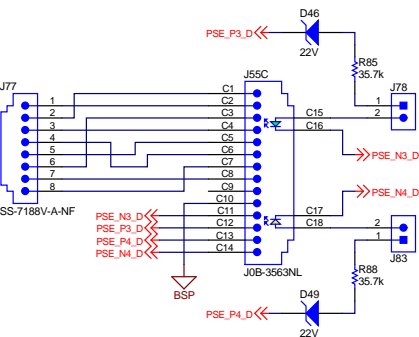
44-57VDC OUTPUT
0.86A MAX OUTPUT



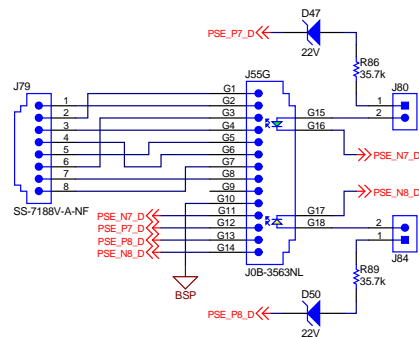
44-57VDC OUTPUT
0.86A MAX OUTPUT



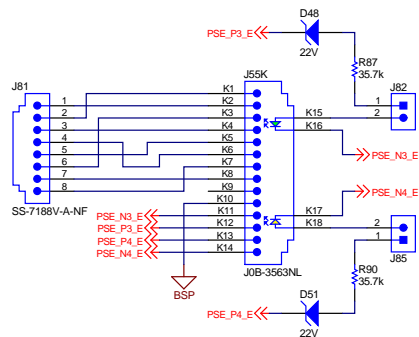
44-57VDC OUTPUT
0.86A MAX OUTPUT



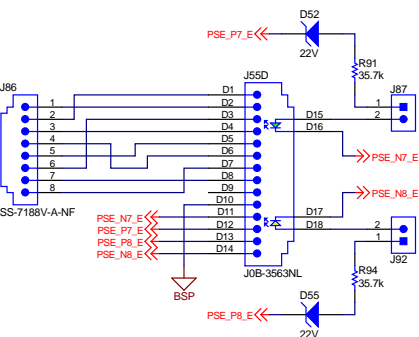
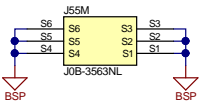
PORT 14 LOWER B



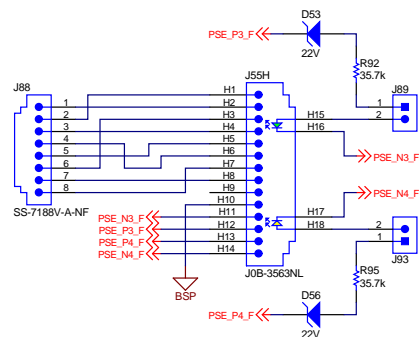
PORT 16 LOWER B



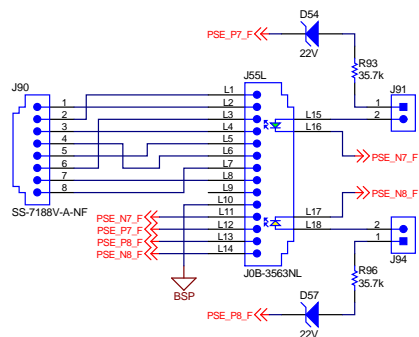
PORT 18 LOWER B



PORT 20 UPPER B



PORT 22 UPPER B



PORT 24 UPPER B

