

## Specification for PCB

Texas Instruments  
Model: ZLLRC  
FCC ID: ZATZLLRC  
IC: 451H-ZLLRC  
Rev 1.1

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Prepared by:  
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Date - 2013-04-03

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### **Type of PCB:**

Number of layers - 2

Dimension:

Board size – 35.58 mm x 80.30 mm

### **Pattern Information:**

Min. conductor width – 0.2mm

Min. isolation dist. – 0.15mm

### **Hole Information:**

Min. hole diam. – 0.3mm

Number of drill holes – 777 pcs/pcb

### **Tolerance:**

Holes tolerance - +/- 0.1mm

Contour - +/-0.2mm

### **Soldermask:**

Photo polymer liquid film

Colour of Solder Mask - Green

### **Component notation**

On primary side - White

On secondary side - White

**NB! No component notation on pads.**

### **Material:**

FR4

### **Stack up:**

	Subclass Name	Type	Material	Thickness (MM)	Conductivity (mho/cm)	Dielectric Constant	Loss Tangent
1		SURFACE	AIR			1	0
2	TOP	CONDUCTOR	COPPER	0.035	595900	4.5	0
3		DIELECTRIC	FR-4	0.766	0	4.5	0.035
4	BOTTOM	CONDUCTOR	COPPER	0.035	595900	4.5	0
5		SURFACE	AIR			1	0

### **Additional information:**

**There are four rectangle holes in the board with dimension 2.3mm x 1.75mm**

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## **DOCUMENT SURVEY**

### **Content of files: ZLLRC.ZIP**

MOUNT_ZLLRC	Manufacturing files	Folder
PCB_ZLLRC	Manufacturing files	Folder
ZLLRC.pdf	Film files	Acrobat file
PCBSPEC_ZLLRC.pdf	PCB Specification	Acrobat file

### **Content of files: PCB\_ZLLRC**

<b>FILE NAME</b>	<b>DESCRIPTION</b>	<b>FILE TYPE</b>
DRM_ZLLRC.ART	Drill and Dimension Drawing	Gerber
PA1_ZLLRC.ART	Pattern Layer 1 "TOP"	Gerber
PA2_ZLLRC.ART	Pattern Layer 2 "BOTTOM"	Gerber
PL1_ZLLRC.ART	Protective Lacquering "TOP"	Gerber
PL2_ZLLRC.ART	Protective Lacquering "BOTTOM"	Gerber
SP2_ZLLRC.ART	Solder Paste "BOTTOM" (SCALE 0.9:1)	Gerber
SS1_ZLLRC.ART	Silk Screen "TOP"	Gerber
SS2_ZLLRC.ART	Silk Screen "BOTTOM"	Gerber
ZLLRC.rou	NC Route Coordinates	NC ROUTE
ZLLRC_ncroute.txt	NC Route Report	txt-file
ZLLRC.drl	NC Drill Coordinates	NC DRILL
ZLLRC_nctools.txt	NC Tool File	txt-file
ZLLRC_ncdrill.txt	NC Drill Report	txt-file
ZLLRC_netlist.txt	Net List	txt-file
ZLLRC_dangling.txt	Dangling Lines Report	txt-file
ZLLRC_drc.txt	Design Rules Check Report	txt-file
ZLLRC_waiveddrc.txt	Waived Design Rules Check Report	txt-file
ZLLRC_sum.txt	Summary Drawing Report	txt-file
ZLLRC_unconpin.txt	Unconnected Pins Report	txt-file

### **Content of files: MOUNT\_ZLLRC**

<b>FILE NAME</b>	<b>DESCRIPTION</b>	<b>FILE TYPE</b>
DRM_ZLLRC.ART	Drill and Dimension Drawing	Gerber
AD1_ZLLRC.ART	Assembly Drawing "TOP"	Gerber
AD2_ZLLRC.ART	Assembly Drawing "BOTTOM"	Gerber
ZLLRC_PICKANDPLACE.xlsx	Mounting/Assembly Data	Excel – file
ZLLRC_netlist.txt	Net List	txt-file
ZLLRC_dangling.txt	Dangling Lines Report	txt-file
ZLLRC_drc.txt	Design Rules Check Report	txt-file
ZLLRC_sum.txt	Summary Drawing Report	txt-file
ZLLRC_unconpin.txt	Unconnected Pins Report	txt-file

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