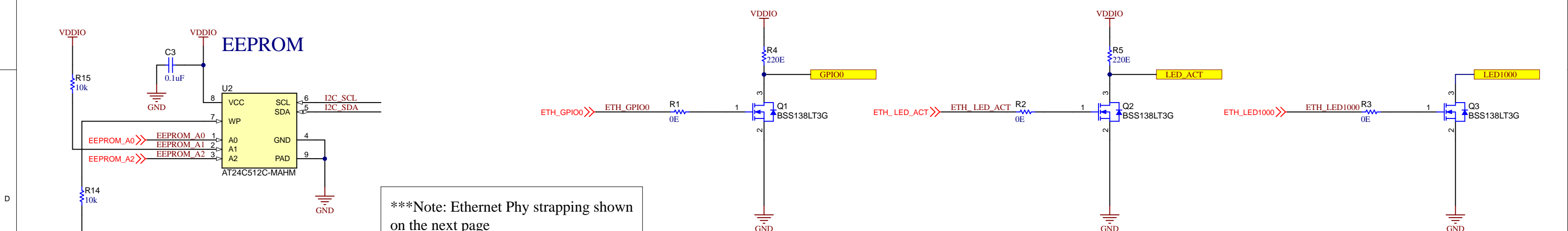



Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A



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Orderable: DP3867-EVM-AM	Designed for: Public Release	Mod. Date: 5/8/2024	 TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments 2024
TID #: N/A	Project Title: DP3867-EVM-AM		
Number: PROC188	Rev: E1	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 3	
Drawn By:	File: DP3867-EVM-AM2.SchDoc	Size: B	
Engineer: Darius Williams	Contact: http://www.ti.com/support		

A

A

B

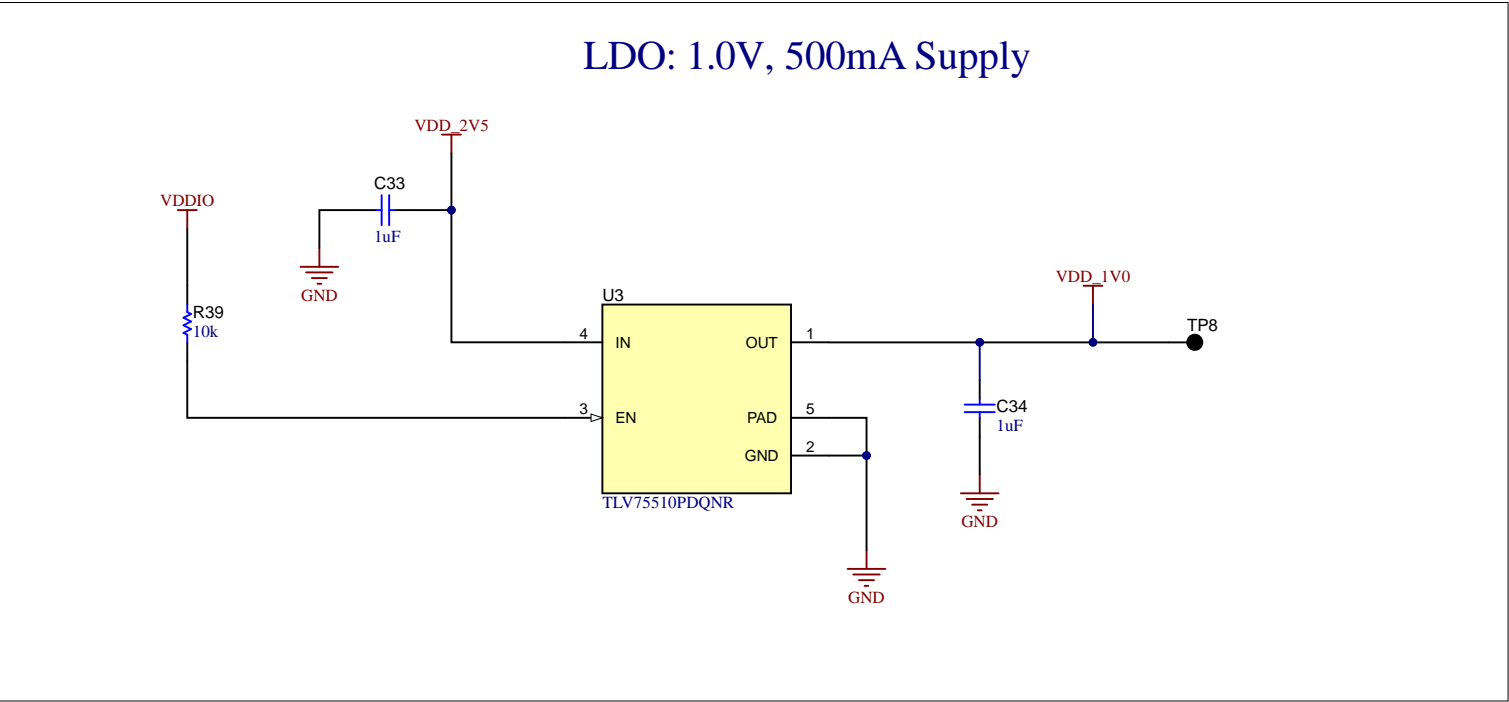
B

C

C

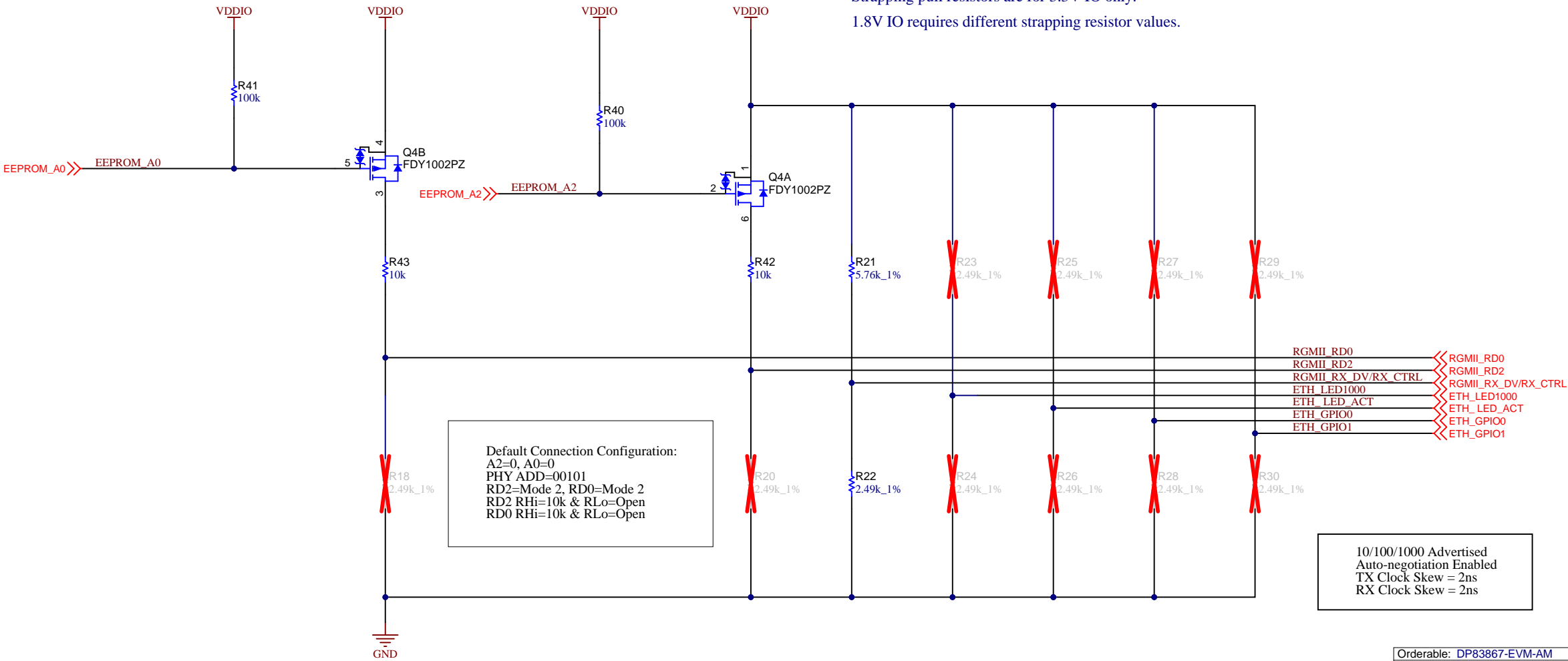
D

D



Strapping Resitors & PHY Address Selection Network

Strapping pull resistors are for 3.3V IO only.
1.8V IO requires different strapping resistor values.



Eth.Exp.Con	I2C EEPROM Addr	Eth PHY Addr
0	0x52	00101
1	0x53	00100
2	0x56	00001
3	0x57	00000

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TID #: N/A	Project Title: DP83867-EVM-AM	
Number: PROC188	Rev: E1	Sheet: 2 of 3
SVN Rev: Not in version control	Assembly Variant: 001	File: DP83867-EVM-AM2_Strapping_LDO.SchDoc
Drawn By:		Size: B
Engineer: Darius Williams	Contact: http://www.ti.com/support	

