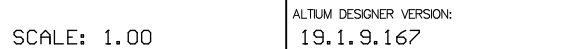


Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.  
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

<input checked="" type="checkbox"/>	OUTER XX MIL TRACES REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE
<input checked="" type="checkbox"/>	LAYER 2 & 3 (INNER LAYERS) XX MIL WIDE, XX MIL SPACE TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC091	REV: G	SUN REV: 1539	Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.
LAYER NAME = 020 Board Dimensions	TID #: N/A			
PLOT NAME = Fabrication Drawing	GENERATED : 02-09-2021 11:16:53		TEXAS INSTRUMENTS	