
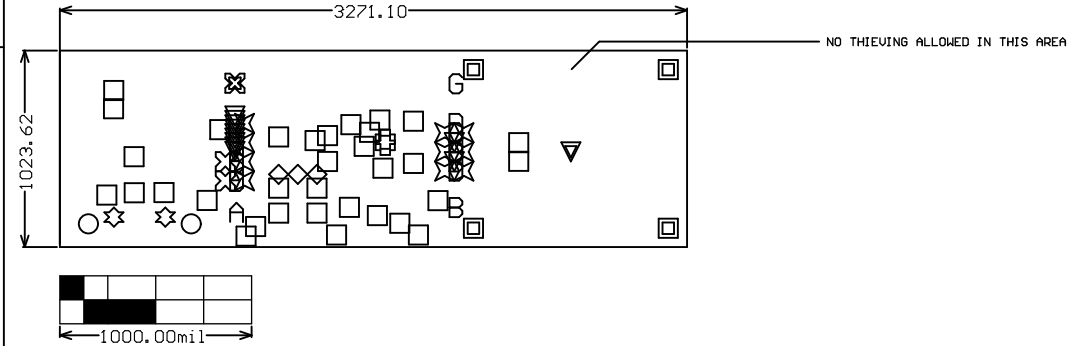


Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0.40mil	3.5	
3	Top Layer	Copper	1.40mil		
4	Dielectric1	FR-4	8.00mil	4.6	
5	Signal Layer 1	Copper	1.40mil		
6	Dielectric 4	FR-4	36.60mil	4.6	
7	Signal Layer 2	Copper	1.40mil		
8	Dielectric 2	FR-4	8.00mil	4.6	
9	Bottom Layer	Copper	1.40mil		
10	Bottom Solder	Solder Resist	0.40mil	3.5	
11	Bottom Overlay				

Symbol	Quantity	Finished Hole Size	Plated	Hole Type
▽	11	14.00mil (0.356mm)	NPTH	Round
D	10	20.00mil (0.508mm)	NPTH	Round
☆	2	35.43mil (0.900mm)	NPTH	Round
■	4	98.00mil (2.489mm)	NPTH	Round
▽	1	197.00mil (5.004mm)	NPTH	Round
A	1	31.00mil (0.787mm)	NPTH	Slot
✕	1	31.00mil (0.787mm)	NPTH	Slot
B	1	31.00mil (0.787mm)	NPTH	Slot
G	1	31.00mil (0.787mm)	NPTH	Slot
⊙	2	7.87mil (0.200mm)	PTH	Round
□	33	8.00mil (0.203mm)	PTH	Round
◇	3	33.46mil (0.850mm)	PTH	Round
⊗	2	39.37mil (1.000mm)	PTH	Round
⊗	10	40.00mil (1.016mm)	PTH	Round
O	2	63.00mil (1.600mm)	PTH	Round
	84 Total			

DESIGN INFORMATION	
MIN. TRACK WIDTH: 6 MIL	
MIN. CLEARANCE: 6 MIL	
MIN. VIA PAD SIZE: 20 MIL	
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL	
PER IPC-D-275 CLASS 2 LEVEL C	
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL	
MATERIAL:	
<input type="checkbox"/> FR-408 <input checked="" type="checkbox"/> FR-4 High Tg <input type="checkbox"/> OTHER	
THICKNESS: <input checked="" type="checkbox"/> 62 MIL (1.6mm) +/-10% <input type="checkbox"/> OTHER	
TOLERANCE: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2	
<input type="checkbox"/> OTHER +/-	
BOW & TWIST: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2	
<input type="checkbox"/> OTHER +/-	
DRILLING:	
REFERENCE: <input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC_DRILL FILES	
PTH MIN COPPER THICKNESS: <input checked="" type="checkbox"/> 1MIL <input type="checkbox"/> OTHER	
BOARD FINISH:	
SILKSCREEN: <input checked="" type="checkbox"/> TOP <input checked="" type="checkbox"/> BOTTOM	
SILKSCREEN COLOR: <input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER	
SOLDER RESIST COLOR:	
<input type="checkbox"/> GREEN <input checked="" type="checkbox"/> BLUE <input type="checkbox"/> OTHER	
SURFACE FINISH: <input checked="" type="checkbox"/> IMMERSION GOLD (ENIG) <input type="checkbox"/> ENEPIG	
<input type="checkbox"/> IMM. TIN/SILVER OR EQUIV <input type="checkbox"/> OTHER	
ARRAY/PANEL:	
<input type="checkbox"/> CUT AND TRIM PER MECH LAYER 1	
<input checked="" type="checkbox"/> N.C. ROUTE <input type="checkbox"/> V. SCORE	
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:	
<input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS -> <input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3	
<input checked="" type="checkbox"/> UL 94V-0 <input checked="" type="checkbox"/> RoHS <input type="checkbox"/> OTHER PER ORDER	
ADDITIONAL REQUIREMENTS:	
MICROSECTION: <input type="checkbox"/> YES	
BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER	
MANUFACTURER'S UL: <input type="checkbox"/> RAIL <input type="checkbox"/> METAL <input checked="" type="checkbox"/> SILK	

	
PROJECT TITLE: LDC0851EUM	
DESIGNED FOR: Public Release	
FILE NAME: SV601198A.PcbDoc	
ENGINEER: Luke LaPointe	LAYOUT BY: Luke LaPointe
SCALE: 1.00	ALTUM DESIGNER VERSION: 14.3.19.51423



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU601198	REV: A	SUN REV: Not In VersionControl
LAYER NAME = 00000-1198A	TID #: .TID		
PLOT NAME = Fabrication Drawing	GENERATED : 11/11/2015 3:43:03 PM	TEXAS INSTRUMENTS	

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