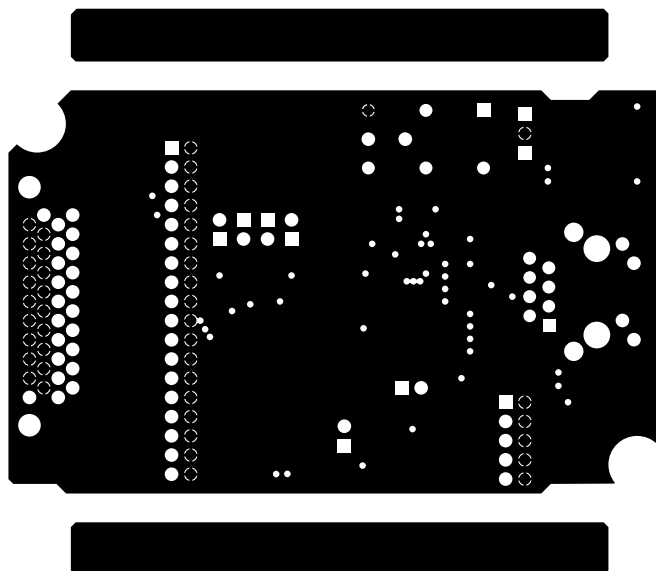

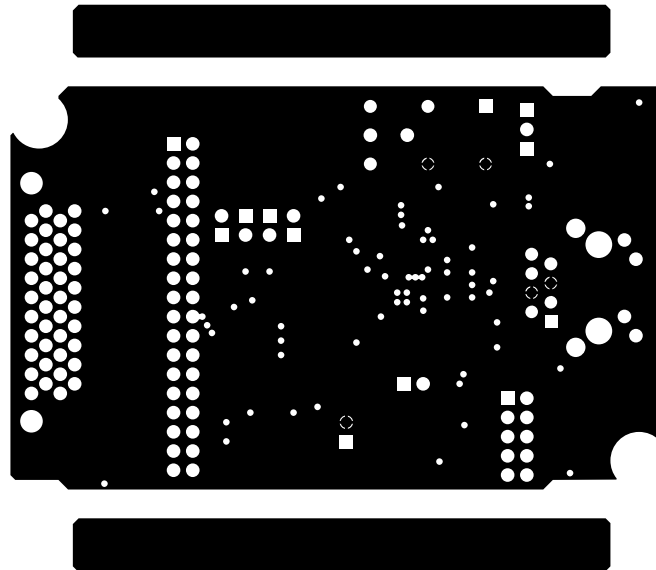



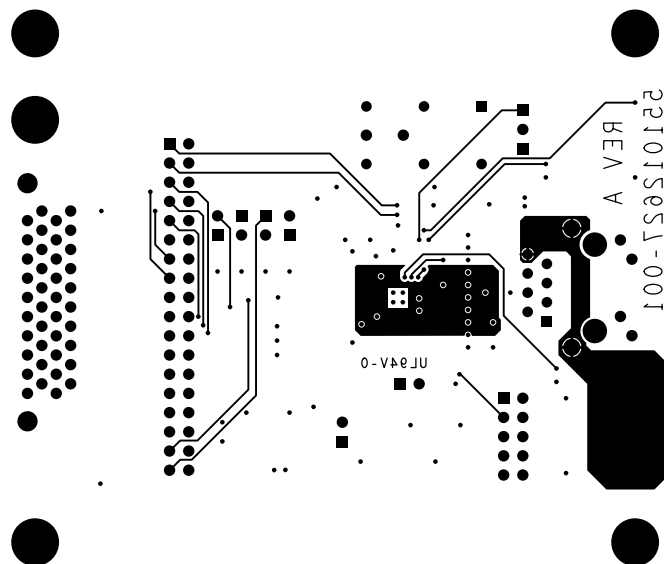
 National Semiconductor™ INTERFACE DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE		DATE: AUGUST 8, 2005	
	BOARD NAME: DP83848 ASPENPHY MAU		BOARD NO: 880012627-001	REV A
	LAYER NAME: TOP ETCH		LAYER NO: 1	SHEET 1 of 8



 National Semiconductor™ INTERFACE DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE		DATE: AUGUST 8, 2005	
	BOARD NAME: DP83848 ASPENPHY MAU		BOARD NO: 880012627-001	REV A
	LAYER NAME: GND		LAYER NO: 2	SHEET 2 of 8



 National Semiconductor™ INTERFACE DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE		DATE: AUGUST 8, 2005	
	BOARD NAME: DP83848 ASPENPHY MAU		BOARD NO: 880012627-001	REV A
	LAYER NAME: 3V3		LAYER NO: 3	SHEET 3 of 8



National Semiconductor™
INTERFACE DIVISION

ALL ARTWORK VIEWED FROM TOP SIDE

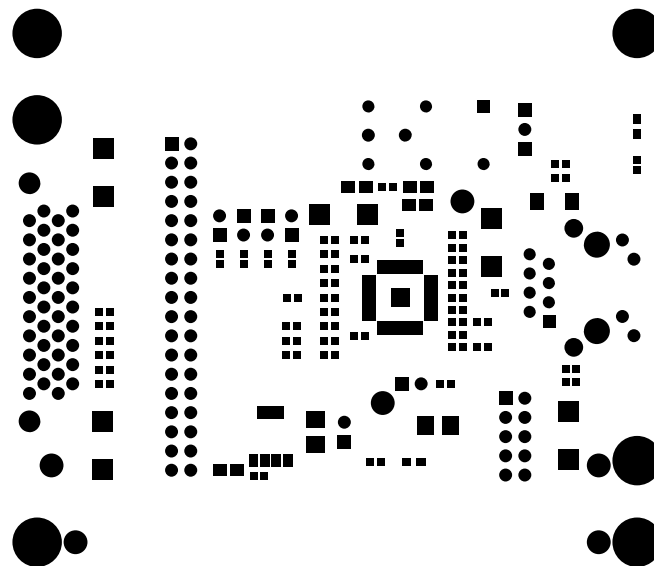
BOARD NAME: DP83848 ASPENPHY MAU


LAYER NAME: BOTTOM ETCH

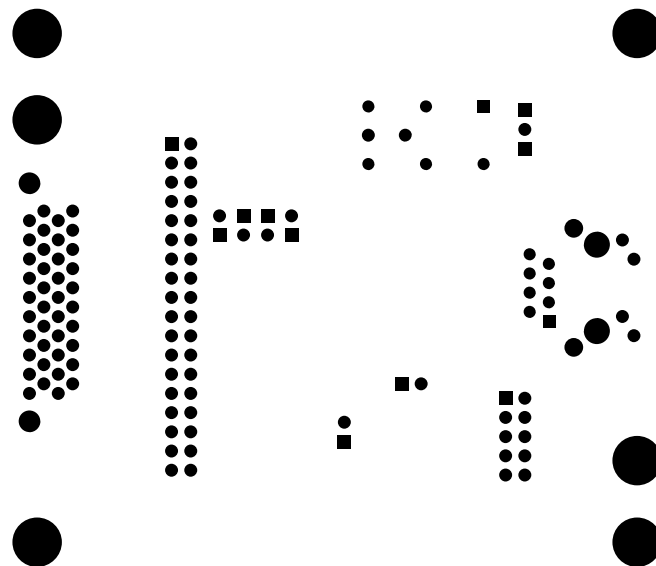
DATE: AUGUST 8, 2005

BOARD NO: 880012627-001 REV A

LAYER NO: 4 SHEET 4 of 8



 National Semiconductor™ INTERFACE DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE		DATE: AUGUST 8, 2005	
	BOARD NAME: DP83848 ASPENPHY MAU		BOARD NO: 880012627-001	REV A
	LAYER NAME: TOP SOLDER MASK		LAYER NO:	SHEET 5 of 8



National Semiconductor™
INTERFACE DIVISION

ALL ARTWORK VIEWED FROM TOP SIDE

DATE: AUGUST 8, 2005

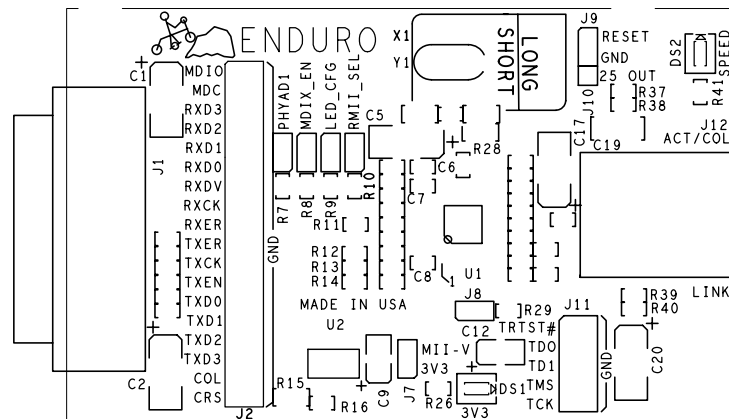
BOARD NAME: DP83848 ASPENPHY MAU

BOARD NO: 880012627-001 REV A

LAYER NAME: BOTTOM SOLDER MASK

LAYER NO:

SHEET 6 of 8



National Semiconductor™
INTERFACE DIVISION

ALL ARTWORK VIEWED FROM TOP SIDE

BOARD NAME: DP83848 ASPENPHY MAU

LAYER NAME: TOP SILKSCREEN

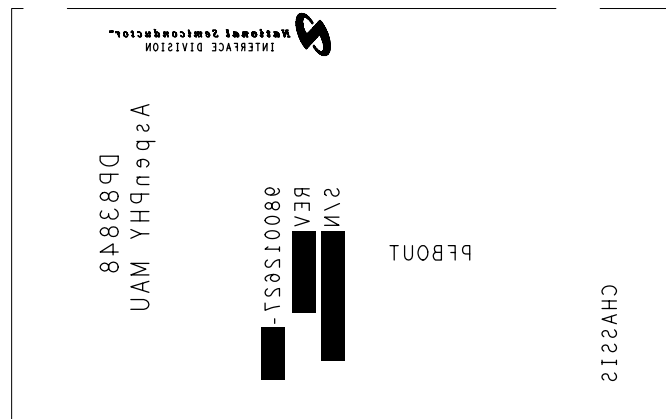
DATE: AUGUST 8, 2005


BOARD NO: 880012627-001

LAYER NO:

SHEET 7 of 8

REV A



 National Semiconductor™ INTERFACE DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE		DATE: AUGUST 8, 2005	
	BOARD NAME:DP83848 ASPENPHY MAU		BOARD NO: 880012627-001	REV A
	LAYER NAME:BOTTOM SILKSCREEN		LAYER NO:	SHEET 8 of 8

