
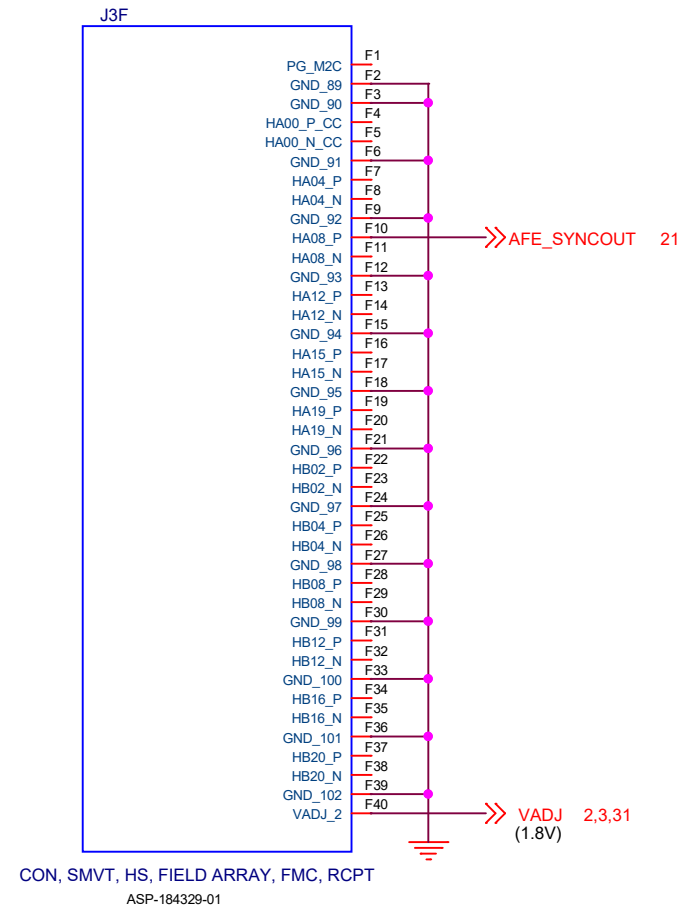
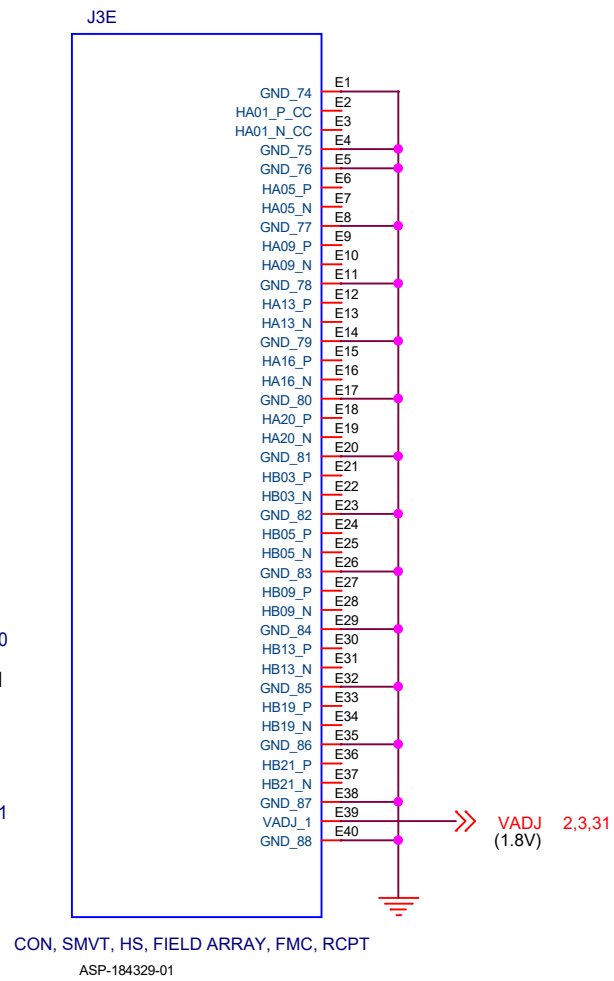
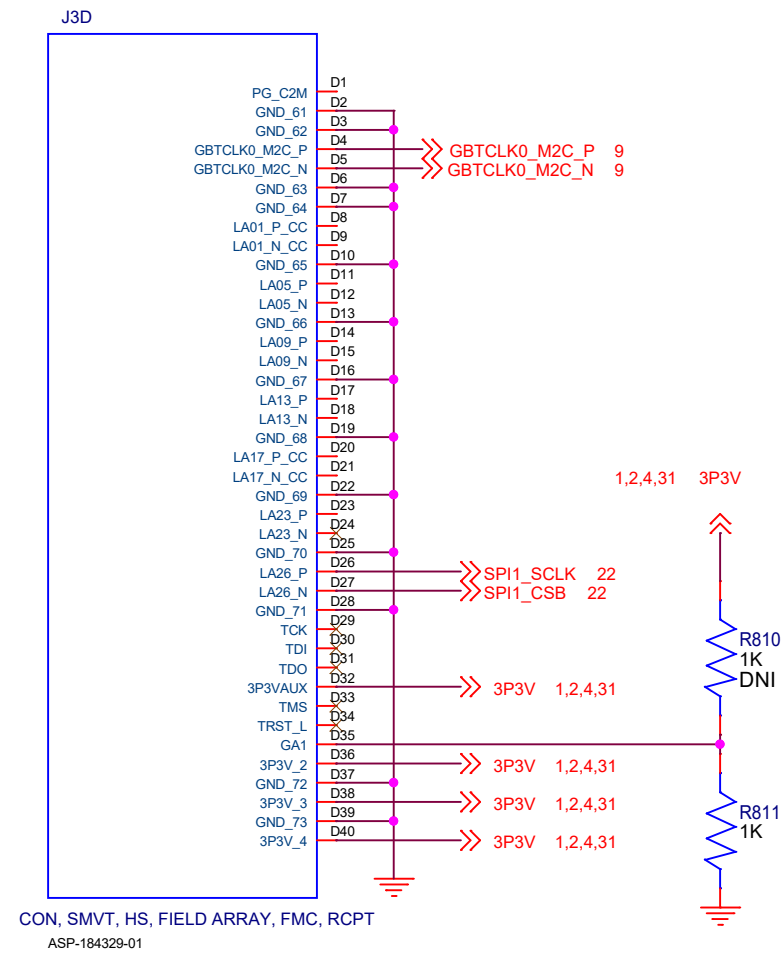



FMC CONNECTOR

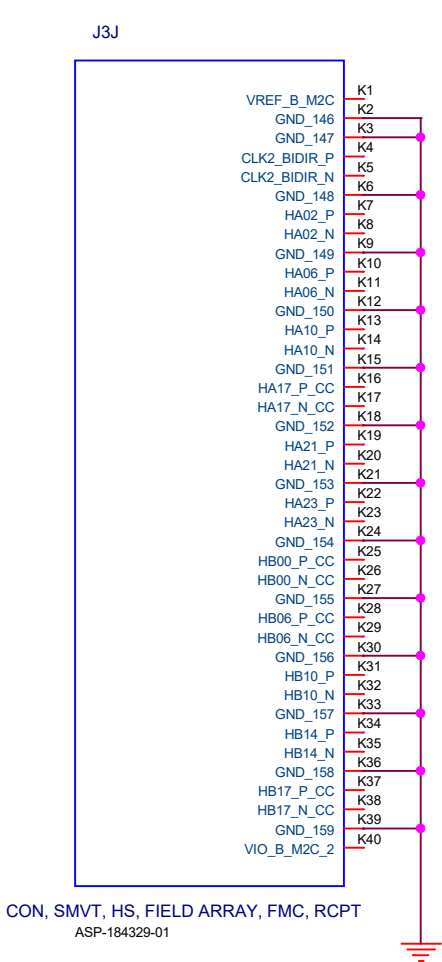
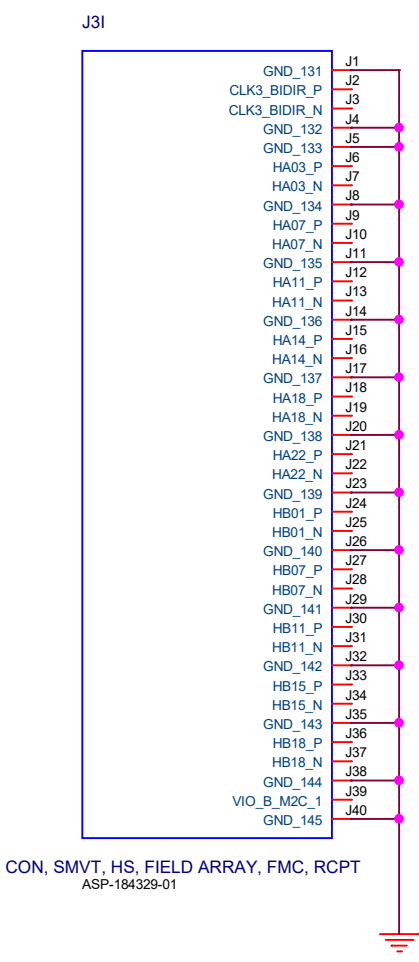
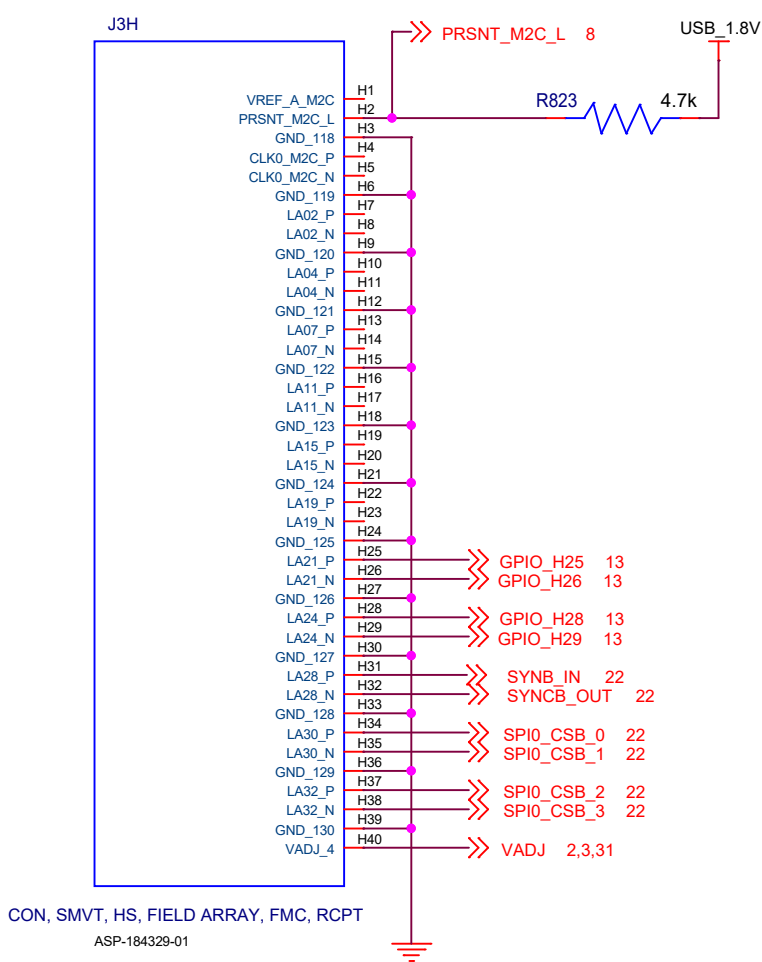
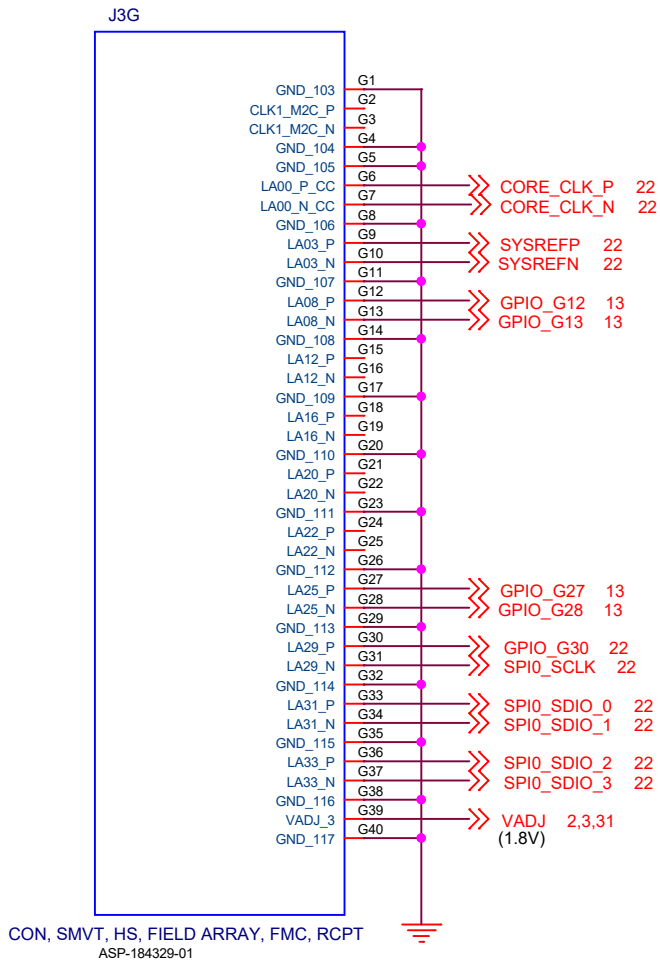
 **TEXAS INSTRUMENTS**

| | | |
|------------------|---------------------------------------|-----------------|
| Title | | |
| Size B | Document Number TSW14J59EVM | Rev A |
| Date: | Thursday, January 27, 2022 | Sheet 1 of 37 |




FMC CONNECTOR

| | | |
|---|----------------------------|---------------|
|  | | |
| Title | | |
| Size | Document Number | Rev |
| | TSW14J59EVM | A |
| Date: | Thursday, January 27, 2022 | Sheet 2 of 37 |

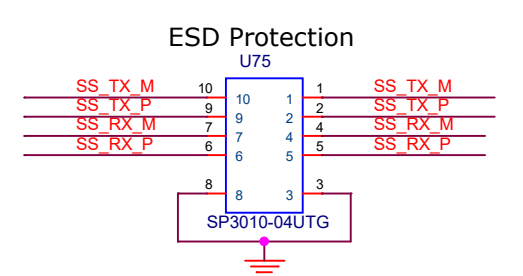
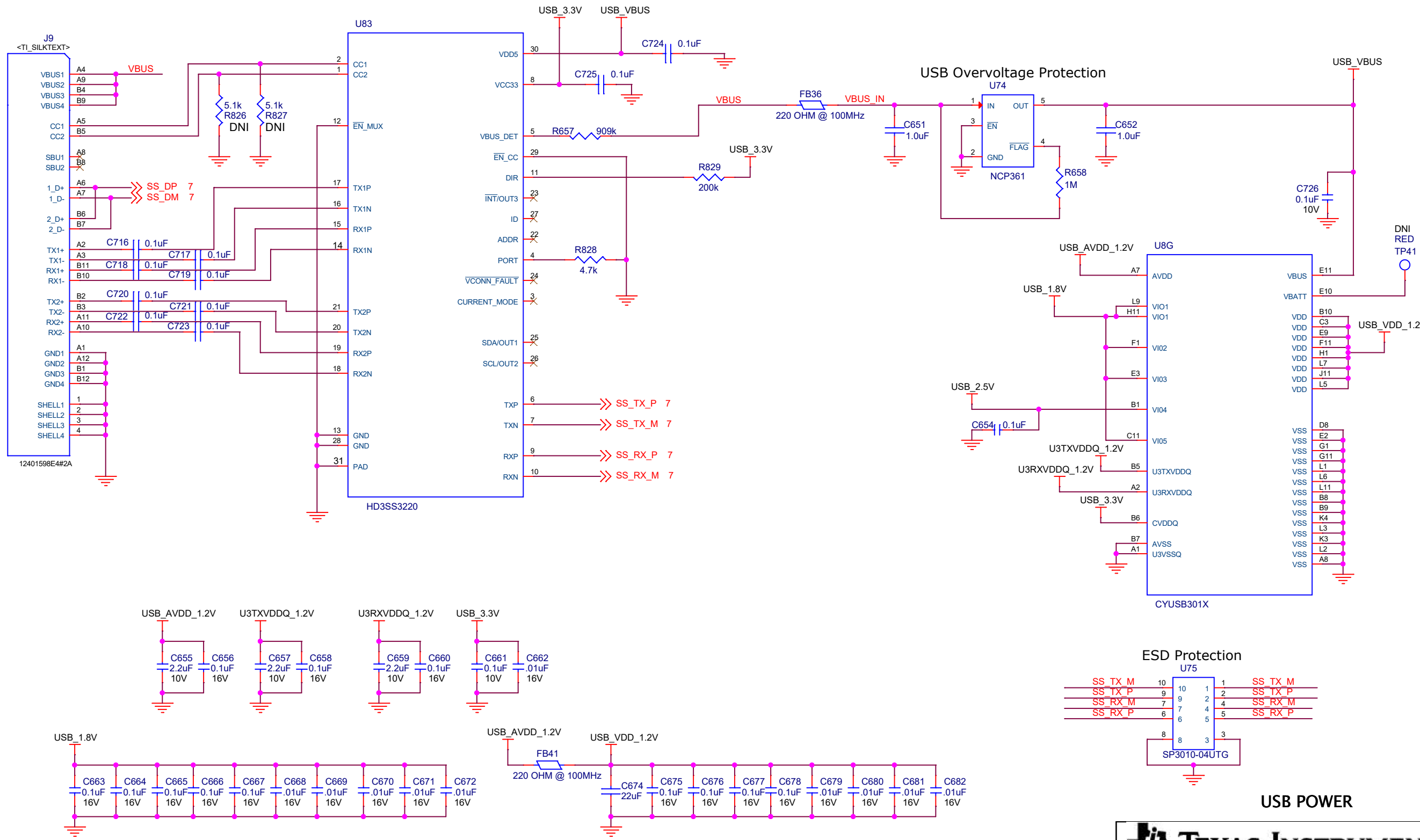


FMC CONNECTOR

 **TEXAS INSTRUMENTS**

Title

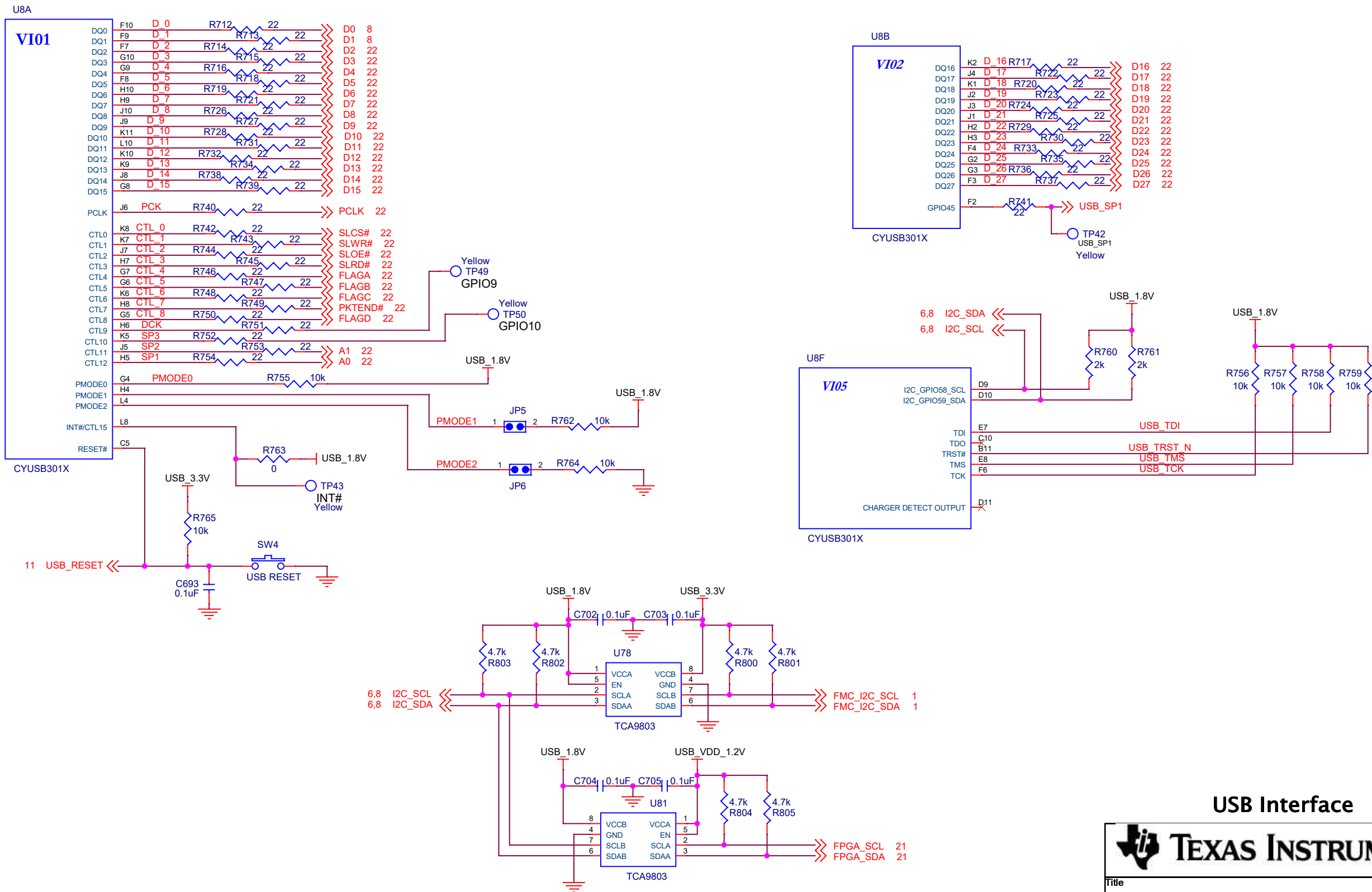
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| Size B | Document Number TSW14J59EVM | Rev A |
| Date: Thursday, August 25, 2022 | Sheet 3 of 37 | |




USB POWER

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|----------------------------------|-----------------------------|-------|
| Title | | |
| Size B | Document Number TSW14J59EVM | Rev A |
| Date: Wednesday, August 17, 2022 | Sheet 5 | of 37 |

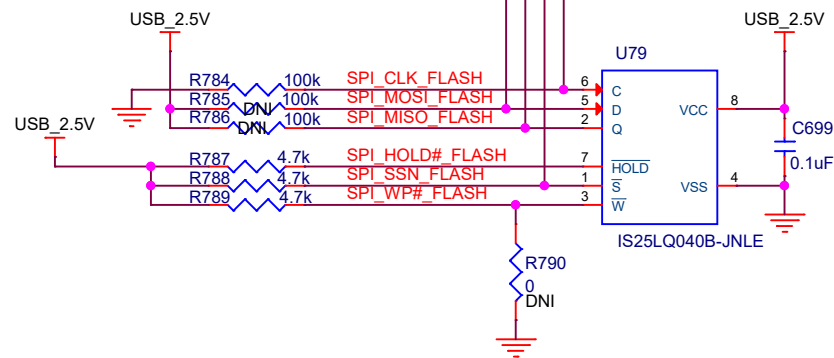
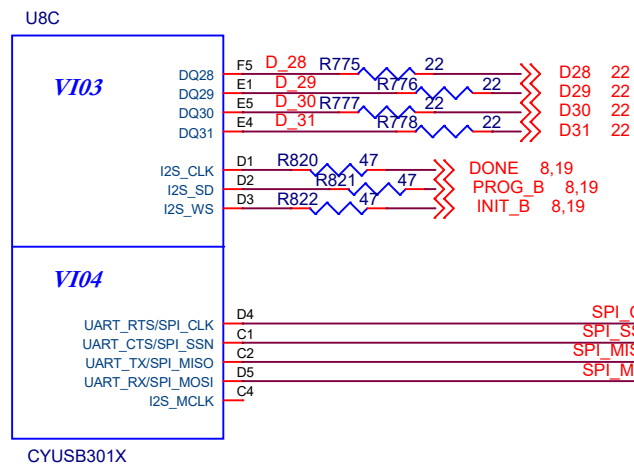
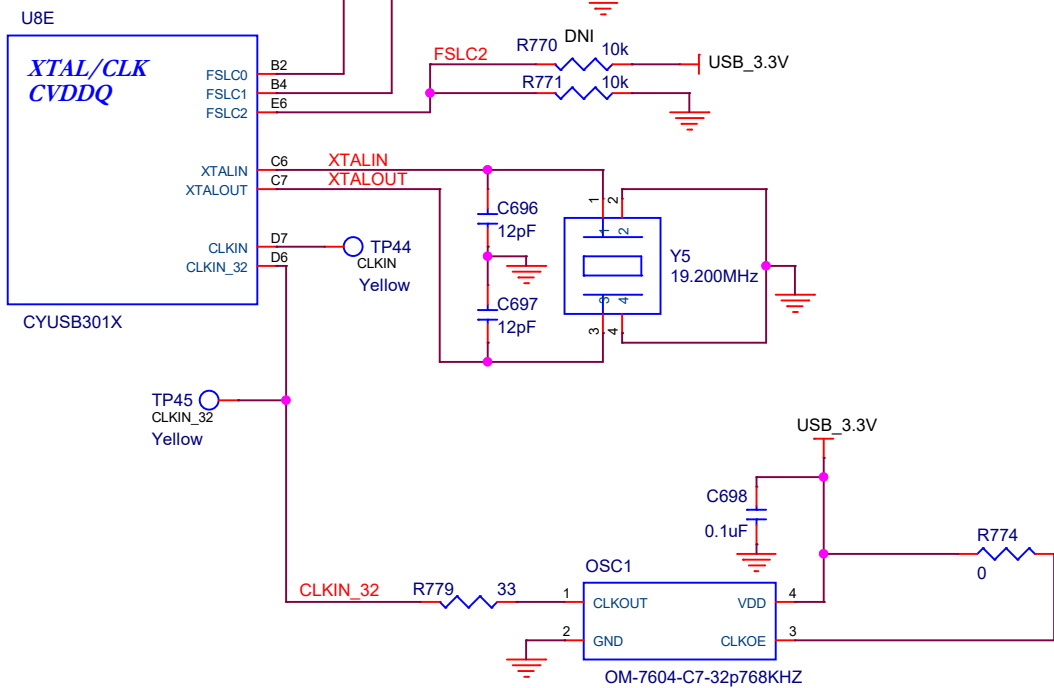
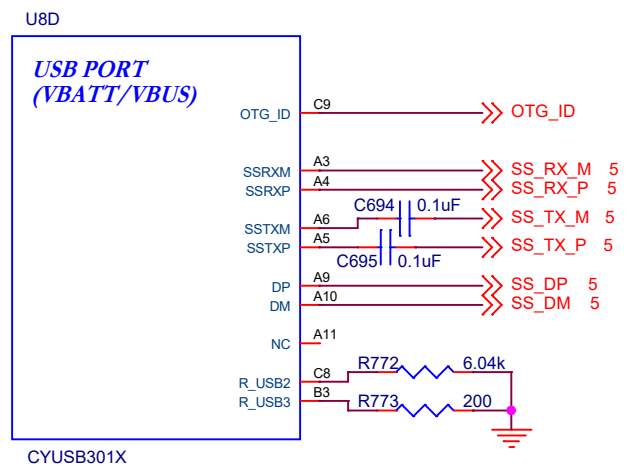
*** Match D0 and D1 trace length ***




USB Interface

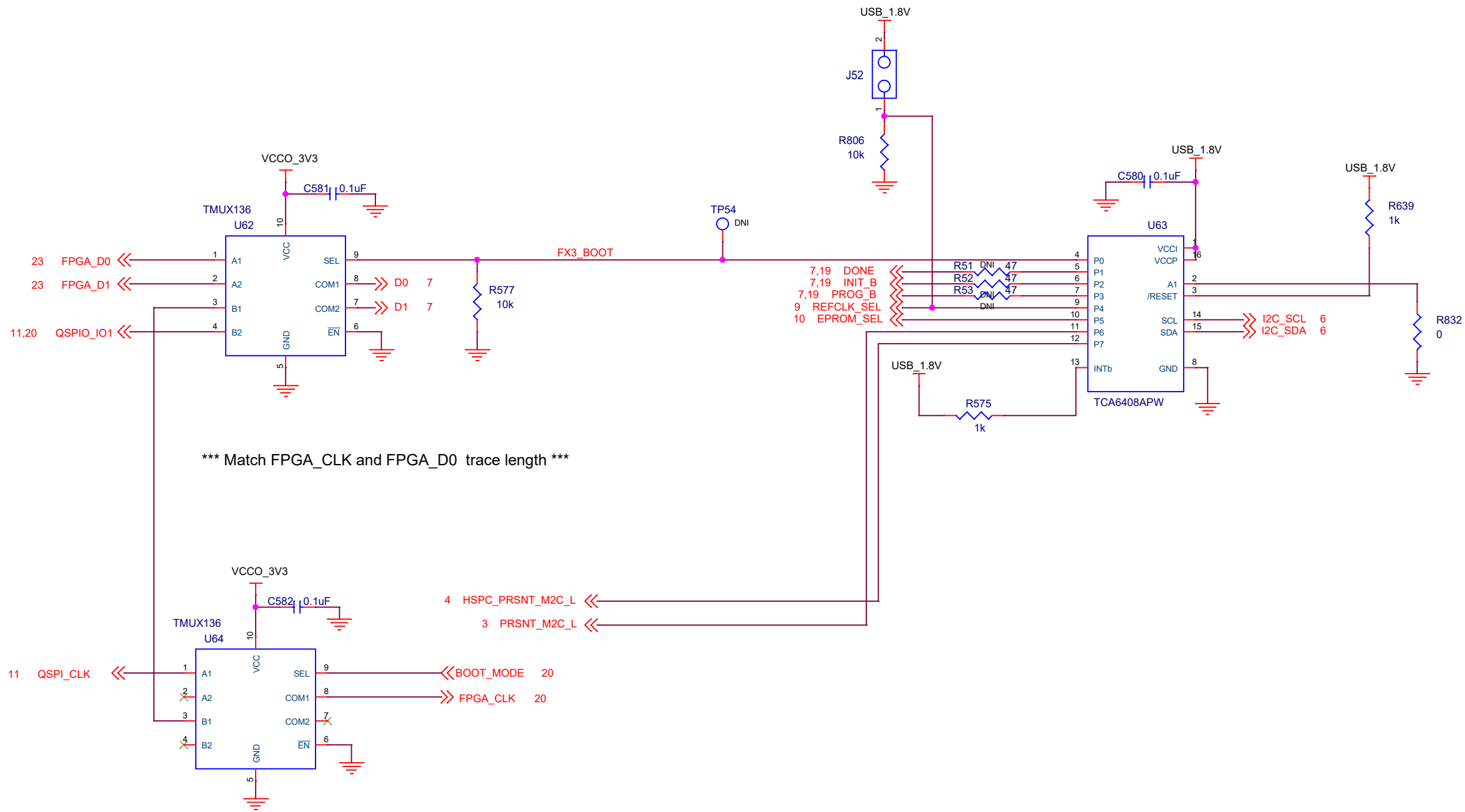
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| Title | | |
| Size B | Document Number | Rev A |
| Date: Thursday, January 27, 2022 | | Sheet 6 of 37 |

TSW14J59EVM

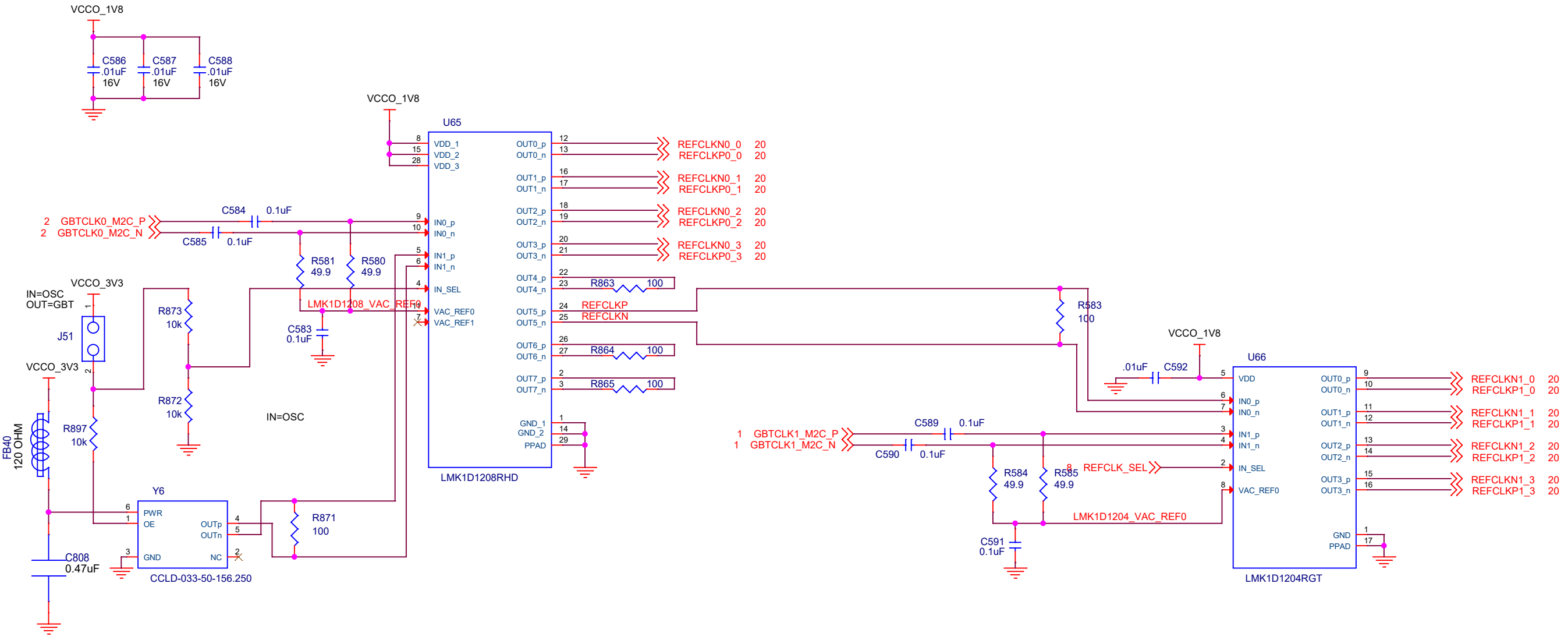


USB


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|  TEXAS INSTRUMENTS | | |
| Title | | |
| Size B | Document Number | Rev A |
| TSW14J59EVM | | |
| Date: | Saturday, February 26, 2022 | Sheet 7 of 37 |



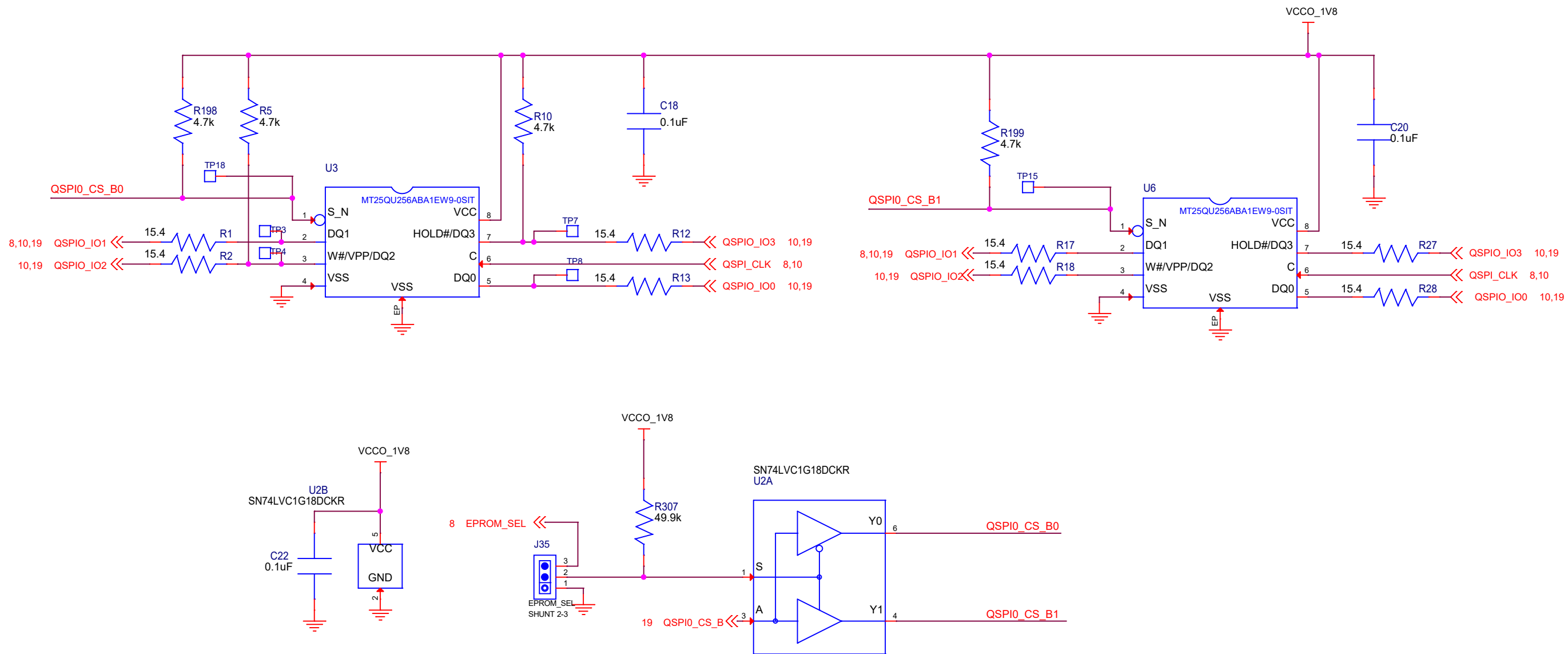
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| Title | | |
| Size B | Document Number TSW14J59EVM | Rev A |
| Date: | Tuesday, August 30, 2022 | Sheet 8 of 37 |




CLOCK FANOUT

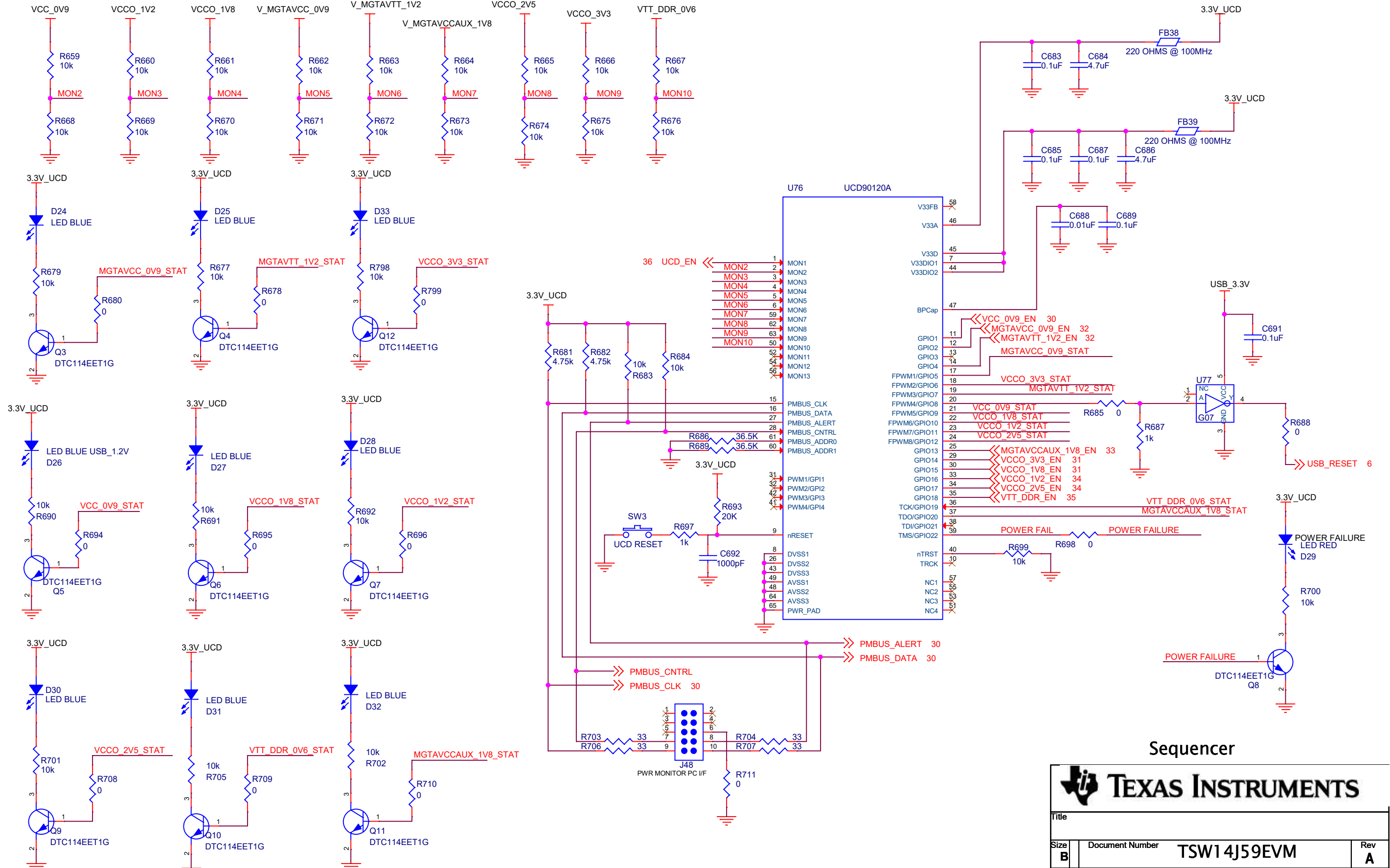
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|--------------------------------|---------------------------------------|-----------------|
| Title | | |
| Size B | Document Number TSW14J59EVM | Rev A |
| Date: Tuesday, August 30, 2022 | | |
| Sheet 9 of 37 | | |




PGM FLASH

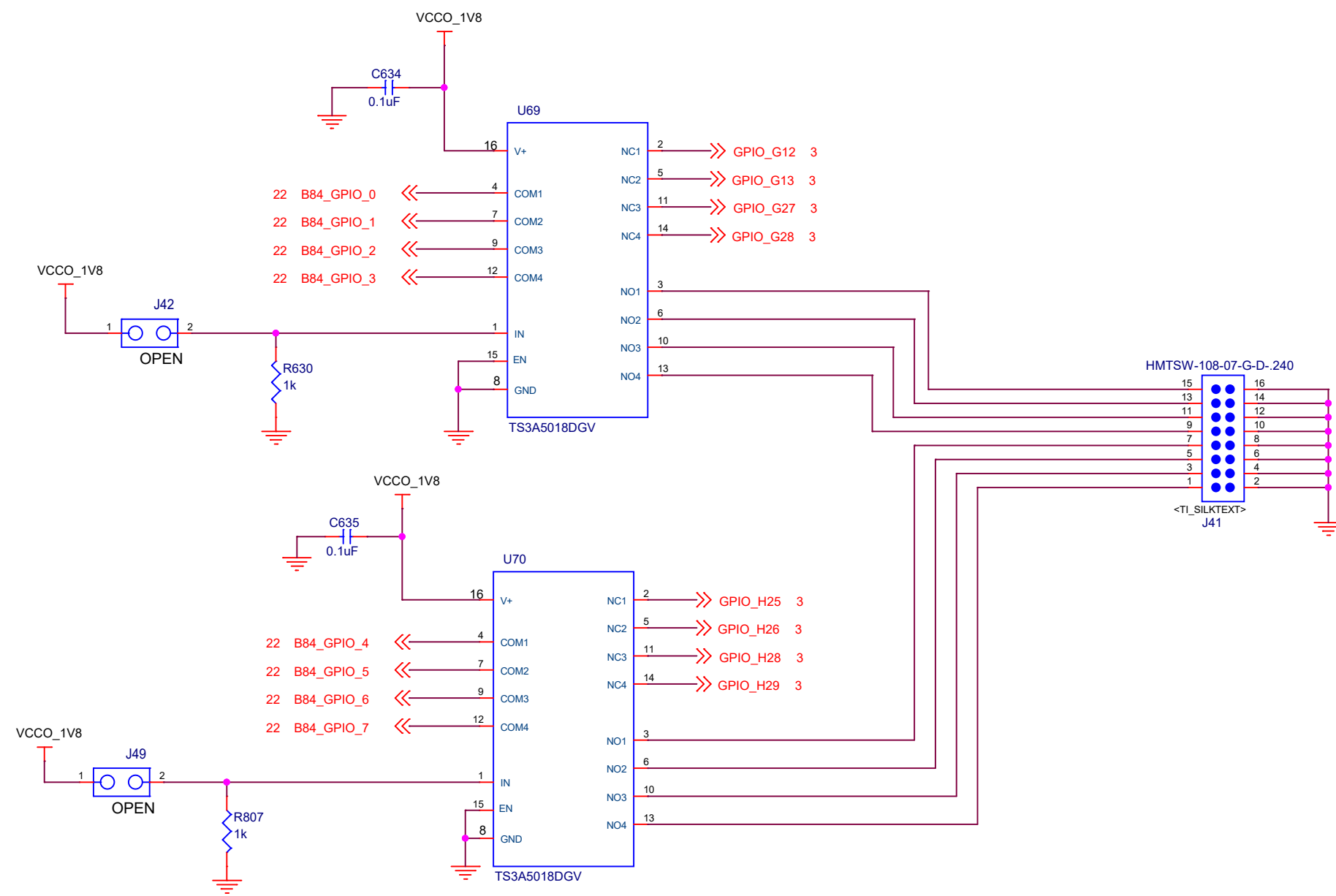
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|  | | | |
| Title | | | |
| Size B | Document Number TSW14J59EVM | | Rev A |
| Date: | Thursday, January 27, 2022 | Sheet | 10 of 37 |

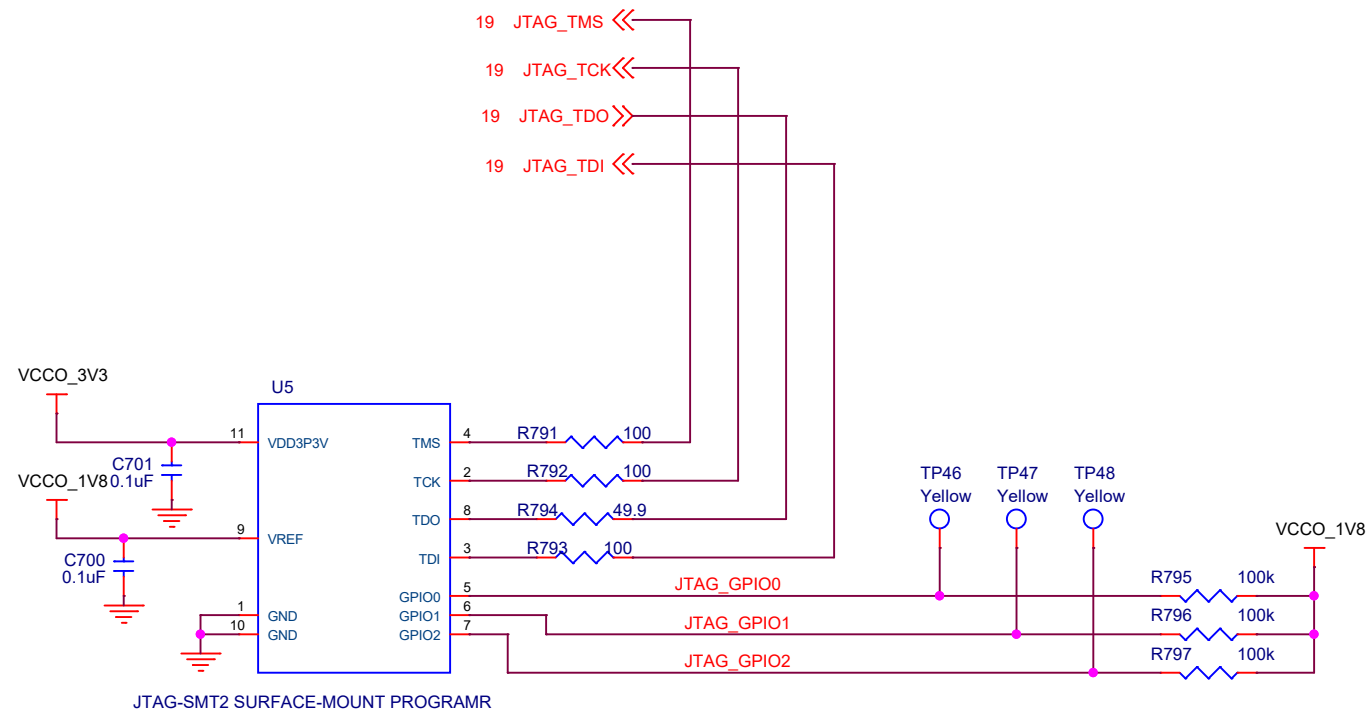


Sequencer


 **TEXAS INSTRUMENTS**

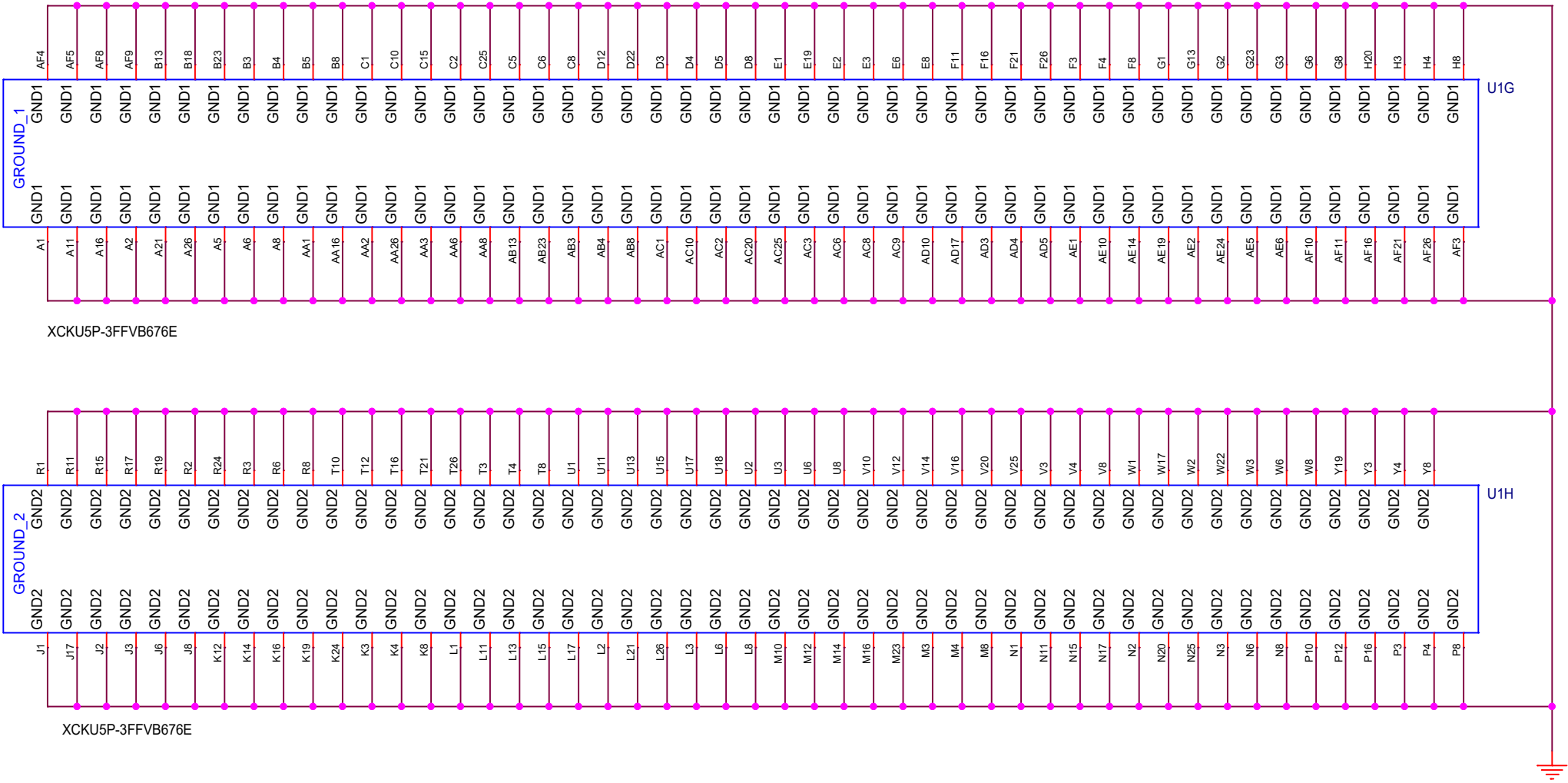
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| Title | | |
| Size B | Document Number TSW14J59EVM | Rev A |
| Date: | Tuesday, September 06, 2022 | Sheet 11 of 37 |






POWER SUPPLIES

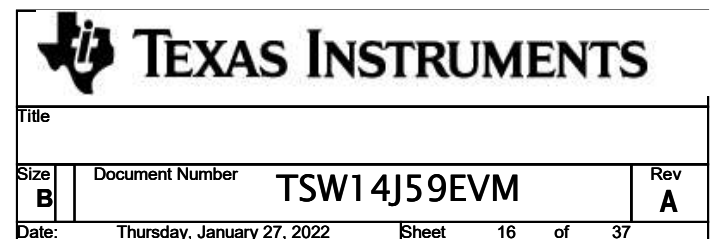
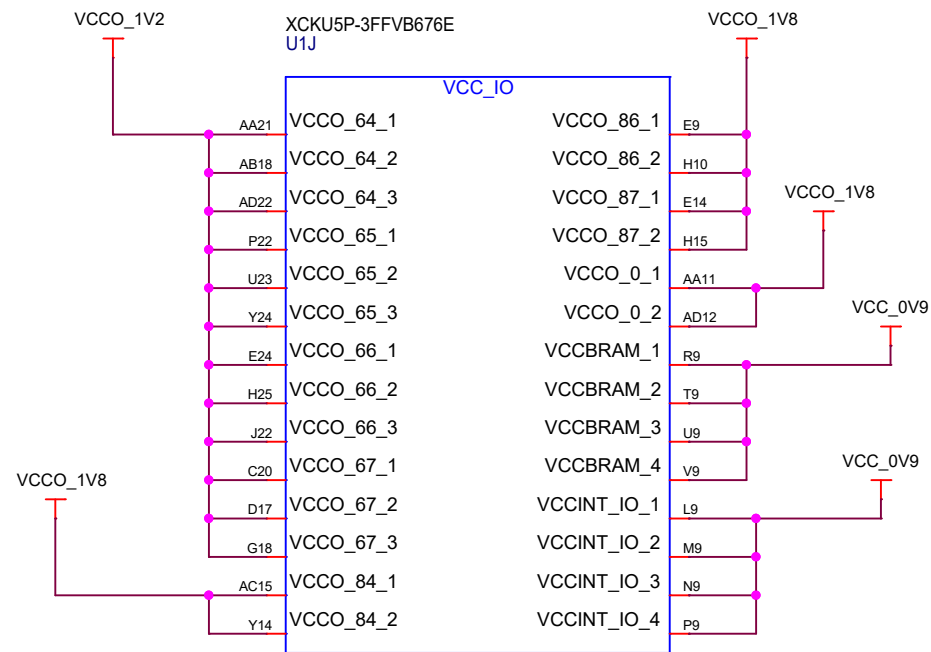
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|  TEXAS INSTRUMENTS | | | |
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| Size B | Document Number TSW14J59EVM | | Rev A |
| Date: | Thursday, January 27, 2022 | Sheet 14 of 37 | |

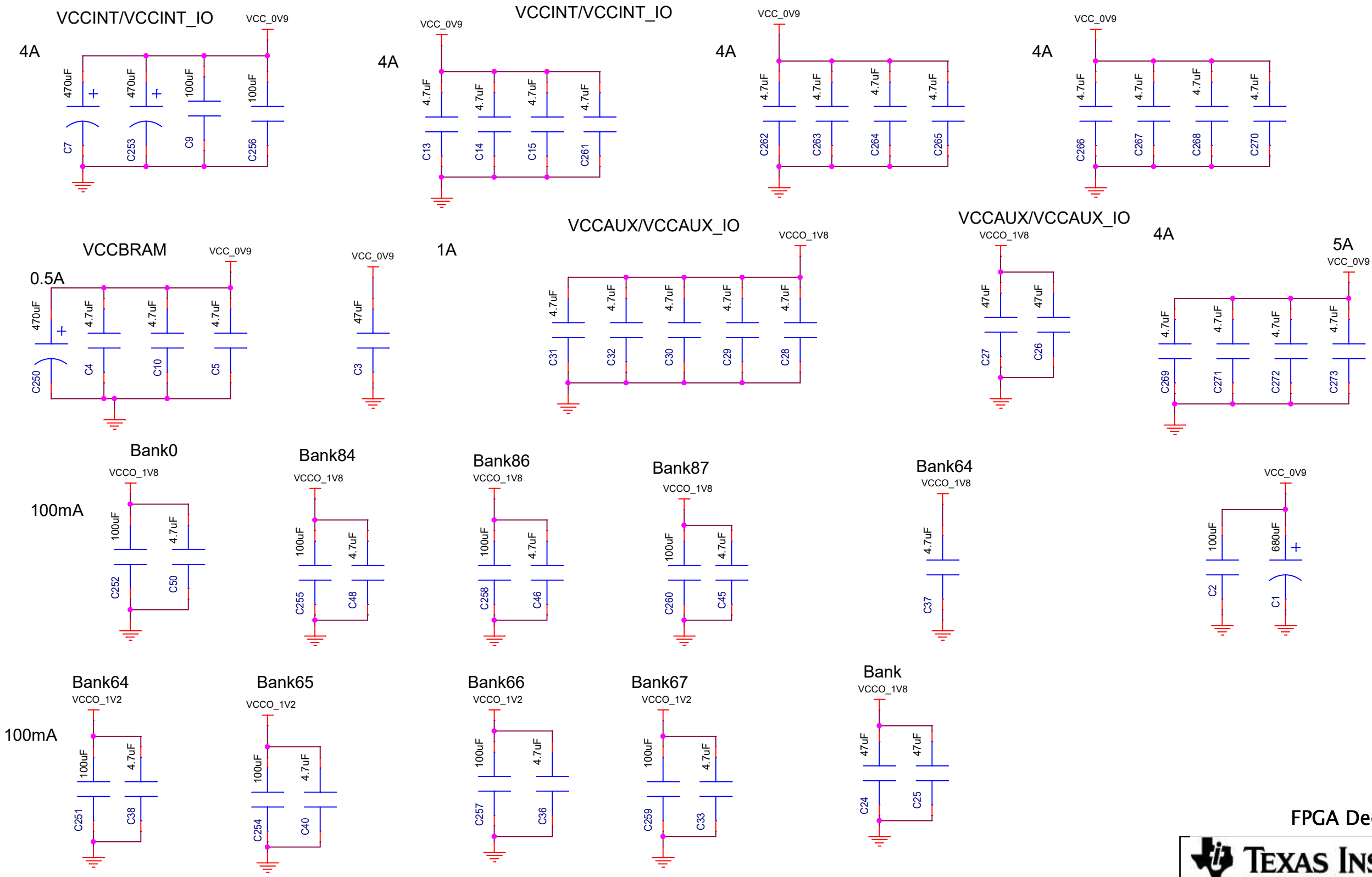


FPGA GND

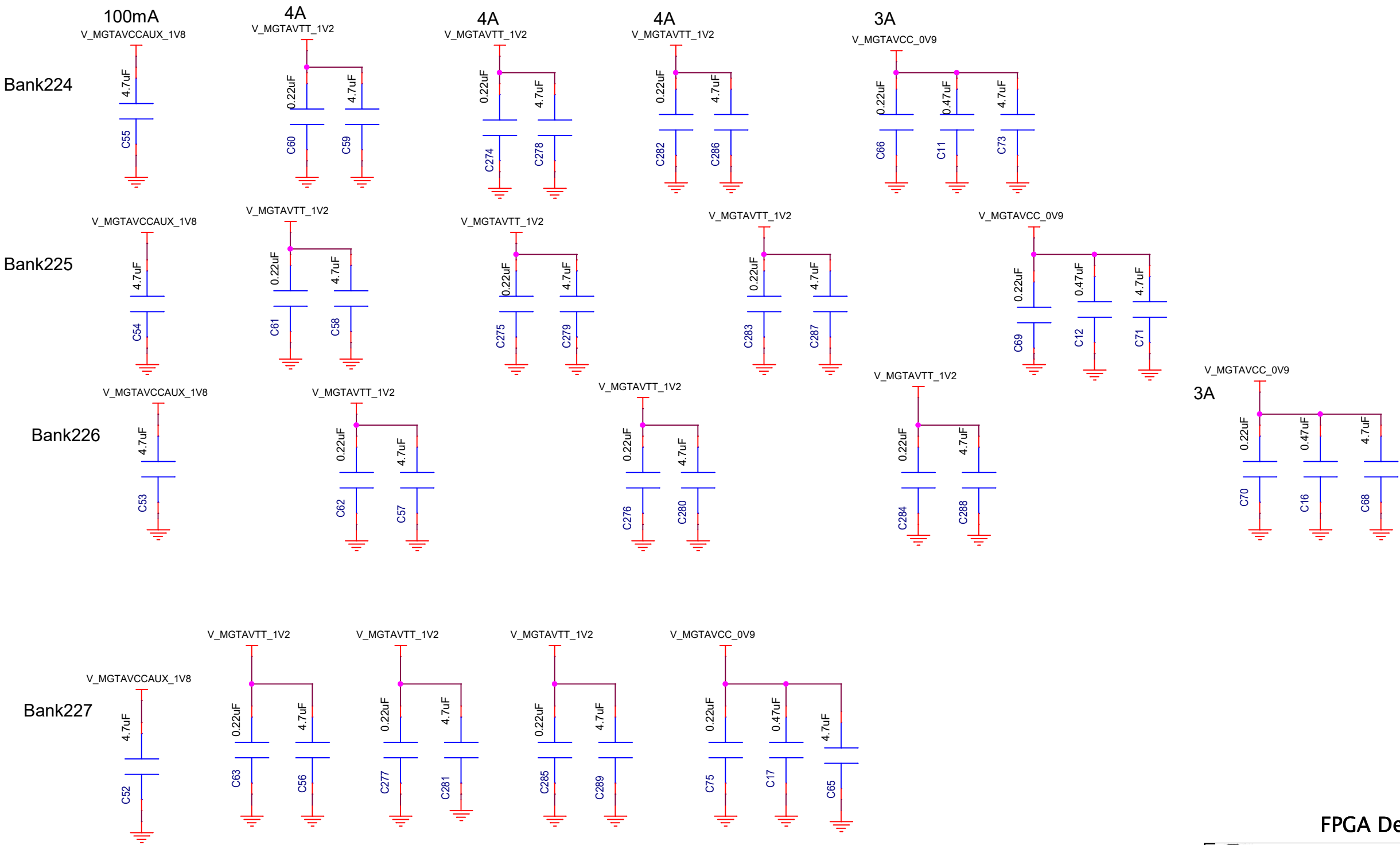
 **TEXAS INSTRUMENTS**

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|------------------|---------------------------------------|-----------------|
| Title | | |
| Size B | Document Number TSW14J59EVM | Rev A |
| Date: | Thursday, January 27, 2022 | Sheet 15 of 37 |

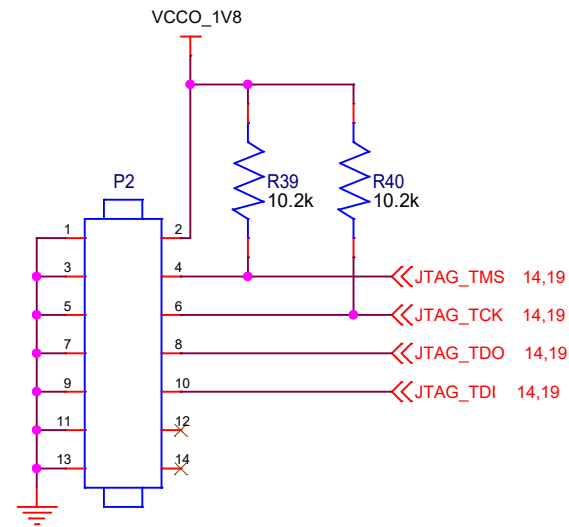
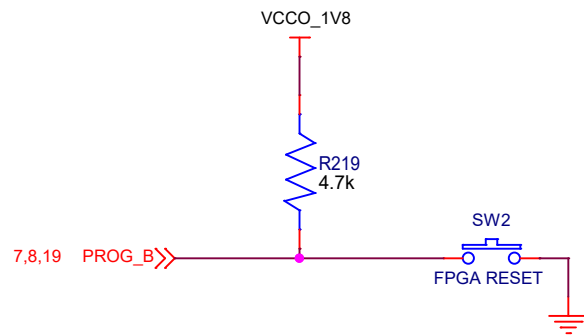
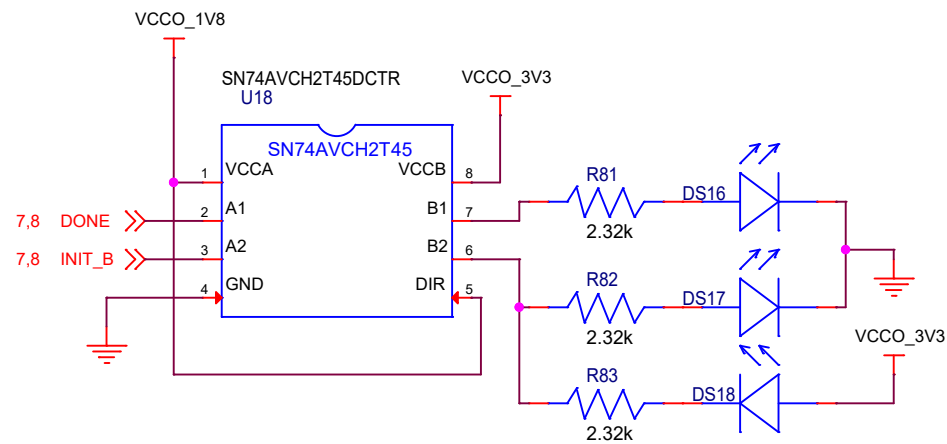
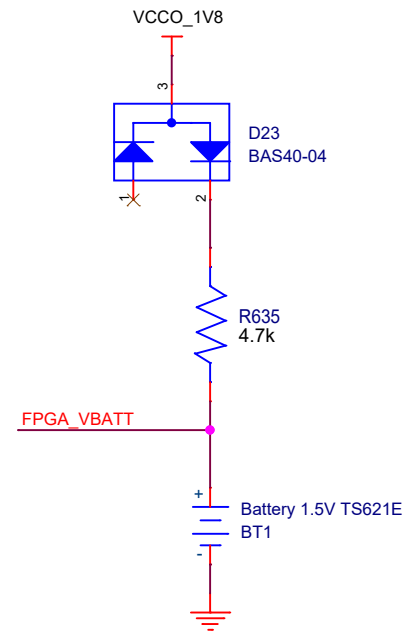
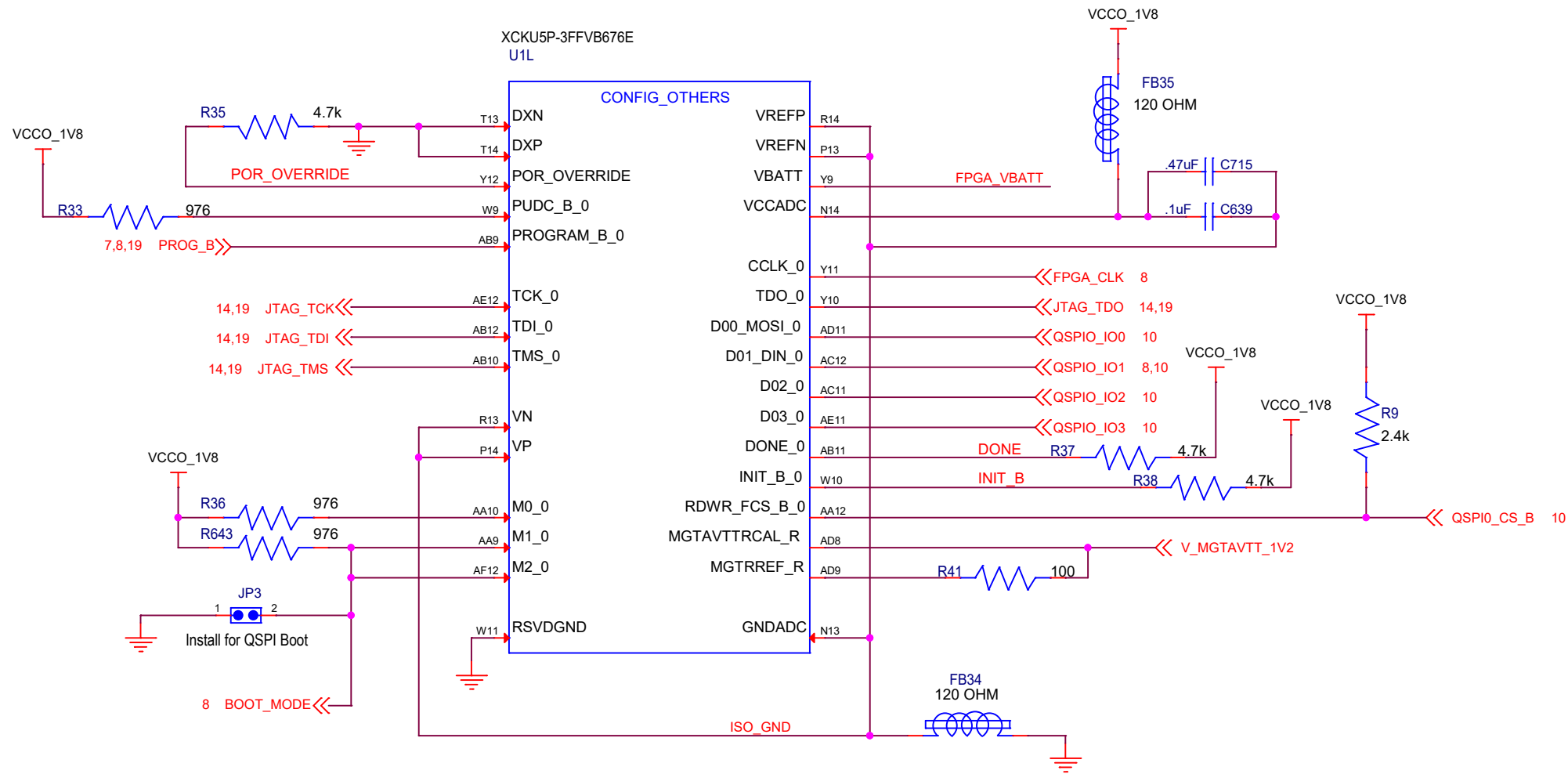


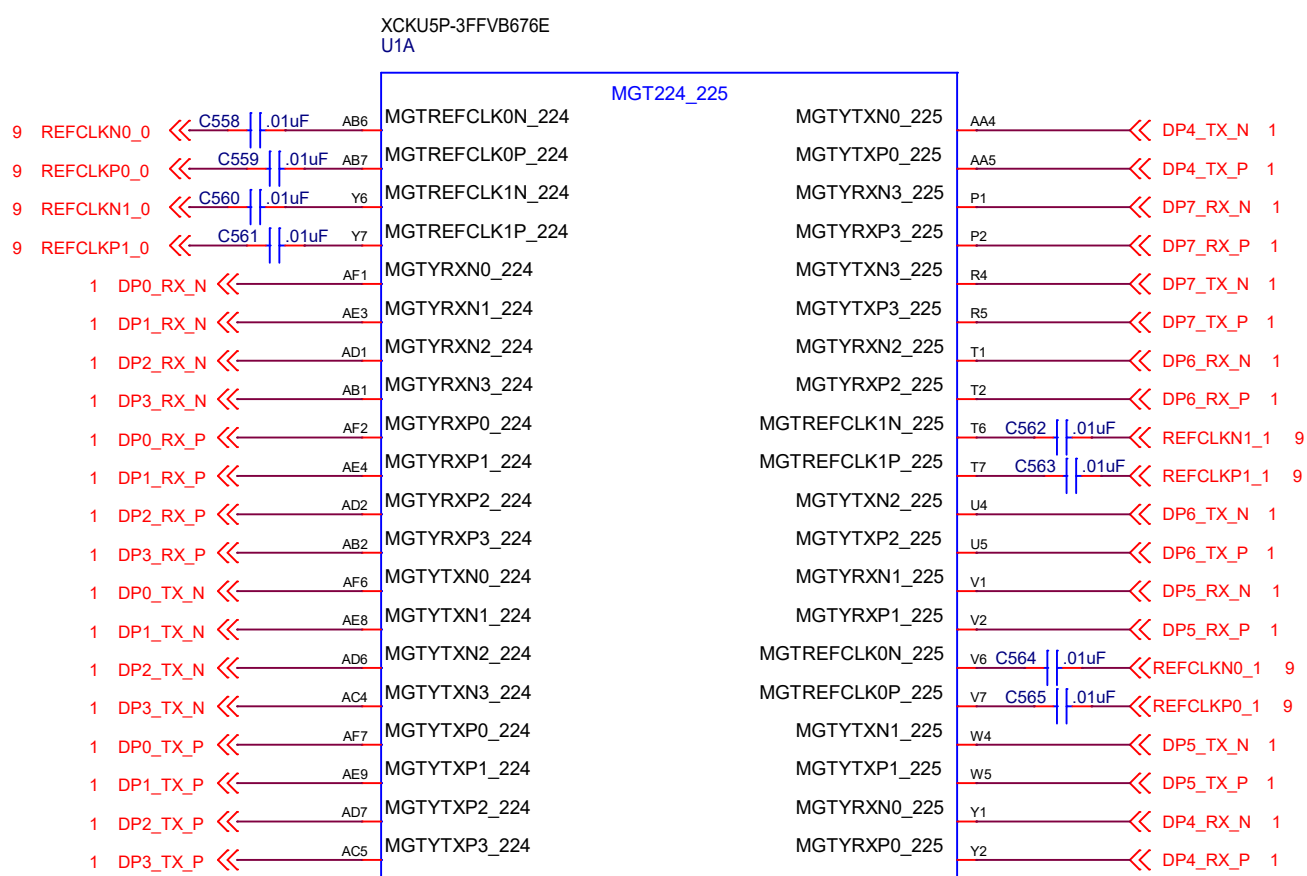


FPGA Decoupling

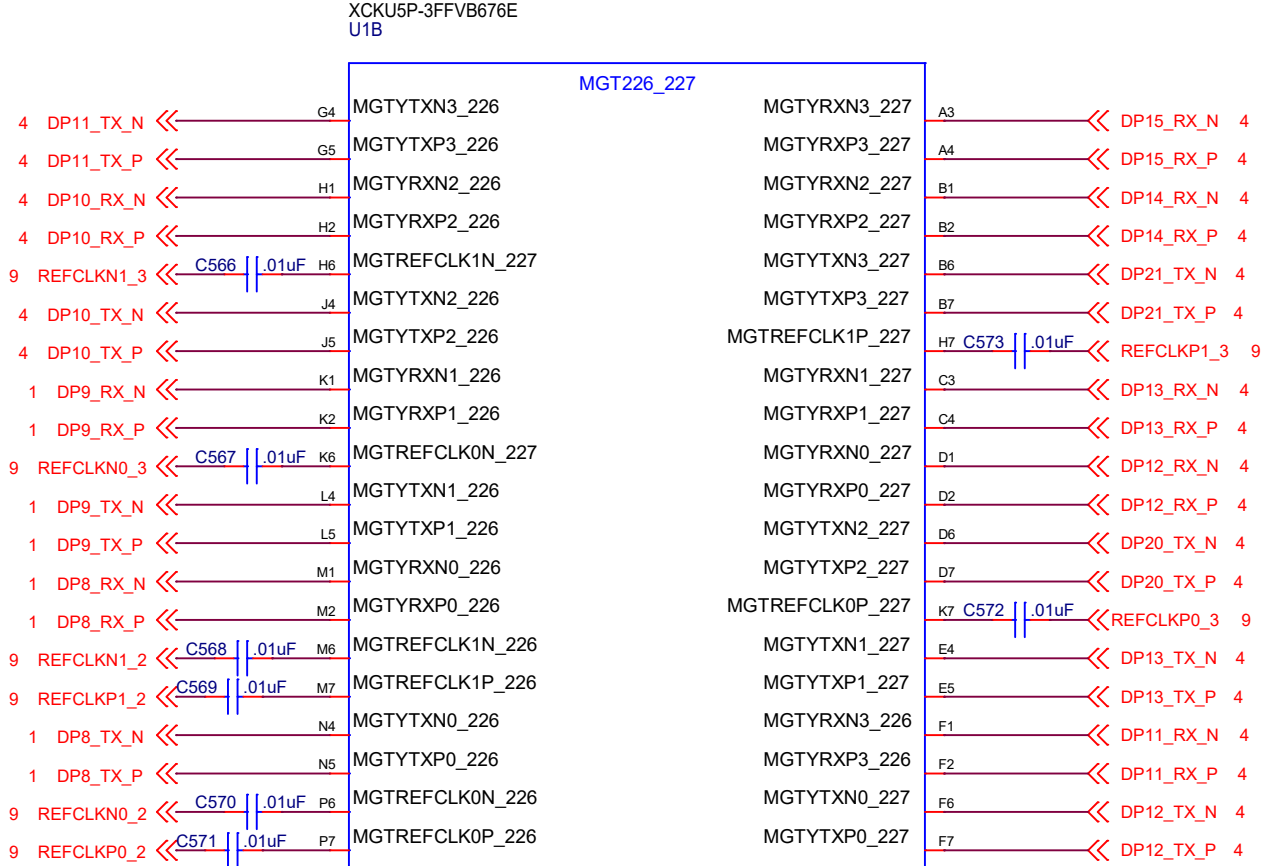


FPGA Decoupling

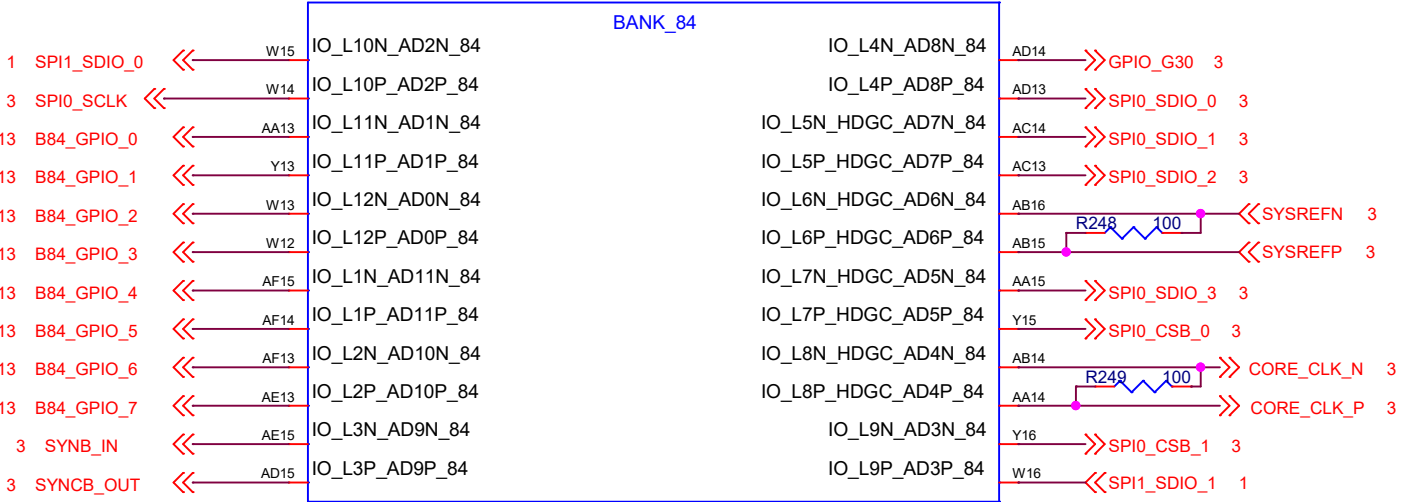
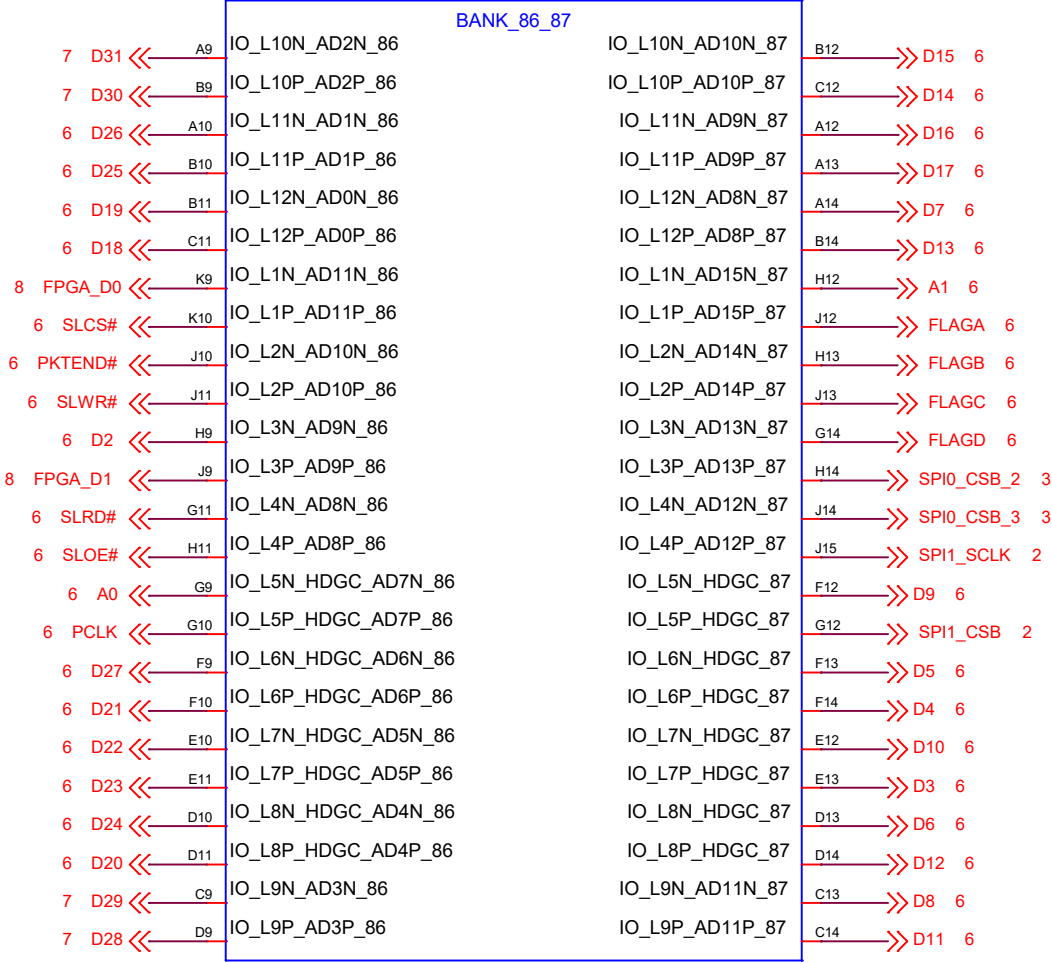




Ensure Clock and Signals are AC Coupled



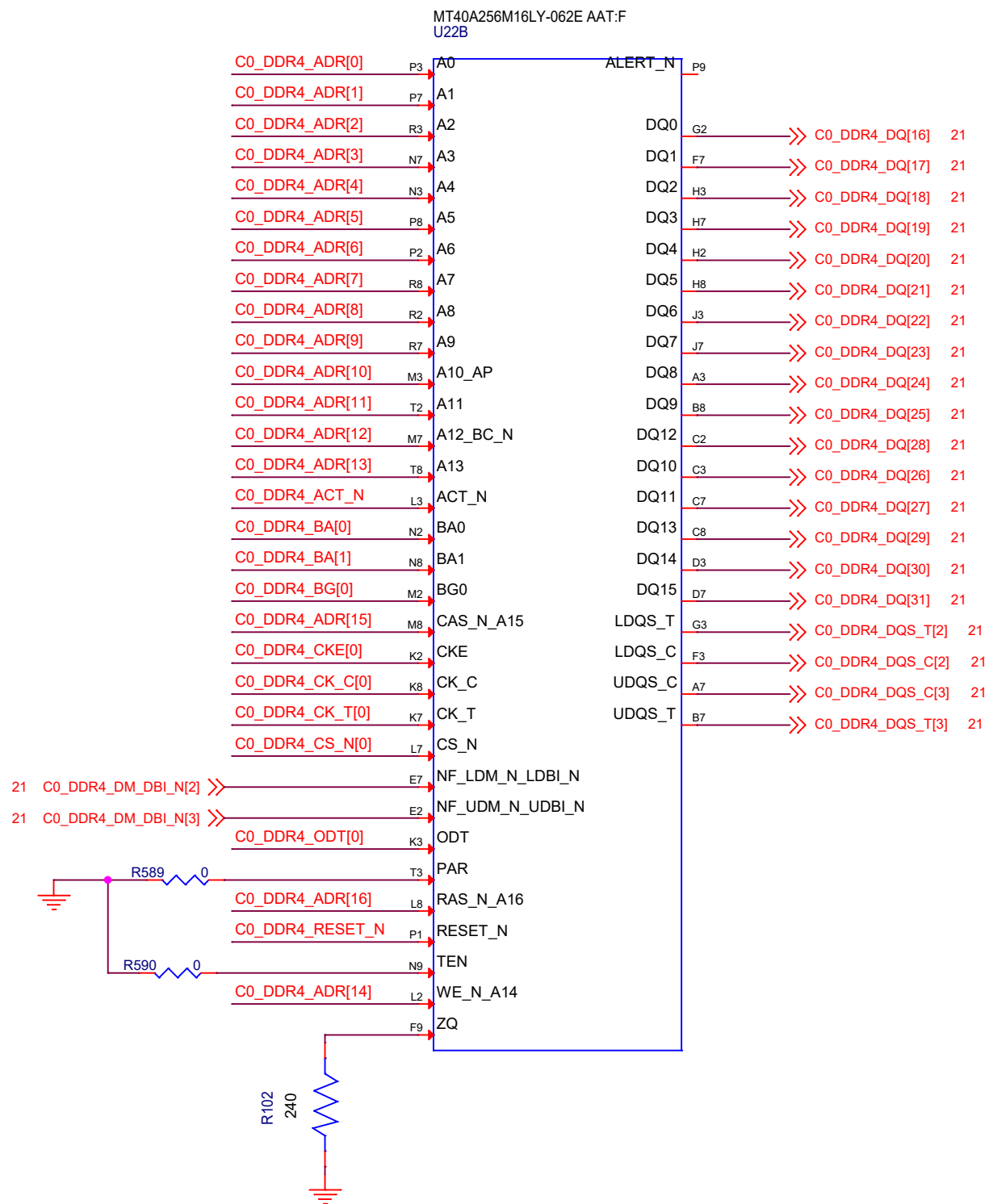
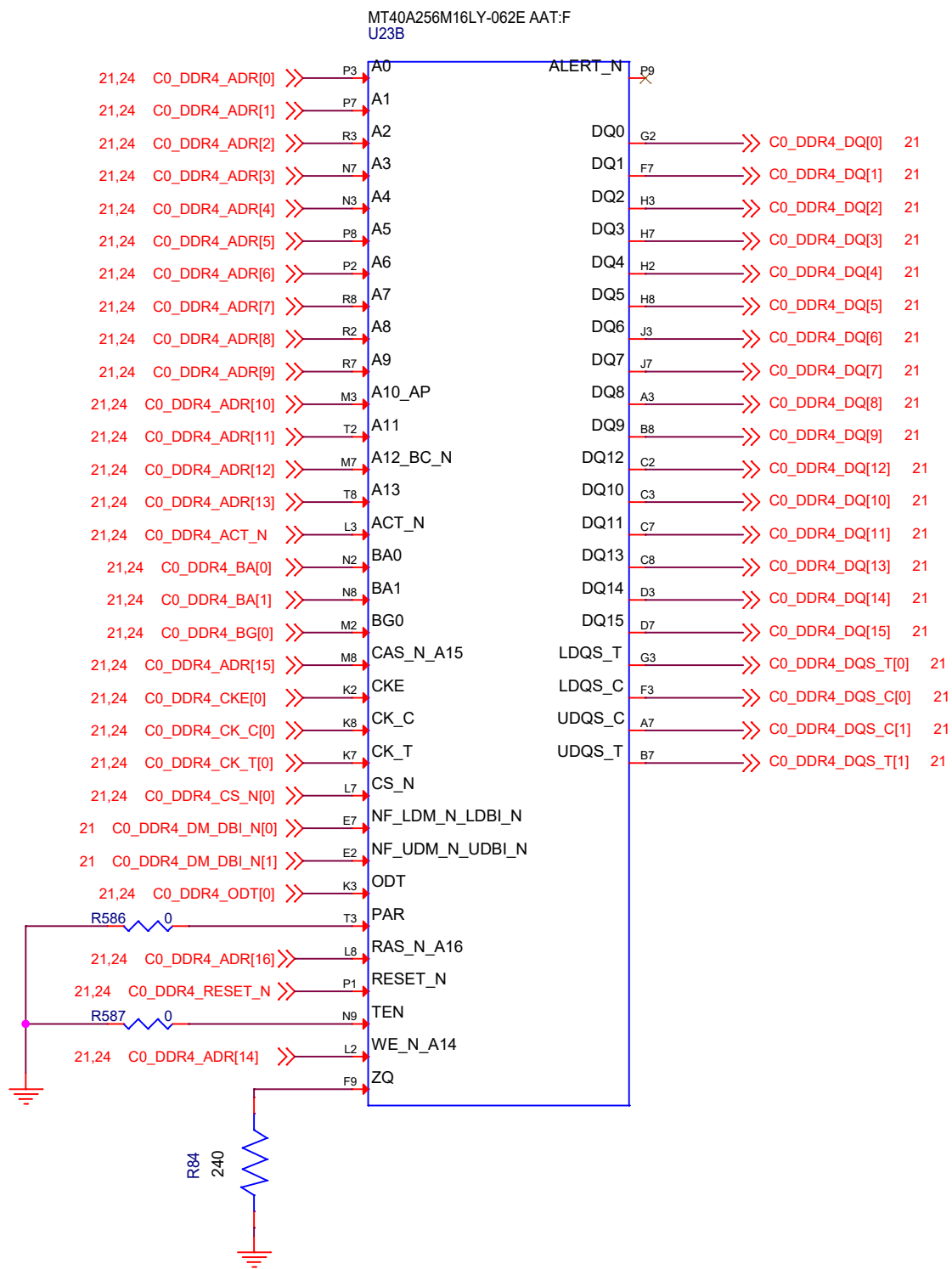
FPGA GTY



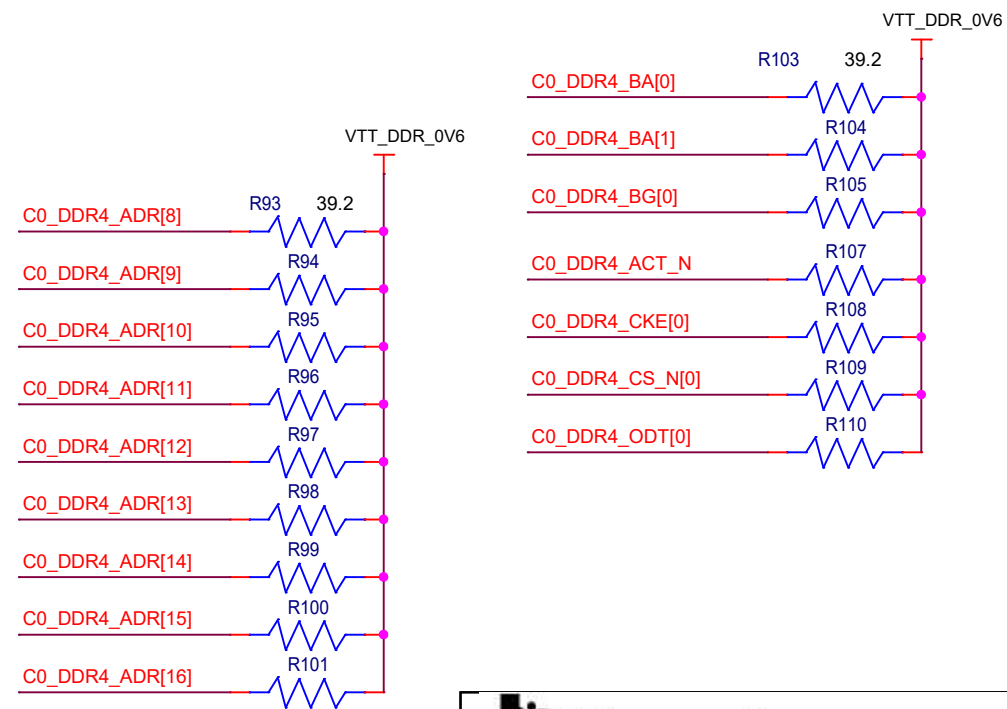
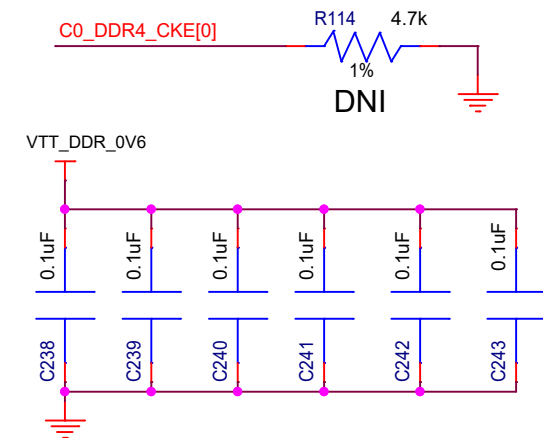
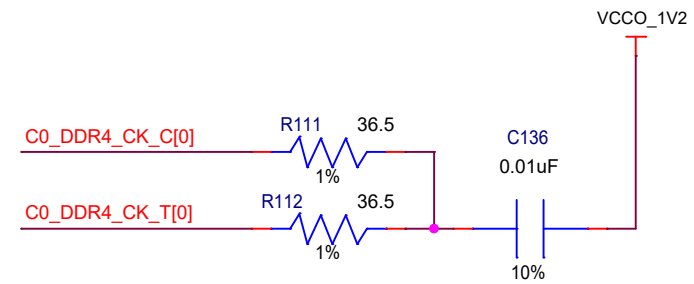
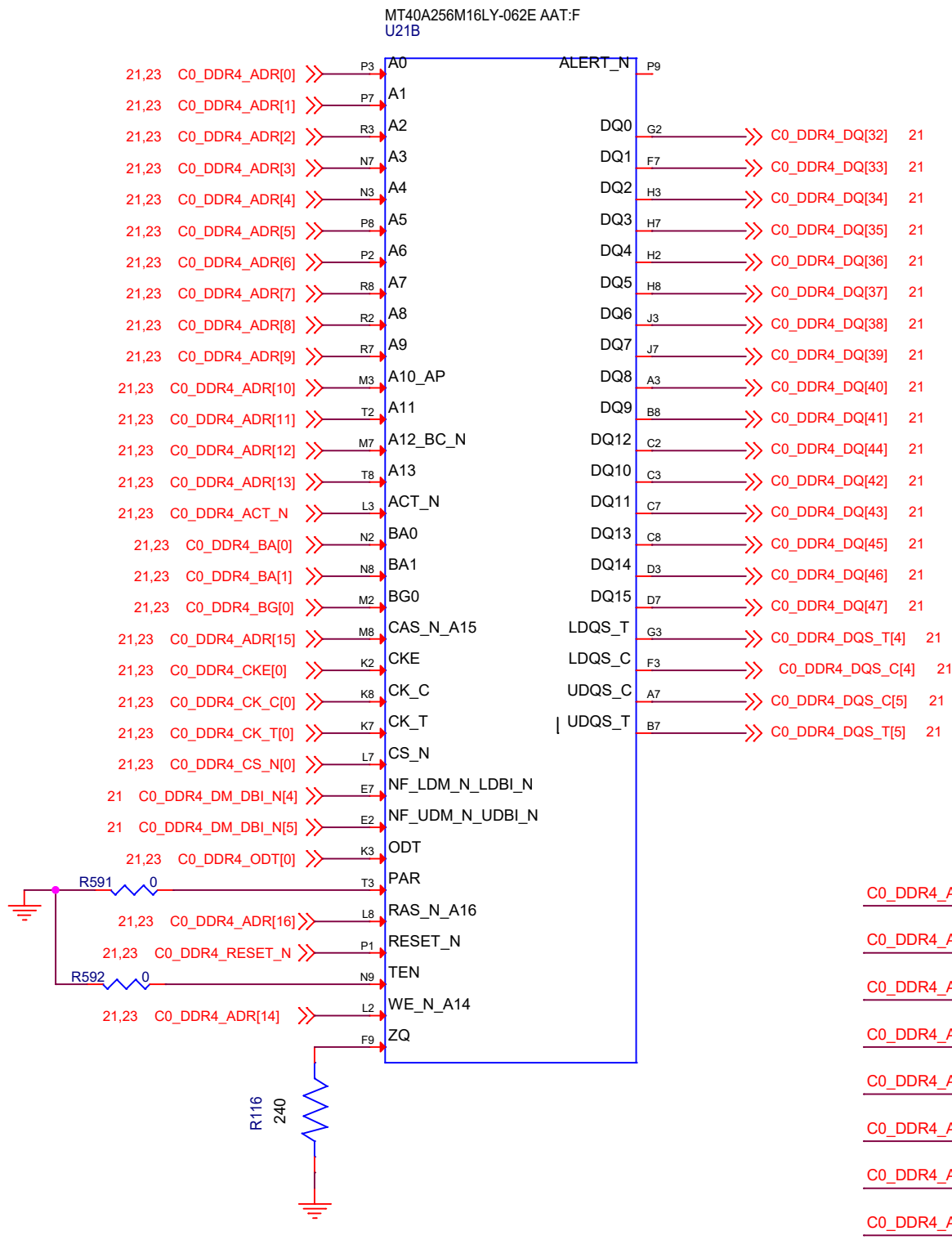
FPGA USB/IO BANK (2.5V Default)



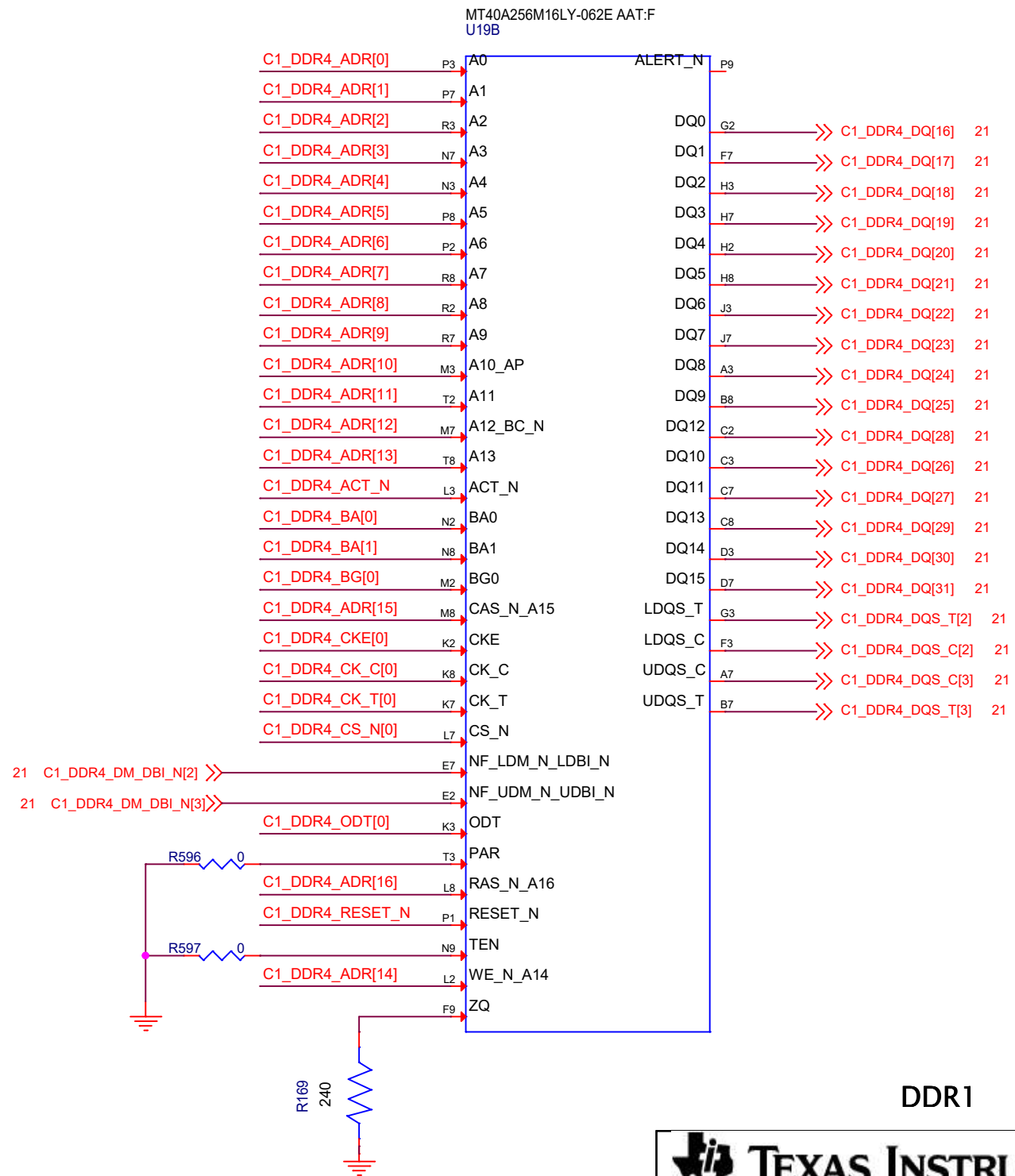
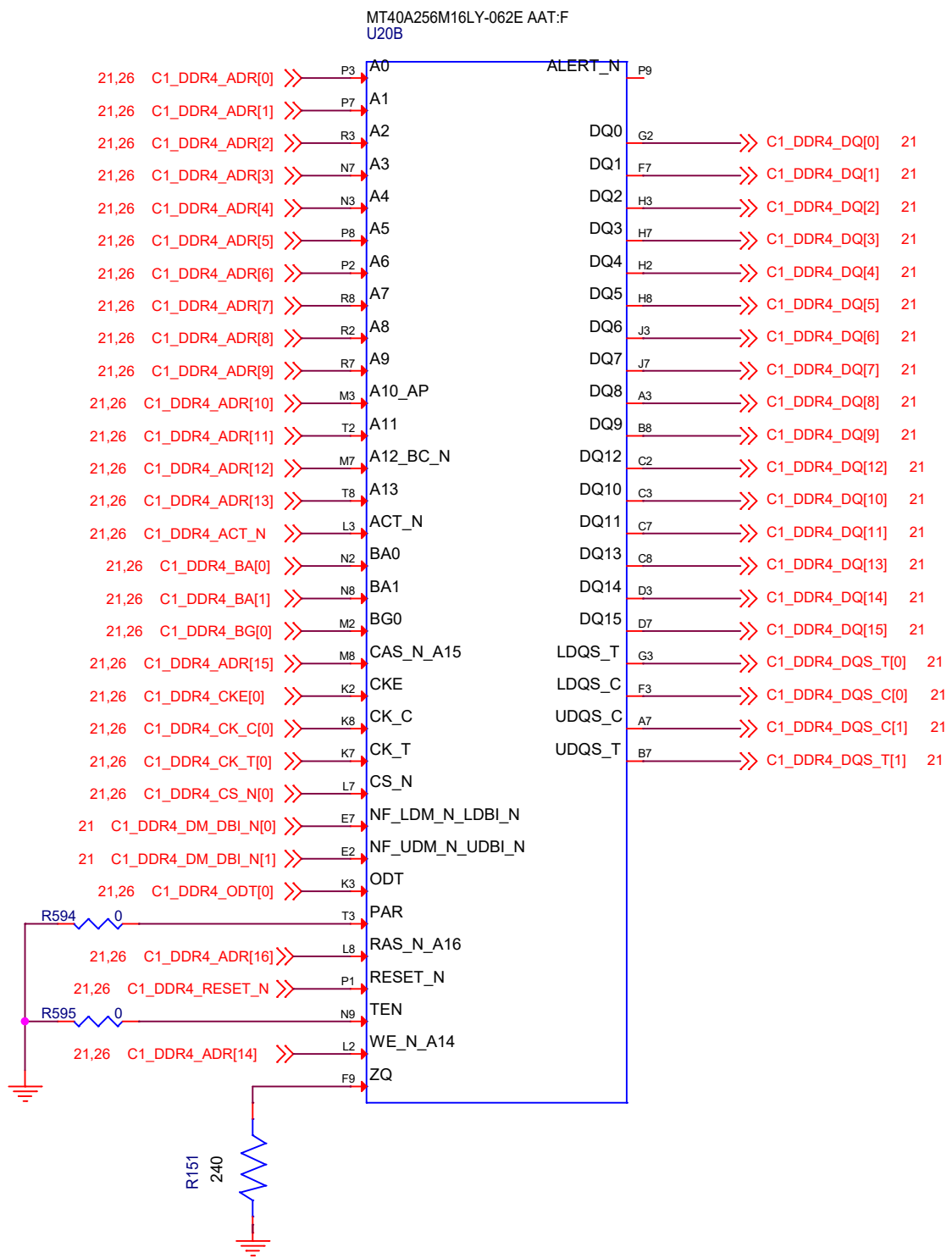
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|-------|----------------------------|-------|----------|
| Title | | | |
| Size | Document Number | | Rev |
| B | TSW14J59EVM | | A |
| Date: | Thursday, January 27, 2022 | Sheet | 22 of 37 |



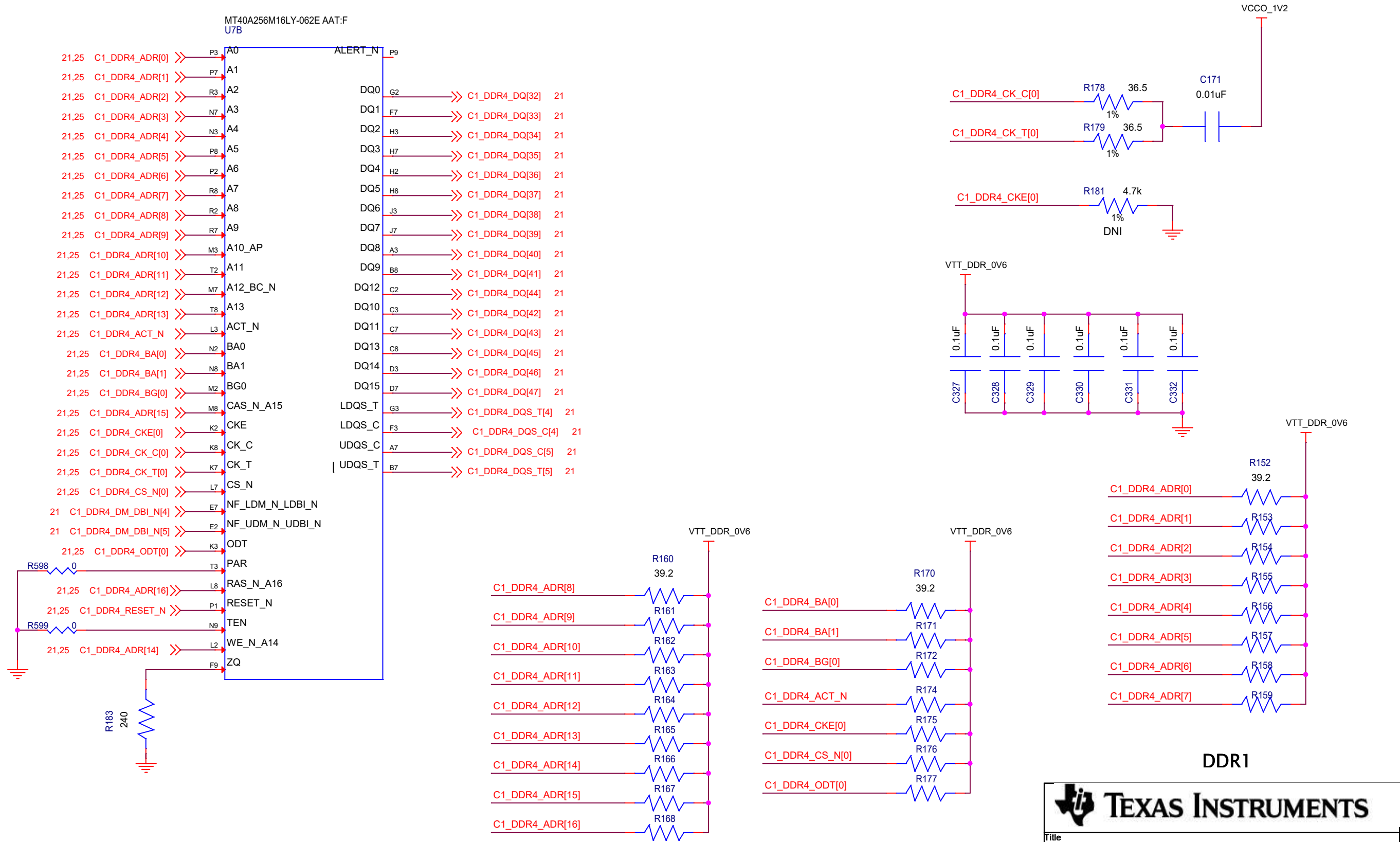
DDR0

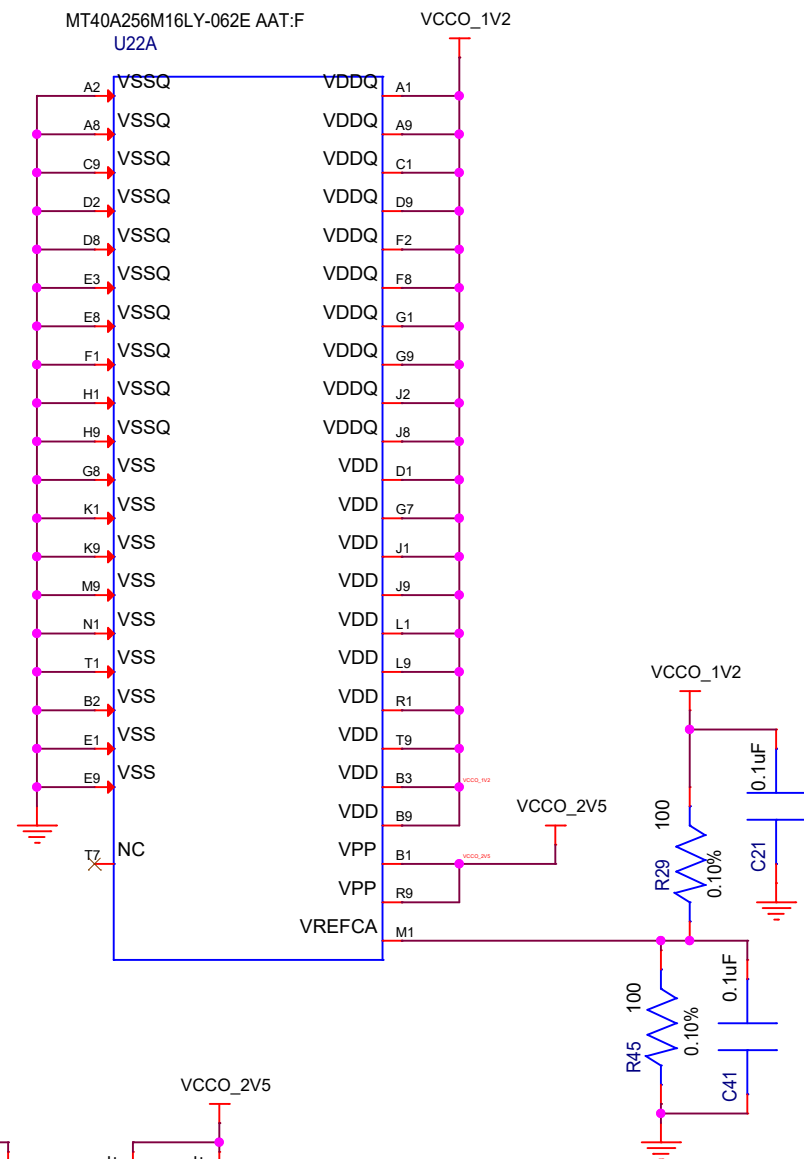
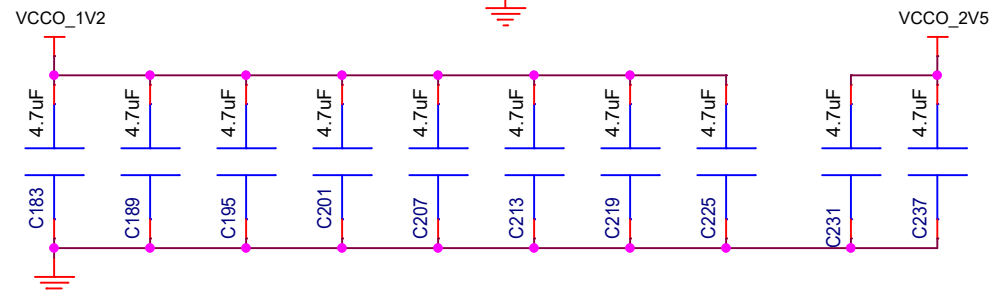
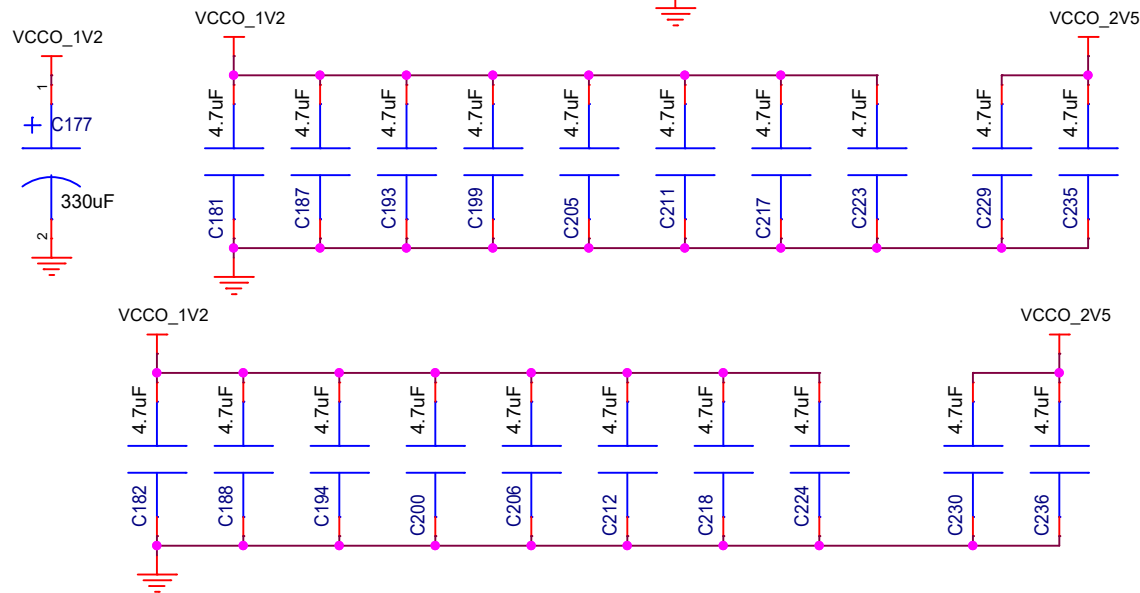


DDR0

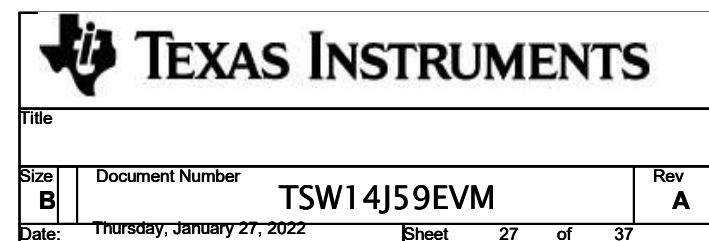


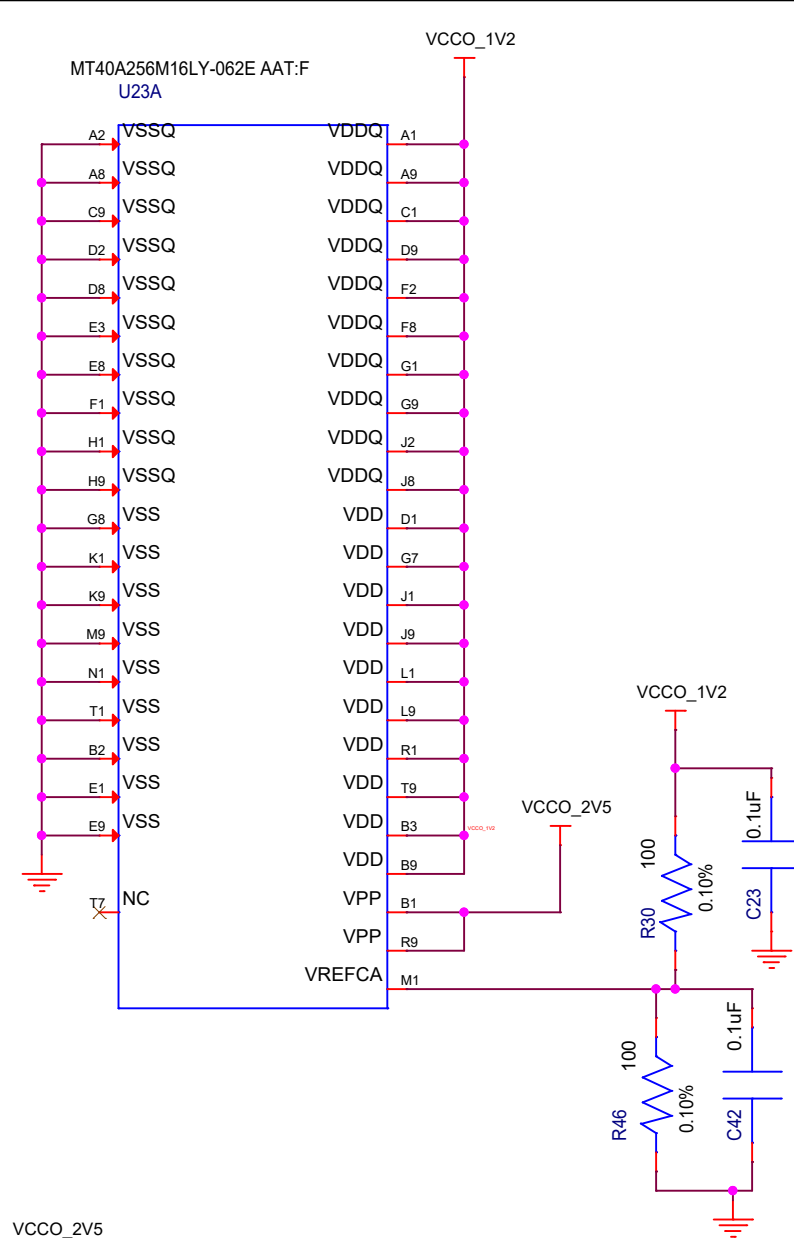
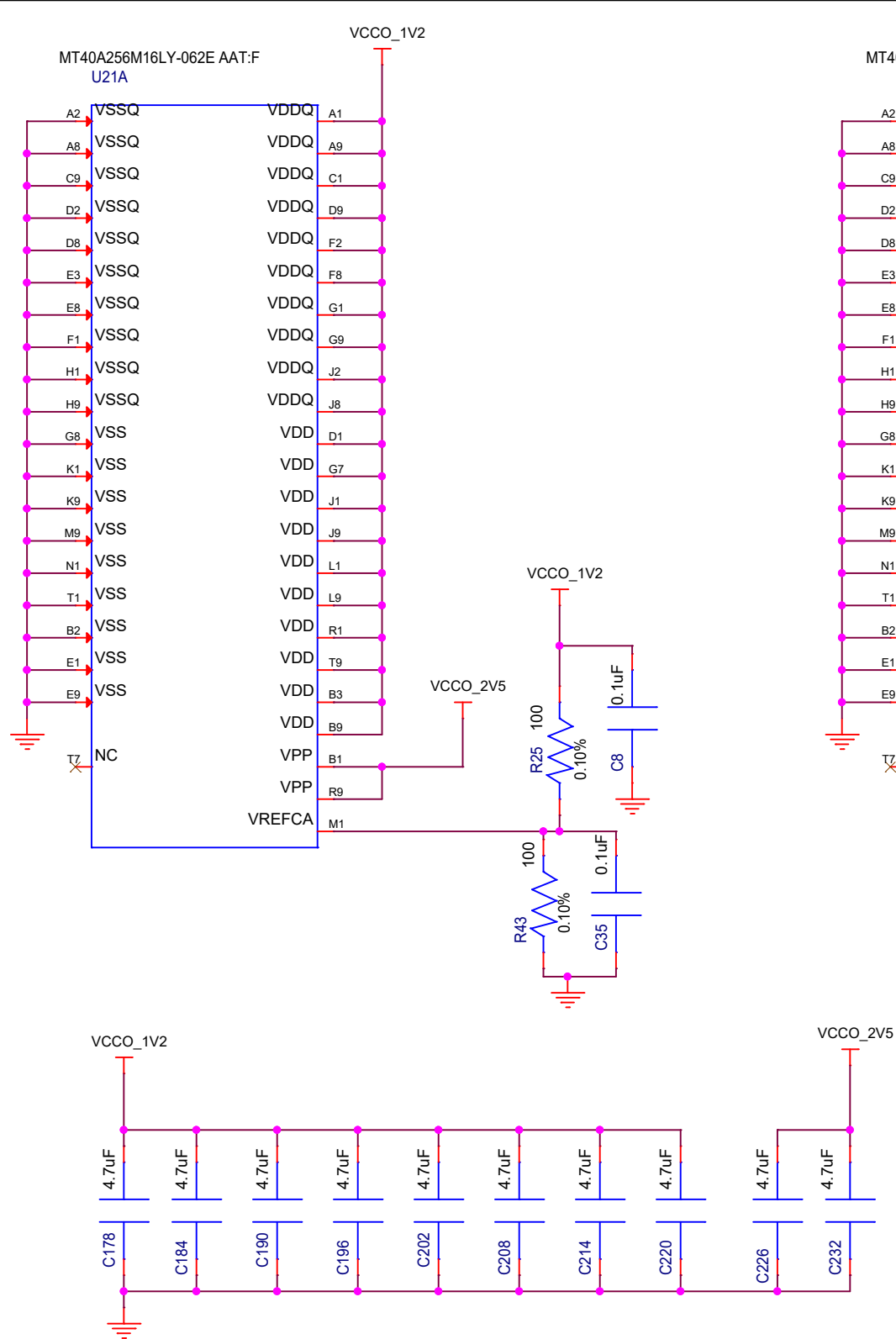
DDR1



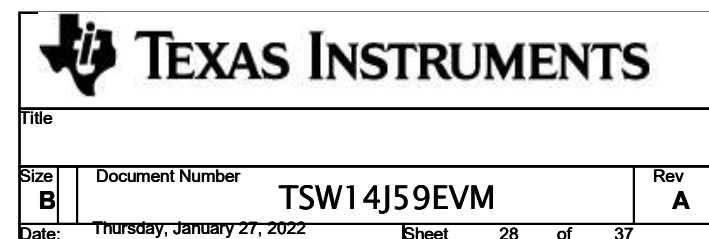


DDR PWR

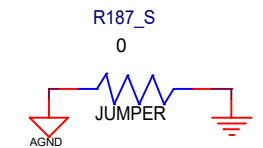
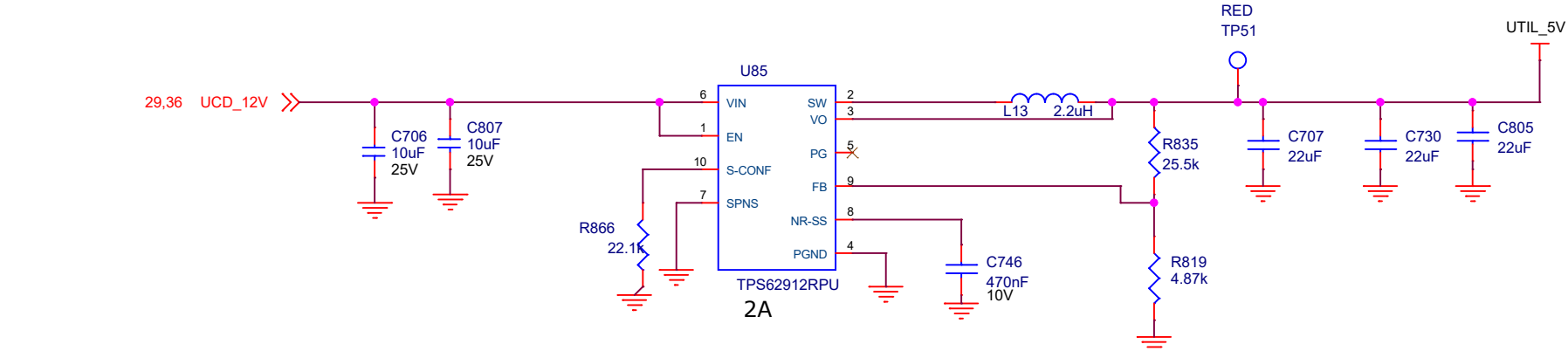
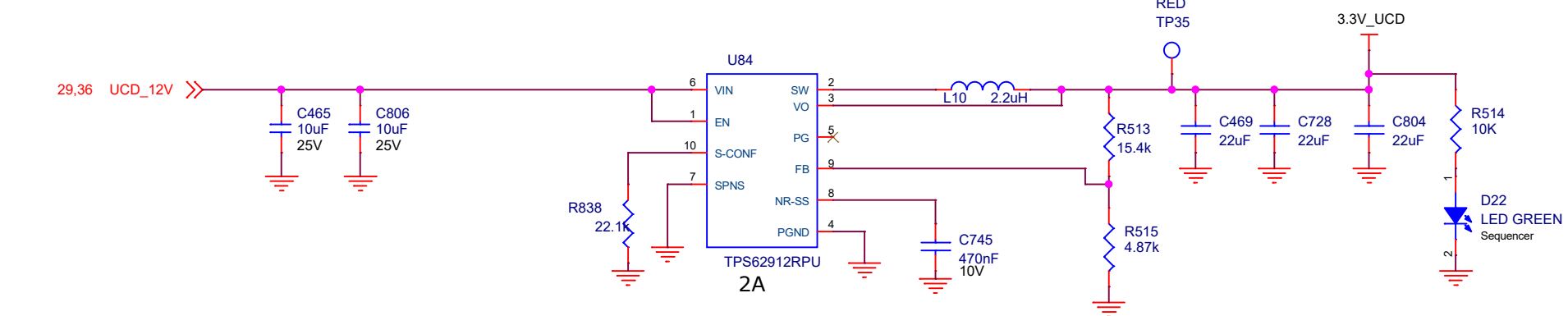
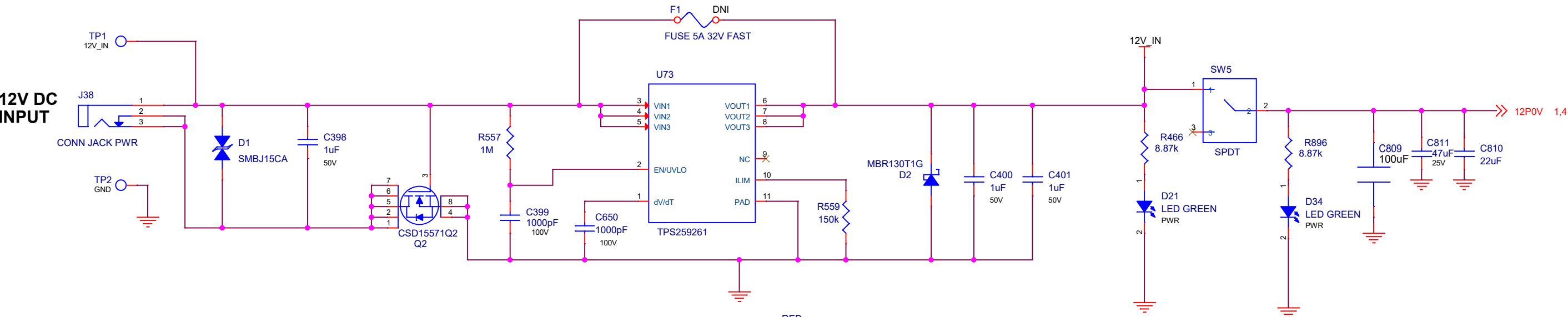





DDR PWR



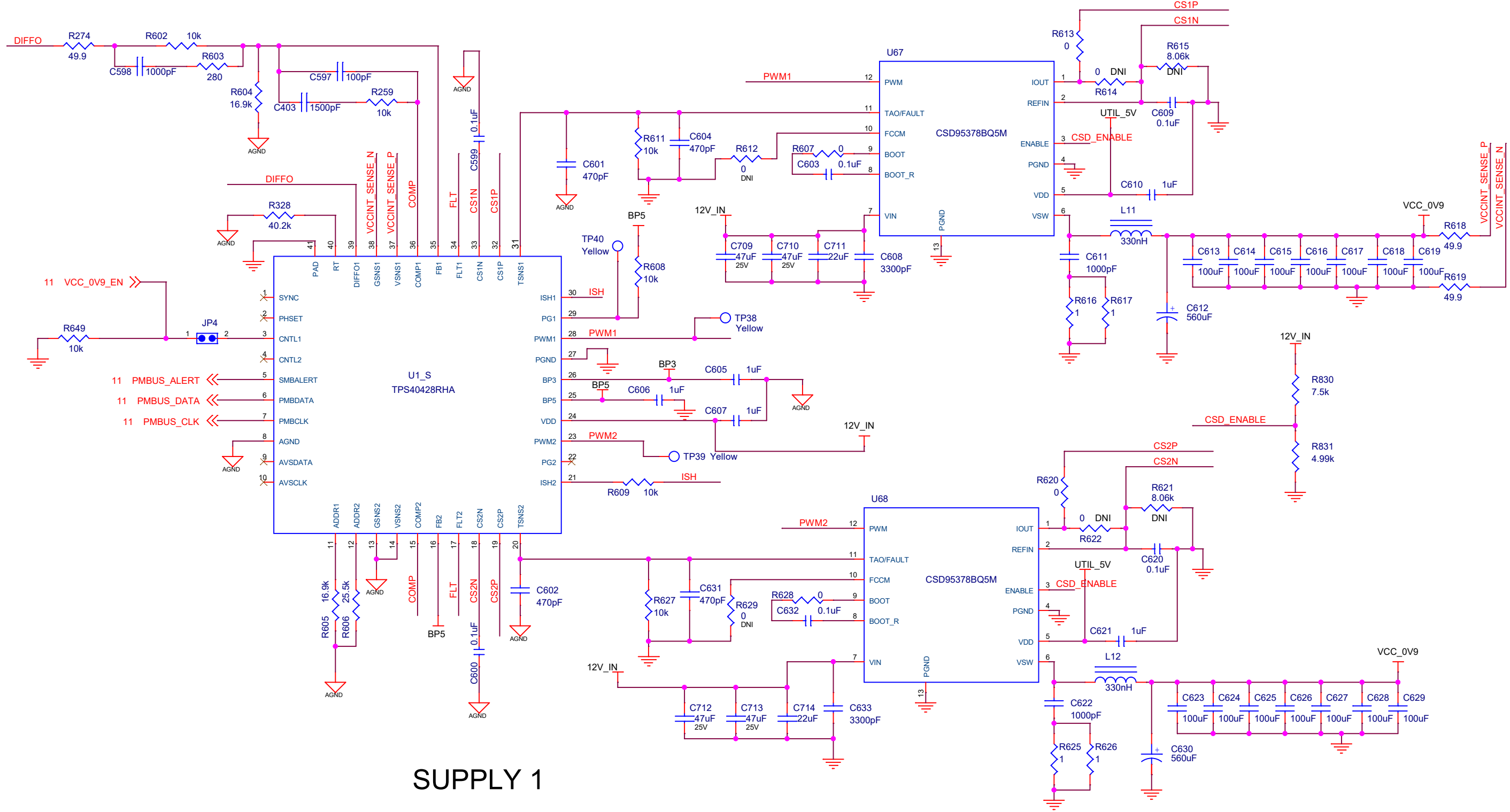
12V DC INPUT



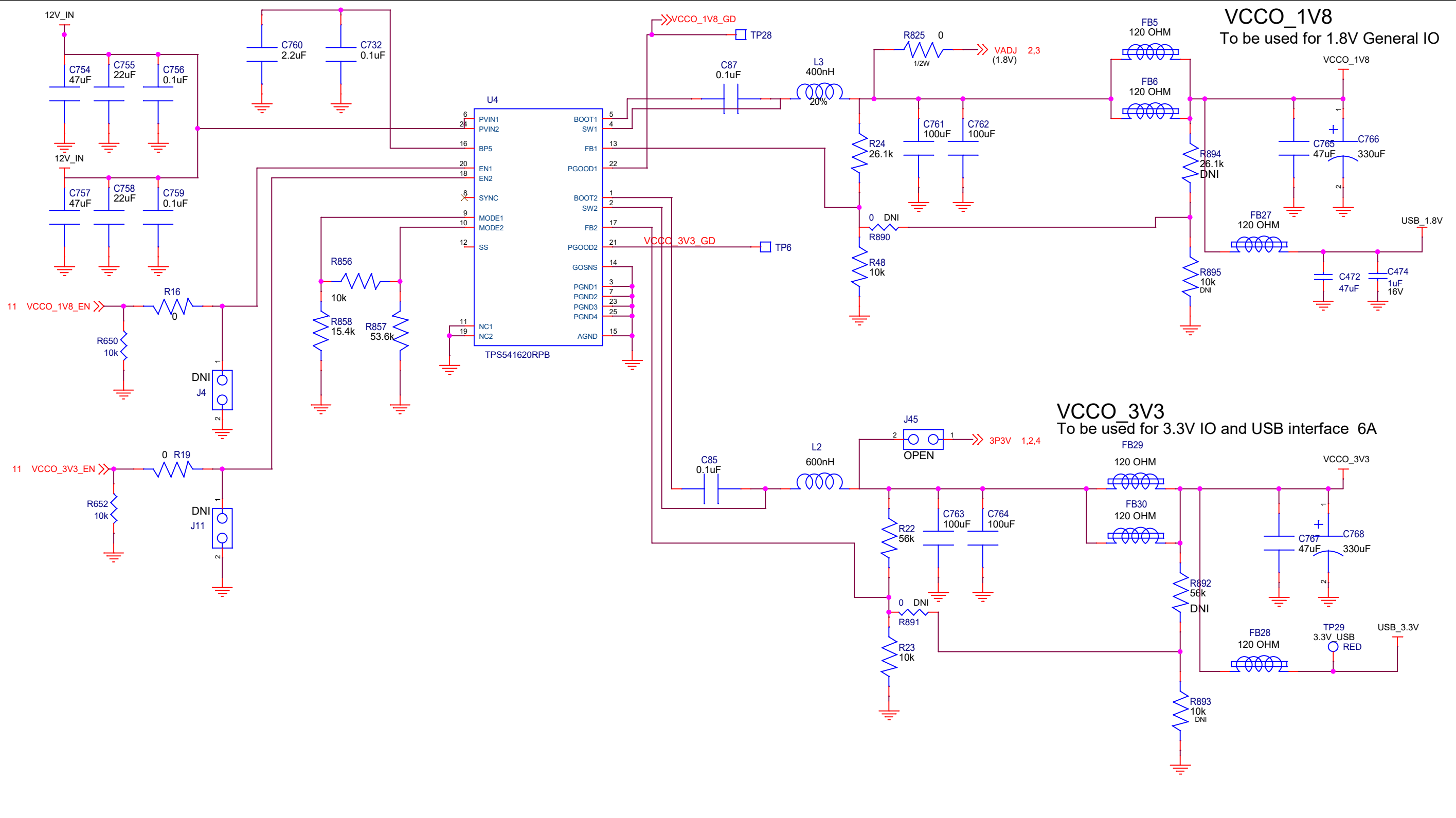
FPGA SUPPLY

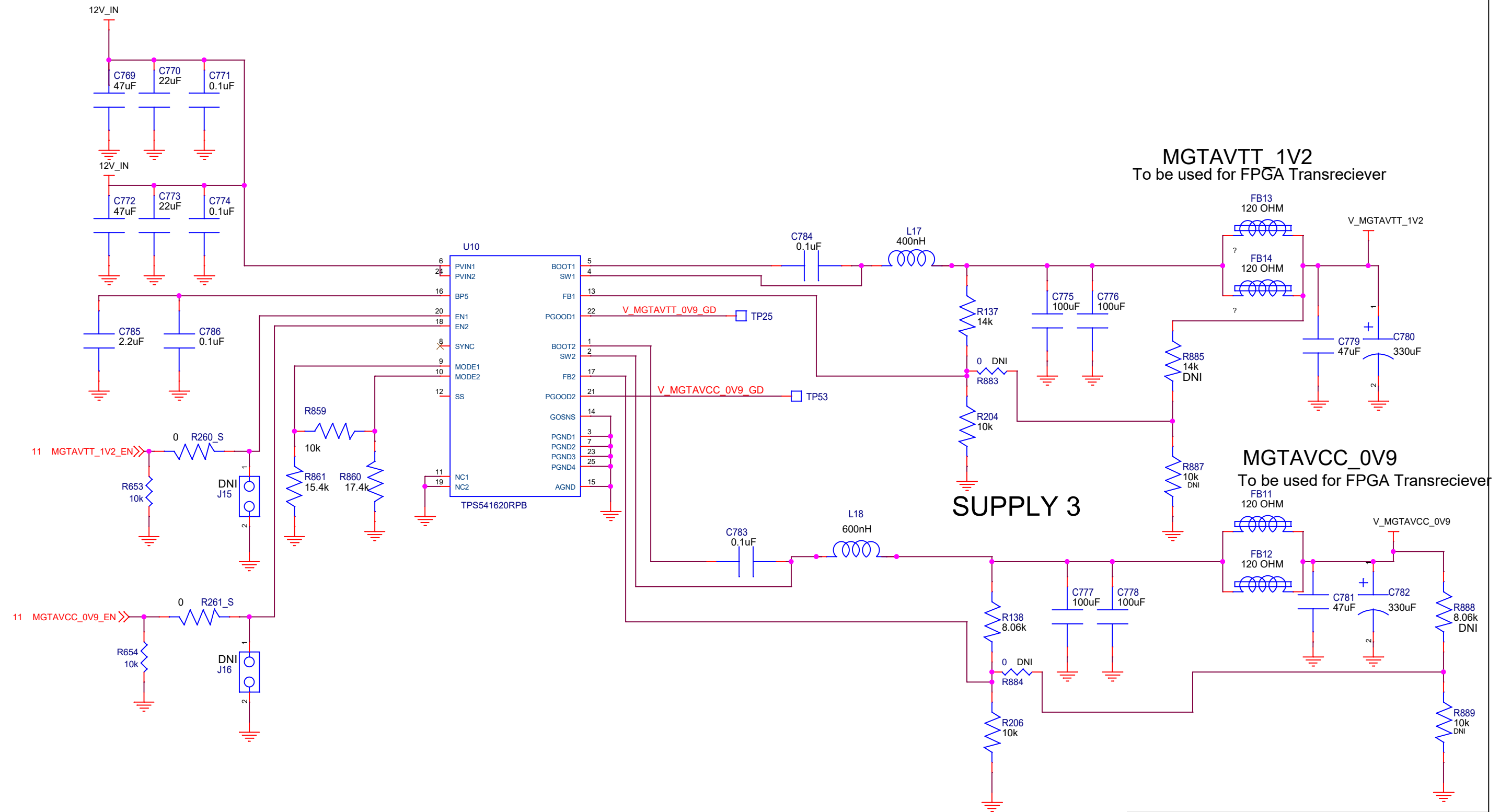
 **TEXAS INSTRUMENTS**

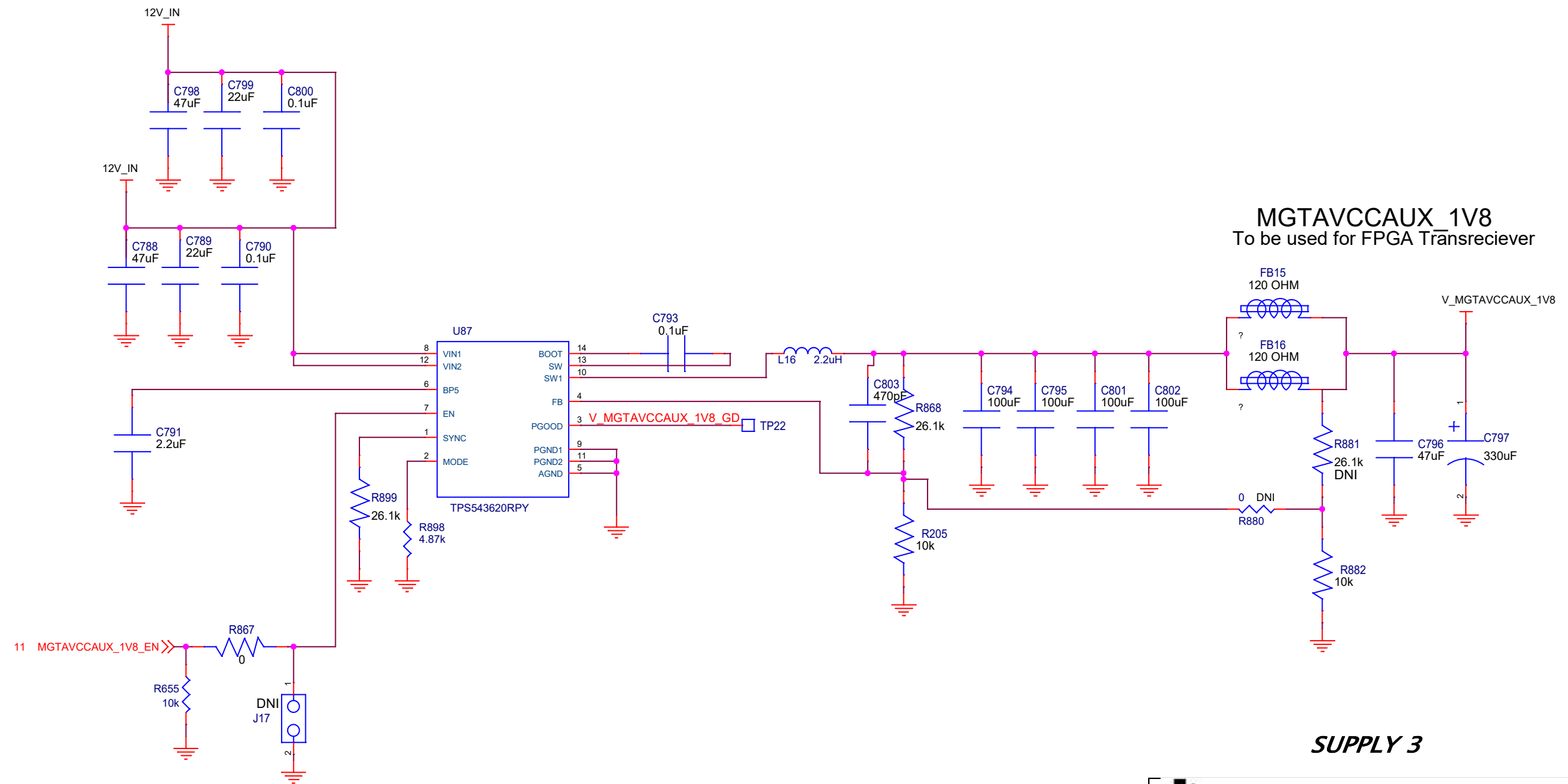
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|-----------------------------------|------------------------------------|--------------|
| Title | | |
| Size B | Document Number TSW14J59EVM | Rev A |
| Date: Tuesday, September 06, 2022 | Sheet 29 of 37 | |

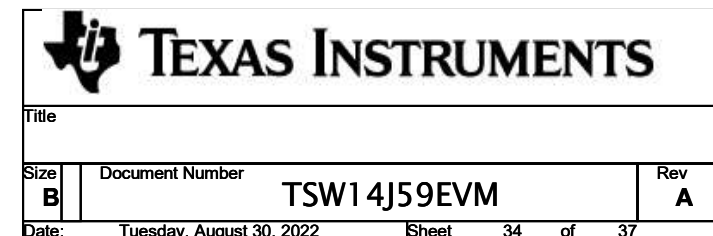


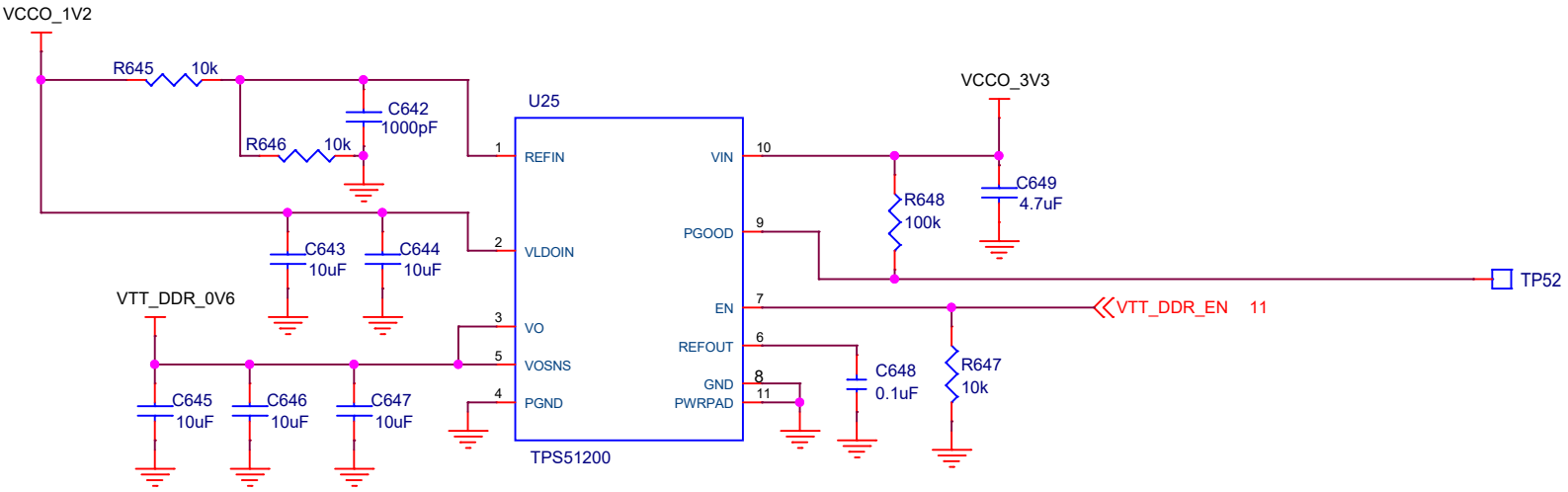
SUPPLY 1











SUPPLY 4

BARE BOARD, TSW14J58



FAN SINK, 27mm x 27mm
F127



TSW14J58EVM



STANDOFF ALUM HEX 2-56 x 3/4"



SCREW PANHEAD 2-56 x 1/4"



STANDOFF ALUM HEX 2-56 x 3/4"



SCREW PANHEAD 2-56 x 1/4"



STANDOFF ALUM HEX 2-56 x 3/4"



SCREW PANHEAD 2-56 x 1/4"



STANDOFF ALUM HEX 2-56 x 3/4"



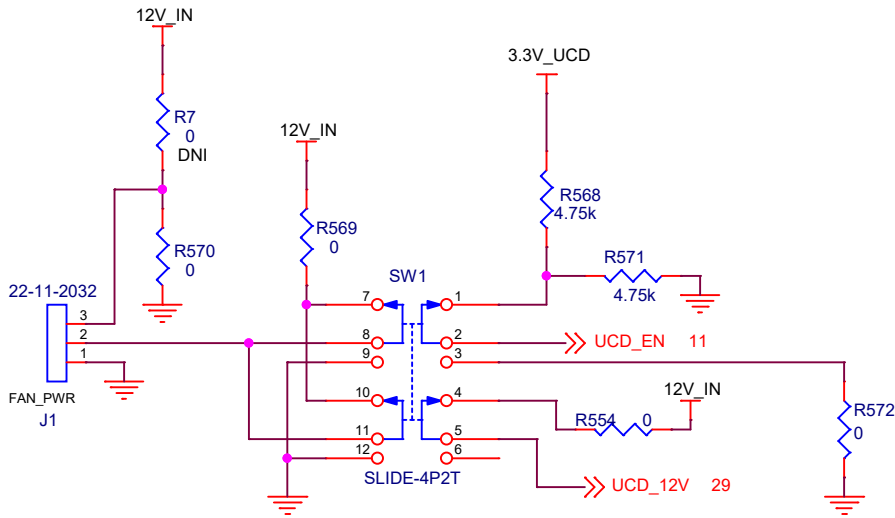
SCREW PANHEAD 2-56 x 1/4"



STANDOFF ALUM HEX 2-56 x 3/4"



SCREW PANHEAD 2-56 x 1/4"



Hardware

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| | | |
| Title | | |
| Size B | Document Number TSW14J59EVM | Rev A |
| Date: | Thursday, January 27, 2022 | Sheet 36 of 37 |

