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Revisions

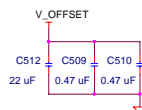
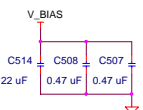
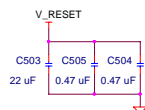
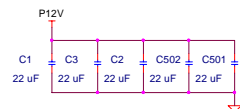
Rev A
Production Release

COMPUTER GENERATED DRAWING - DO NOT REVISE MANUALLY			
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	Production Release	02/24/2021	

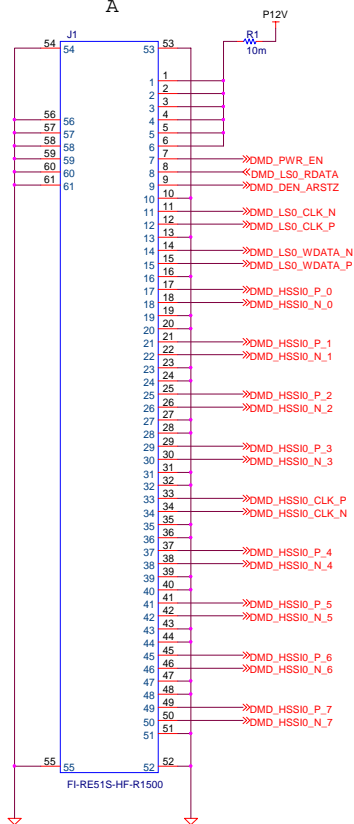
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		ENGR	O. Guevara		02/24/2021					
		SYST	A. Dewa		02/24/2021					
		PRJ	O. Guevara		02/24/2021					
		QA	---		---					
NEXT ASSY	USED ON					TITLE				
						DLP471TE and DLP650TE EVMs				
						A3	DRAWING NO	DLP057	REV	A
APPLICATION		SW	Cadence			SCALE		SHEET 1 of 6		
		Capture V.17.2								

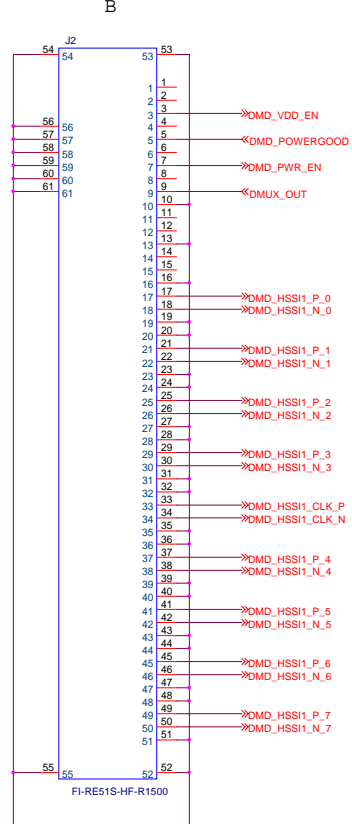
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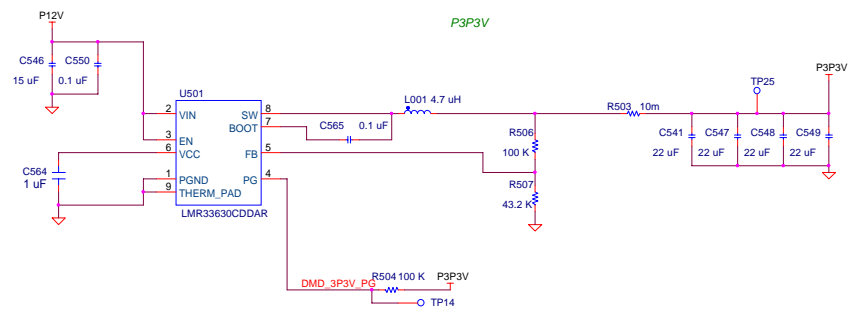
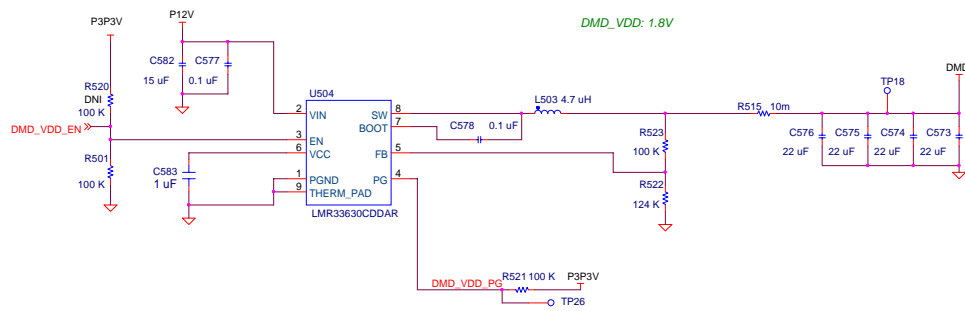


A

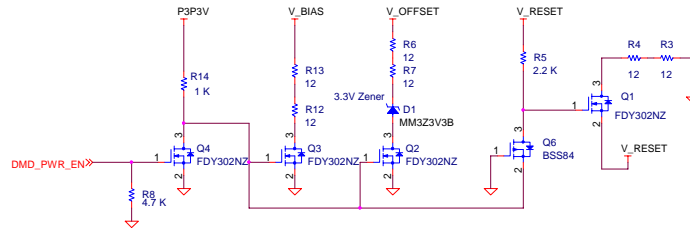


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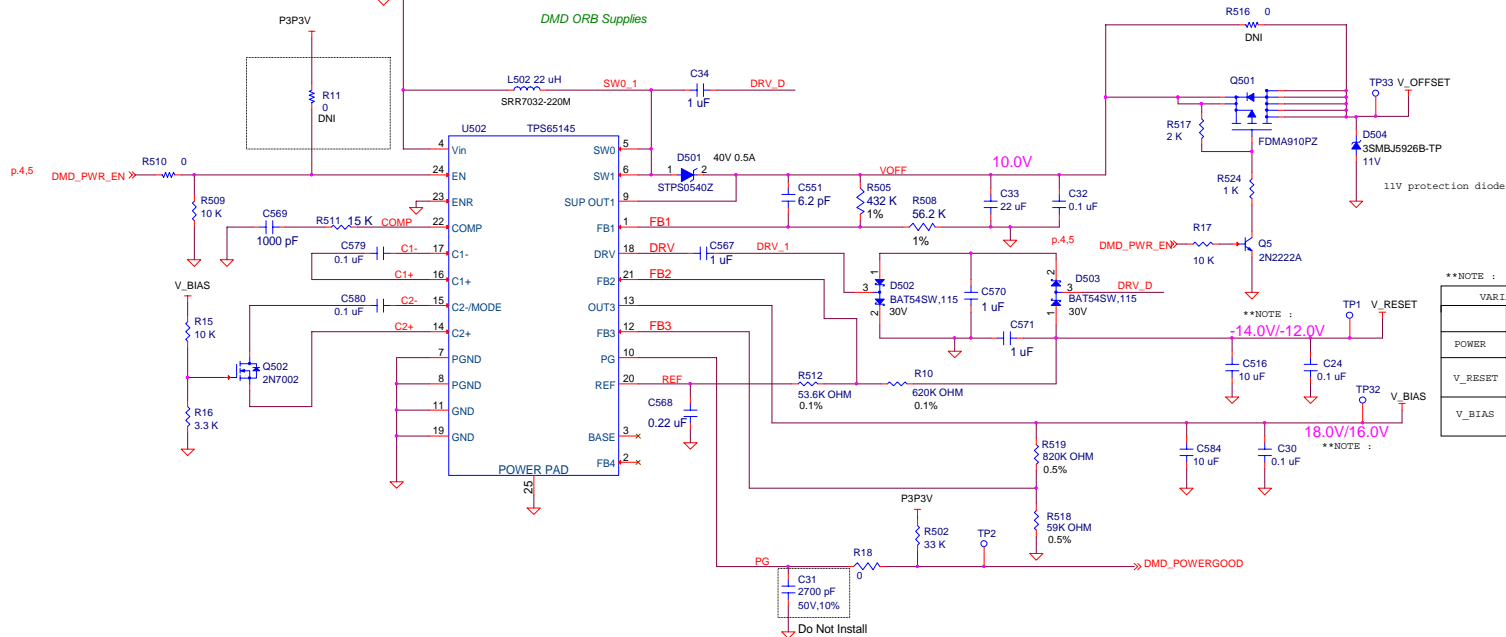




DMD ORB Supply Power Off Drains



DMD ORB Supplies



**NOTE :

VARIANT VOLTAGE DIFFERENCES		
	DLP057-001	DLP057-002
POWER	DLP471TEBVM	DLP650TEBVM
V_RESET	-14.0V	-12.0V
V_BIAS	18.0V	16.0V

**NOTE :

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TEXAS INSTRUMENTS

DWN M. Vega	DATE 02/24/21	A3	DRAWING NO DLP057	REV A
ISSUE DATE 02/24/21		SCALE	SHEET 5 OF 6	

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A --- Production Release

PCB LOGO:
Texas Instruments



PCB LOGO:
WEEE and CE



PCB LOGO:
FCC disclaimer

*For evaluation only; not
FCC approved for resale*

LBL1
PCB LABEL
THT-1-423-10
LABEL 0.25"HX0.75"W WH

VARIANT LABEL - LBL1	
DLP057-001	DLP471TEEV
DLP057-002	DLP650TEEV

Z_BOARD1
Printed Circuit Board, DLP057

Z_MECH2
CABLE ASSY HI SPEED 51POS 20CM
JF08R0R051020UA

Z_MECH3
CABLE ASSY HI SPEED 51POS 20CM
JF08R0R051020UA

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