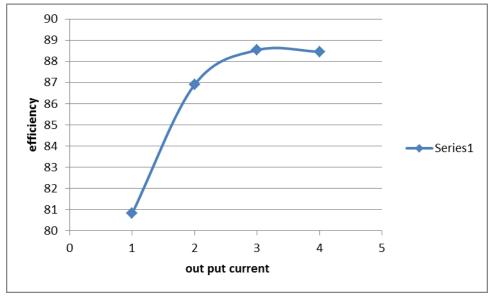
1 Photo



2 EFFICIENCY

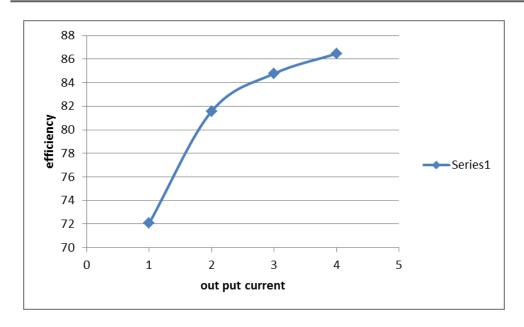
Since measuring AC to DC efficiency tends to be not as accurate, DC to DC efficiency was also measured. The DC was applied to the AC input and includes losses in the AC bridge rectifier.

Vin I	DC	lin	Pin	vout	lout		efficiency	Pout
	120	0.366	43.92	12.97		3	88.5929	38.91
	150	0.293	43.95	12.97		3	88.53242	38.91
	200	0.221	44.2	12.97		3	88.03167	38.91
	300	0.152	45.6	12.97		3	85.32895	38.91
	350	0.134	46.9	12.97		3	82.96375	38.91
	150	0.107	16.05	12.97		1	80.80997	12.97
	150	0.199	29.85	12.97		2	86.90117	25.94
	150	0.293	43.95	12.97		3	88.53242	38.91
	150	0.391	58.65	12.97		4	88.45695	51.88
	300	0.06	18	12.97		1	72.05556	12.97
	300	0.106	31.8	12.97		2	81.57233	25.94
	300	0.153	45.9	12.97		3	84.77124	38.91
	300	0.2	60	12.97		4	86.46667	51.88



With 150 VDC input

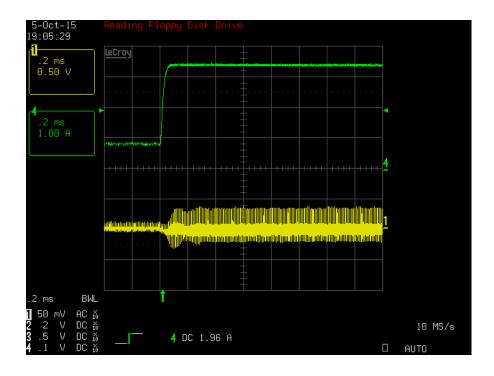
PMP11329 Test Results



With 300VDC input

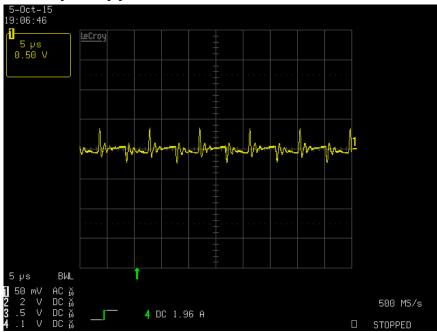
	Pin	Vout		Efficiency	Pout
120vac	29.75	12.97	2	87.19328	25.94
	43.92	12.97	3	88.5929	38.91
	58.14	12.97	4	89.23289	51.88
240vac					
	32.22	12.97	2	80.509	25.94
	45.8	12.97	3	84.95633	38.91
	60.04	12.97	4	86.40906	51.88

3 Load step response



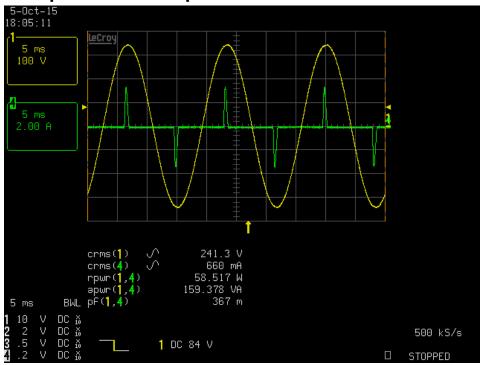
Output current (green) load step and output voltage response (yellow)

4 Output ripple

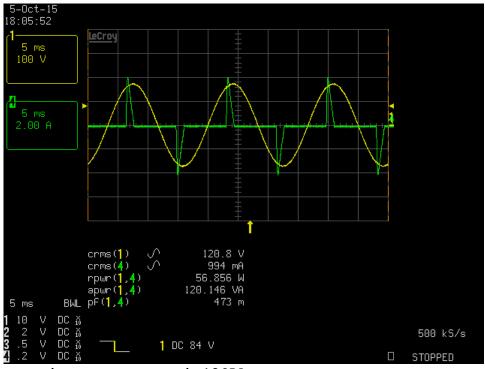


At 4 amp load

5 Input line at 4 amp load



Green is current, orange is 240Vac



green is current, orange is 120Vac

6 Thermal image

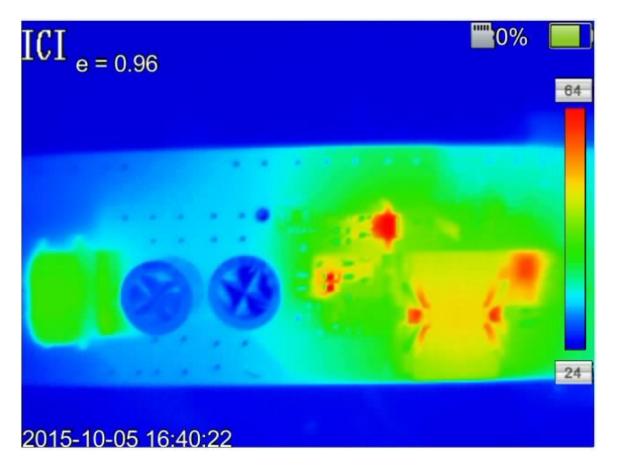
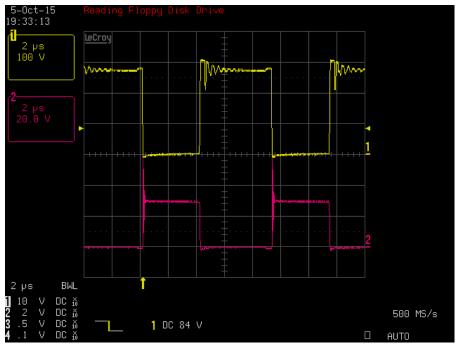
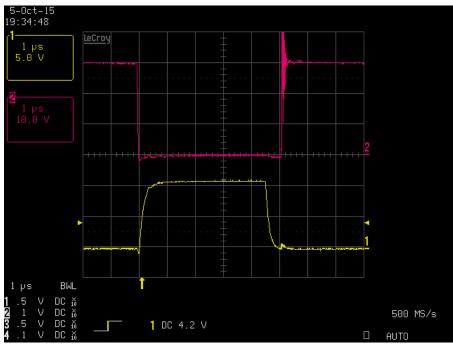


Image taken at full load in still air at 24 deg C. the hottest part on the board is about 65 deg C $\,$

7 Dain wave forms.



Yellow is the primary drain and the red is the secondary drain with 150 vdc input.



Red is the secondary FET drain and yellow is the synchronous rectifier gate

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