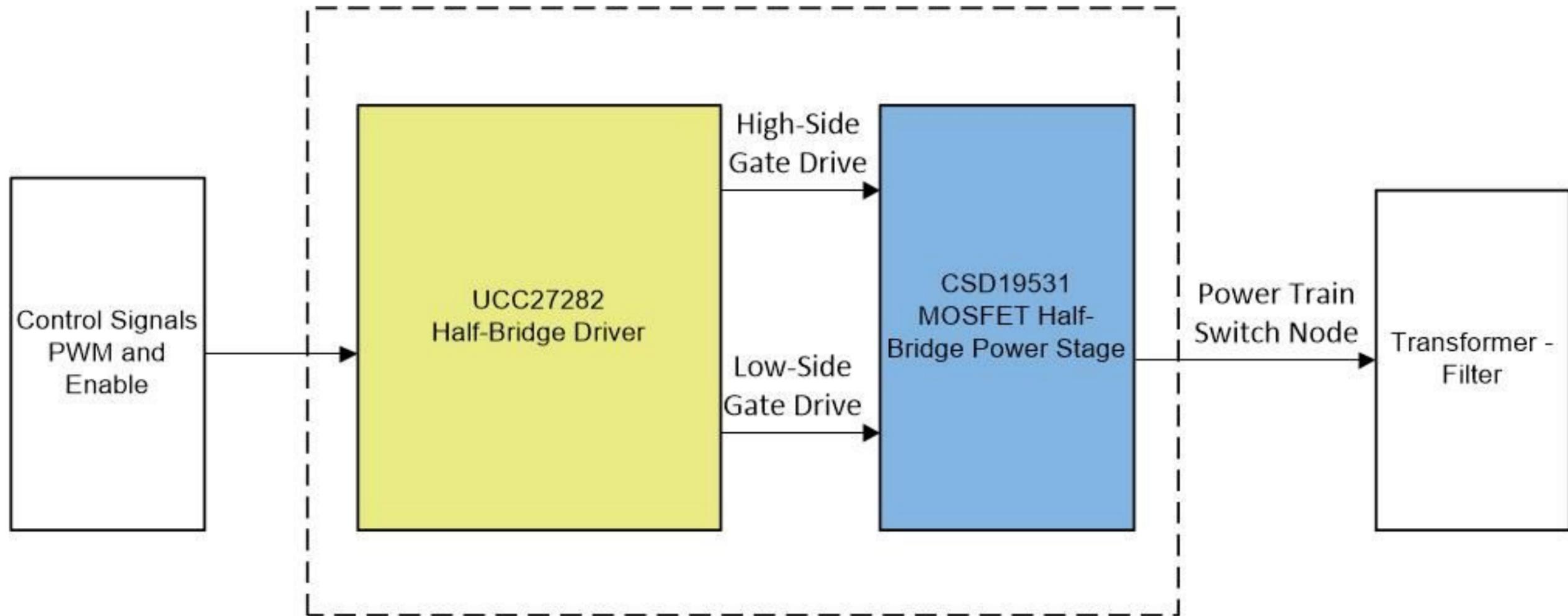
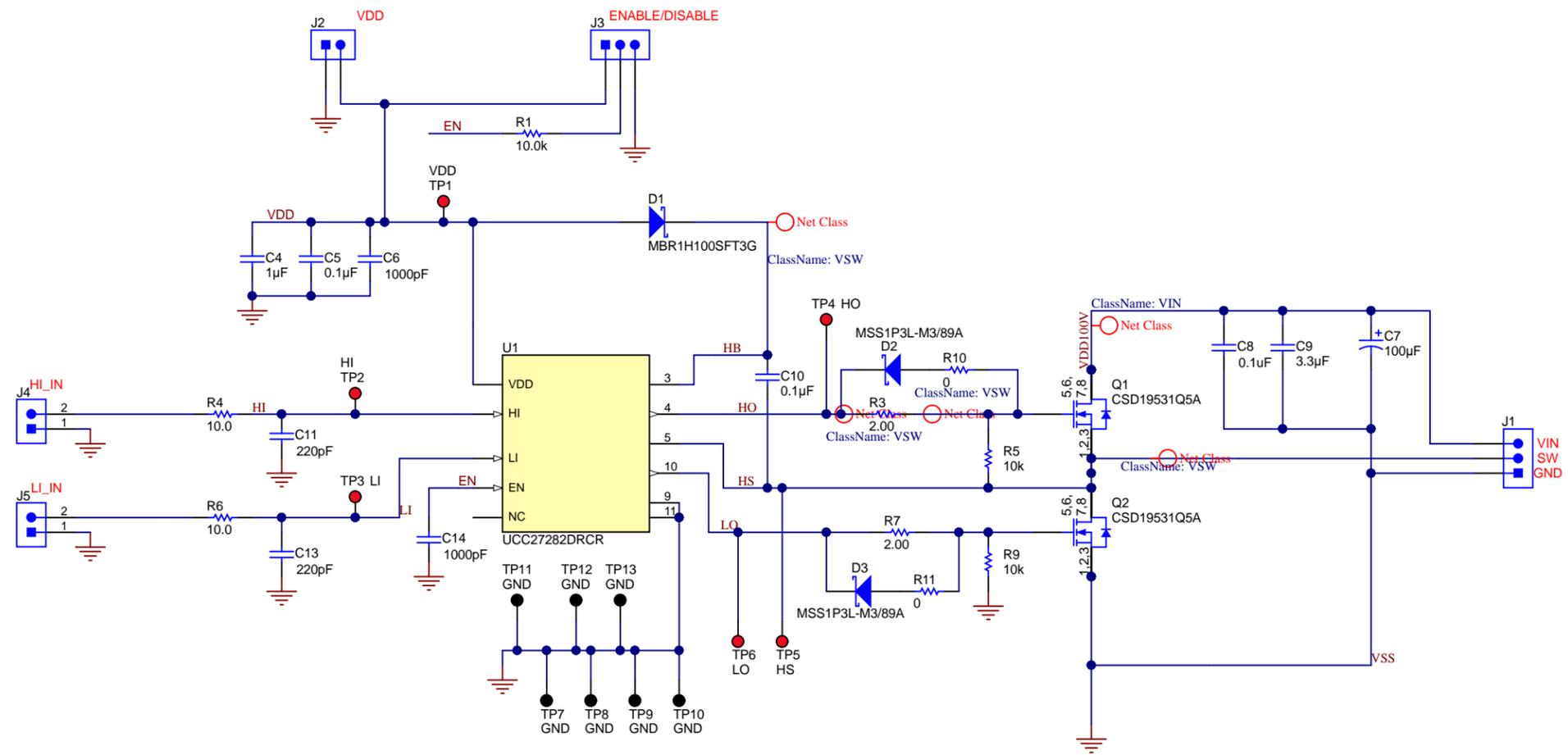


Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A



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Orderable: N/A	Designed for: Public Release	Mod. Date: 4/18/2019
TID #: TIDA-050022	Project Title: Power Stage for <100VIN DC/DC Converters	
Number: TIDA-050022	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 1 of 3
Drawn By: Richard Herring	File: TIDA-050022 CoverSheet_SchDoc	Size: B
Engineer: Richard Herring	Contact: http://www.ti.com/support	



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Orderable: N/A	Designed for: Public Release	Mod. Date: 4/9/2019
TID #: TIDA-050022	Project Title: Power Stage for <100VIN DC/DC Converters	
Number: TIDA-050022	Rev: A	Sheet Title: TIDA050022
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 1 of 1
Drawn By: Richard Herring	File: TIDA-050022.SchDoc	Size: B
Engineer: Richard Herring	Contact: http://www.ti.com/support	



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FCC disclaimer

Logo4
PCB LOGO
WEEE logo



CAUTION HOT SURFACE

PCB Number: TIDA-050022
PCB Rev: A



LOGO5



LOGO6



LOGO7



LOGO8



LOGO9

Label Table	
Variant	Label Text
001	

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ5
Assembly Note
Place jumper SH-J3 across pin 1 and pin 2 of connector J3

Orderable: N/A	Designed for: Public Release	Mod. Date: 4/18/2019
TID #: TIDA-050022	Project Title: Power Stage for <100VIN DC/DC Converters	
Number: TIDA-050022 Rev: A	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 3 of 3
Drawn By:	File: TIDA-050022_Hardware.SchDoc	Size: B
Engineer: Richard Herring	Contact: http://www.ti.com/support	

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