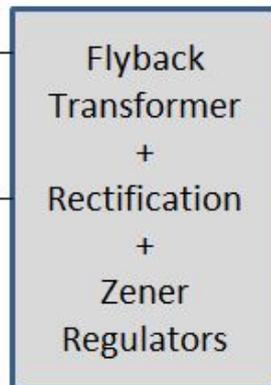


Input
(24V ± 5%)



→ VCC_U_I
→ VEE_U_I

→ VCC_V_I
→ VEE_V_I

→ VCC_W_I
→ VEE_W_I

→ VCC_B
→ VEE_B

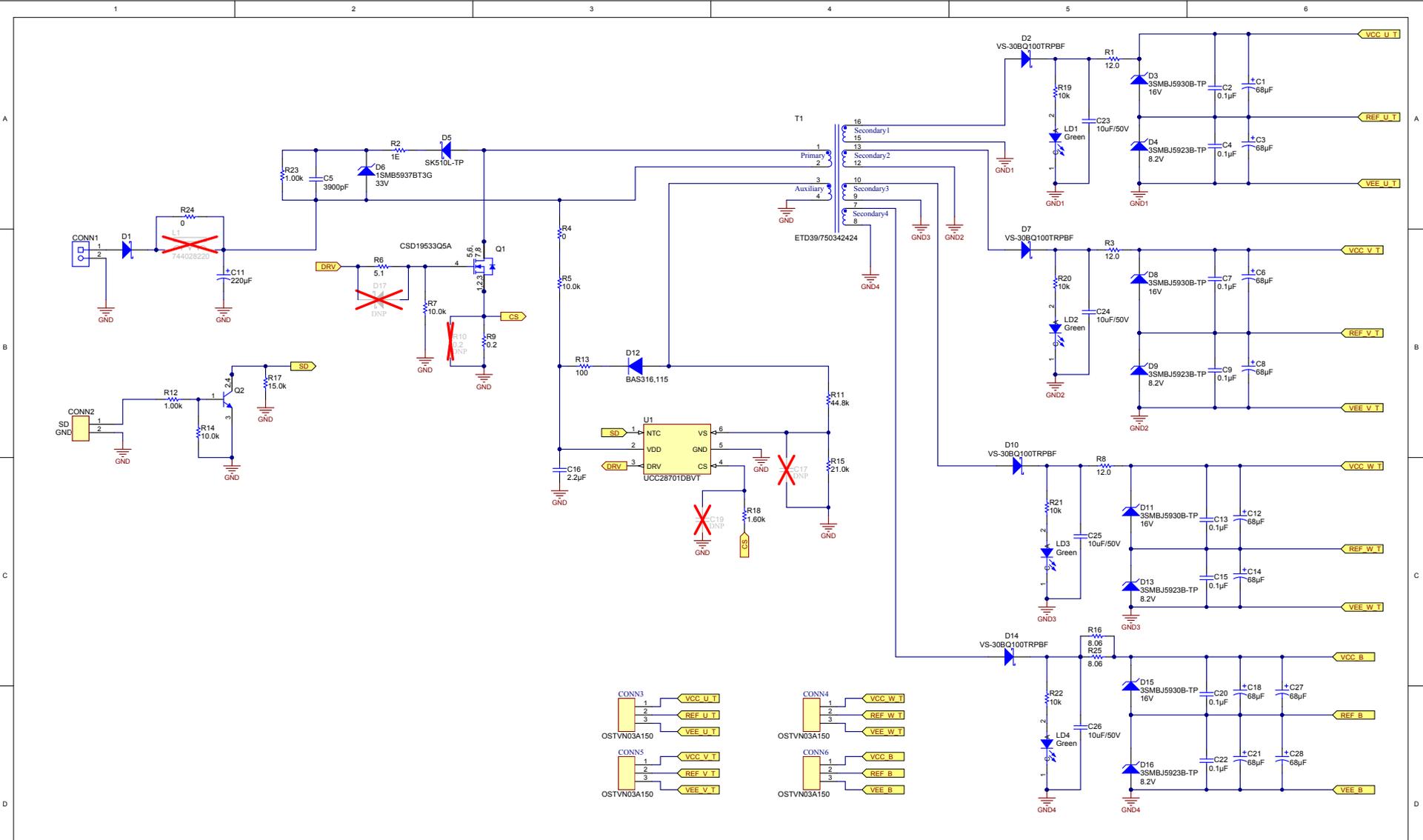
For
TOP
IGBTs

For
BOTTOM
IGBTs

Shutdown
Signal

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Number: TIDA-00182	Rev: E2	Designed for: Public Release	Mod. Date: 6/30/2014
SVN Rev.: Not in version control		Project Title: 8-Output Gate-drive power supply	
Drawn By: Engineer: Sanjay Pithadia		Sheet Title:	
		Assembly Variant: 001	Sheet 1 of 3
		File: TIDA-00182-Cover_SchDoc	Size: B
		Contact: http://www.ti.com/support	http://www.ti.com



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Number: TIDA-00182	Rev: E2	Designed for: Public Release	Mod. Date: 7/1/2014
SVM Rev: Not in version control		Project Title: 8-Output Gate-drive power supply	
Drawn By: Sanjay Pithadia		Sheet Title: Main	
Engineer: Sanjay Pithadia		Assembly Variant: 001	
File: TIDA-00182-Main.SchDoc		Sheet 2 of 3	
Contact: http://www.ti.com/support		Size: B	
		http://www.ti.com	
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H1 NY PMS 440 0025 PH H2 NY PMS 440 0025 PH H3 NY PMS 440 0025 PH H4 NY PMS 440 0025 PH

H5 1902C H6 1902C H7 1902C H8 1902C

~~H9~~ ~~H10~~ ~~H11~~

PCB Number: TIDA-00182
PCB Rev: E2

PCB
LOGO
Texas Instruments

Label Table

Variant	Label Text
001	ChangeMe!
002	ChangeMe!

LBL1
PCB Label
Size: 0.65" x 0.20"

Z21
Label Assembly Note
This Assembly Note is for PCB labels only

Z22
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

Z23
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

Z24
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Decided for: Public Release		Mod. Date: 9/11/2014	
Project Title: 8-Output Gate-drive power supply			
Number: TIDA-00182	Rev: E2	Sheet Title:	
SVN Rev.: Not in version control		Assembly Variant: 001	Sheet: 3 of 3
Drawn By:	Engineer: Sanjay Pithadia	File: Hardware_ANSI-B_SchDoc	Size: B
Contact: http://www.ti.com/support		http://www.ti.com	

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