

eFuses: Clamping and Cutoff and Auto Retry, Oh My! – Part 2/3



Alexander Gronbach

If you missed [part 1 of this blog series](#), be sure to check out the different options available with eFuses for handling overvoltage events (output-voltage clamping vs. output-voltage cutoff). In this installment, I'll focus on eFuse options for overcurrent protection (current limiting vs. circuit breaking). Continuing our journey down the Yellow Brick Road, let's once again start by delving into the more common option: current limiting.

The Benefits of Current Limiting

When an overcurrent event occurs, an eFuse such as the [TPS25940](#) will limit the output current to a threshold set by an external resistor. In [Figure 1](#), when the eFuse sees 4A (assuming a current limit [I_{LIM}] of 3A), it will proceed to limit the output current to 3A. The scope shot in [Figure 2](#) shows this response, with $I_{LIM} = 3.6A$. The eFuse will current limit until either the overcurrent event is removed ($I_{IN} < I_{LIM}$) or until the eFuse reaches thermal shutdown (typically $T_J = 150^{\circ}C$). Once an eFuse enters thermal shutdown, it will enter one of two modes: either latch off or auto retry (both of which I'll discuss in the third installment of this series).

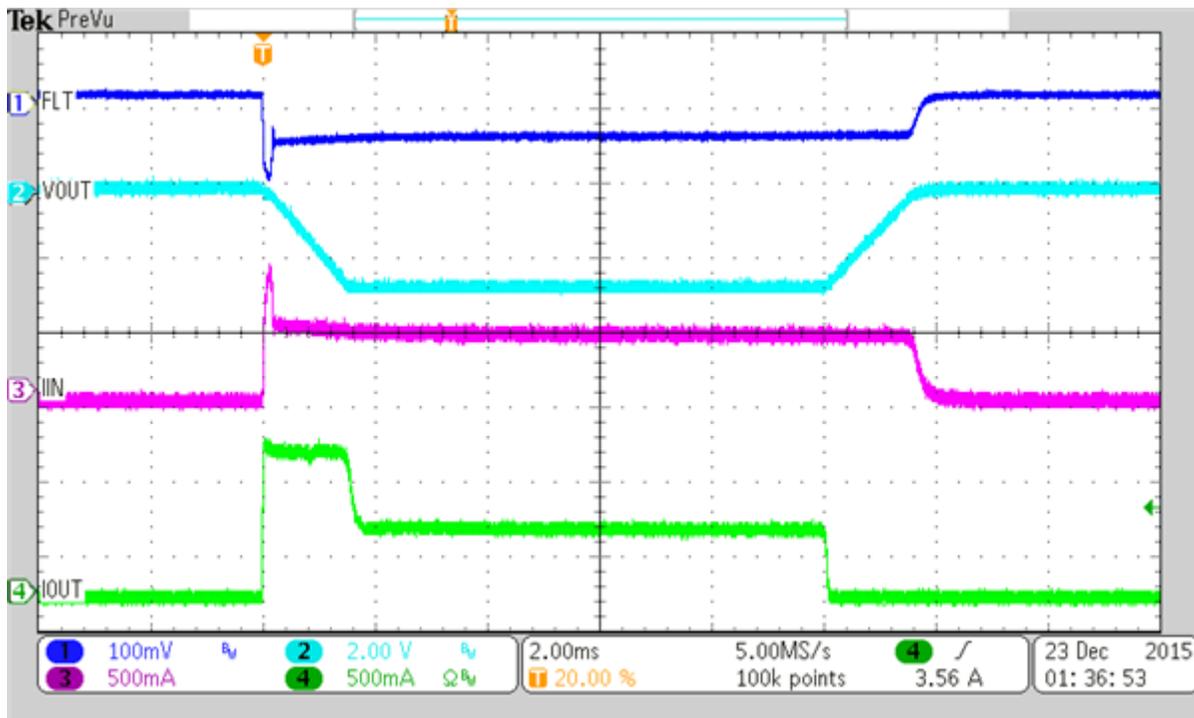


Figure 1. TPS25940 Current Limiting 4A Down to 3.6A

Current limiting shares similar benefits to that of output-voltage clamping, in that the eFuse allows the system to not see the overcurrent event, protecting all downstream circuitry from the higher current. It will also report the fault, allowing the system to prepare for imminent shutdown and perform “last-gasp” functionality. This can be beneficial in applications such as [Solid State Drives \(SSDs\)](#), especially if the overcurrent event is temporary. However, applications where safety is more important than uptime can benefit from an eFuse with circuit breaking instead of current limiting.

The Benefits of Circuit Breaking

An eFuse with circuit breaking acts as its name suggests; it breaks the circuit in response to an overcurrent event. Looking again at [Figure 1](#), if I_{IN} suddenly became 4A, an eFuse with circuit-breaker functionality (such as the [TPS25944A](#) or [TPS25944L](#)) would open the circuit. This means that I_{OUT} would be 0A, and all of the downstream circuitry would be protected from the higher current, as shown in [Figure 2](#).

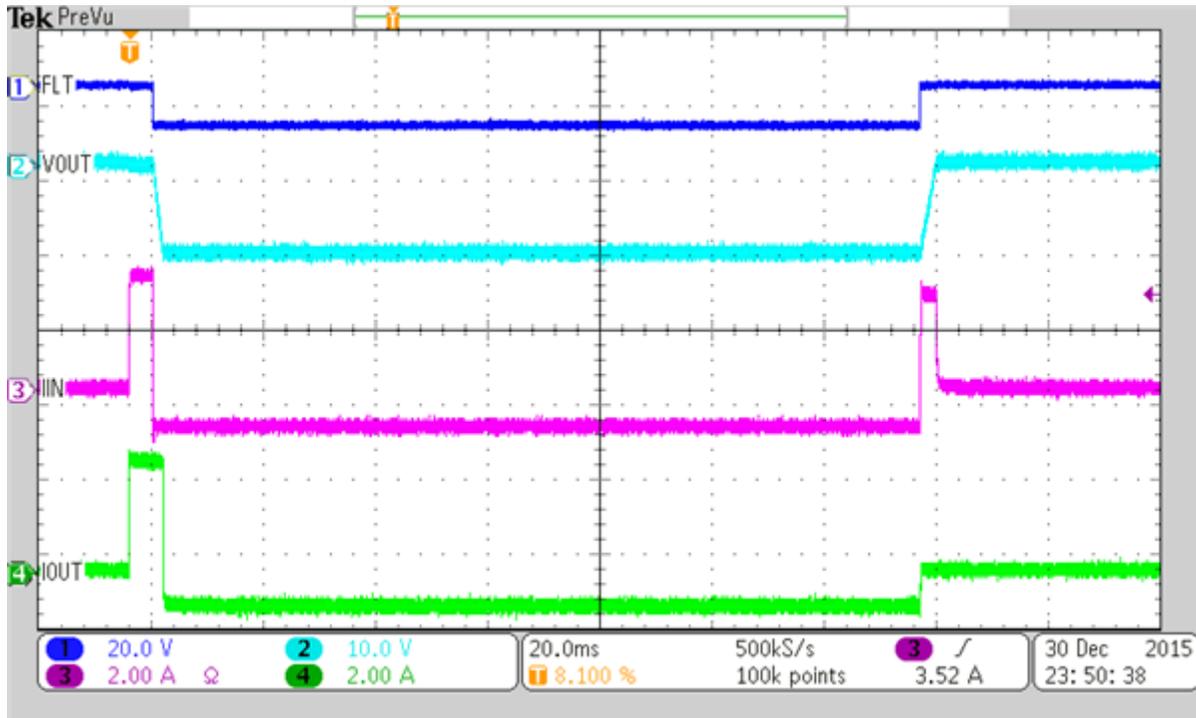


Figure 2. TPS25944A Circuit-breaker Functionality ($I_{IN} = 4A$, $I_{OUT} = 0A$)

The benefit of this functionality is similar to the benefit of an eFuse with output-voltage cutoff; it does not allow the higher current to affect any downstream circuitry. This form of protection comes at the cost of the downstream circuitry losing power, but that can actually be a good thing in an application like the inside of a data center server rack.

Imagine that the [TPS25944A](#) is protecting the input power to the hard drive, and the backplane connector shorts. If the hard drive has a current-limiting eFuse, it will continue to draw 3A through the backplane connector until the eFuse reaches thermal shutdown. This current draw through a faulty connector could cause the connector to overheat and begin to smoke or catch fire. If the hard drive contains sensitive data, it could be damaged in the resulting fire and the data could be lost.

There is no one-size-fits-all answer as to which type of overcurrent protection will be best for every application. However, now that you are familiar with what options are available, you should be able to make the best decision for your next design. Stay tuned for the [third](#) and final installment of this series, when I'll discuss what happens after an eFuse enters thermal shutdown.

Additional Resources

- Read [part 1](#) and [part 3](#) of this blog series.
- Check out [TI's eFuse portfolio](#).
- Check out these reference designs: [TI Designs Last Gasp Hold Up Energy Storage Solution reference design](#) and [TI Designs High-Efficiency Backup Power Supply Reference Design](#)

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2023, Texas Instruments Incorporated