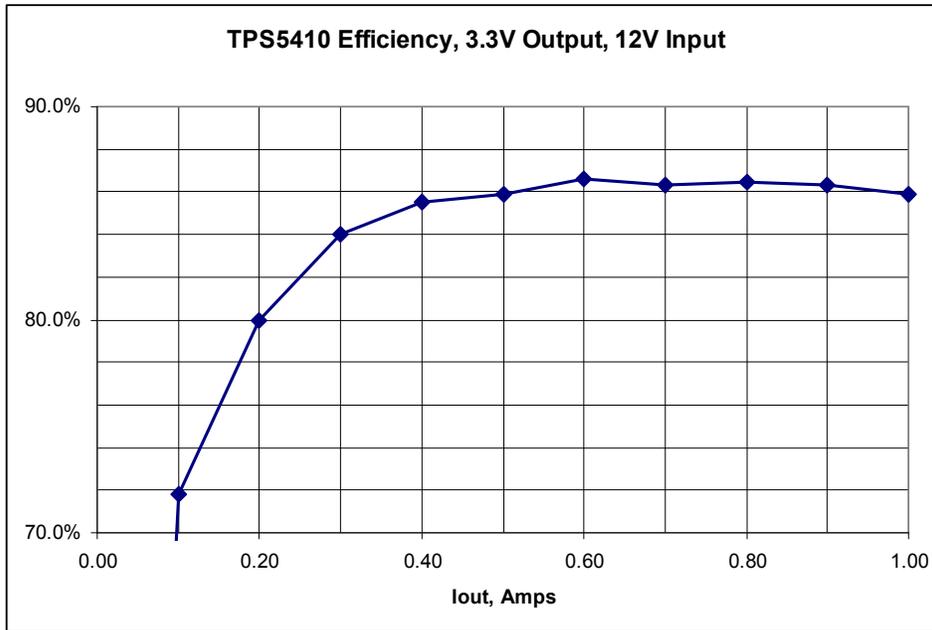


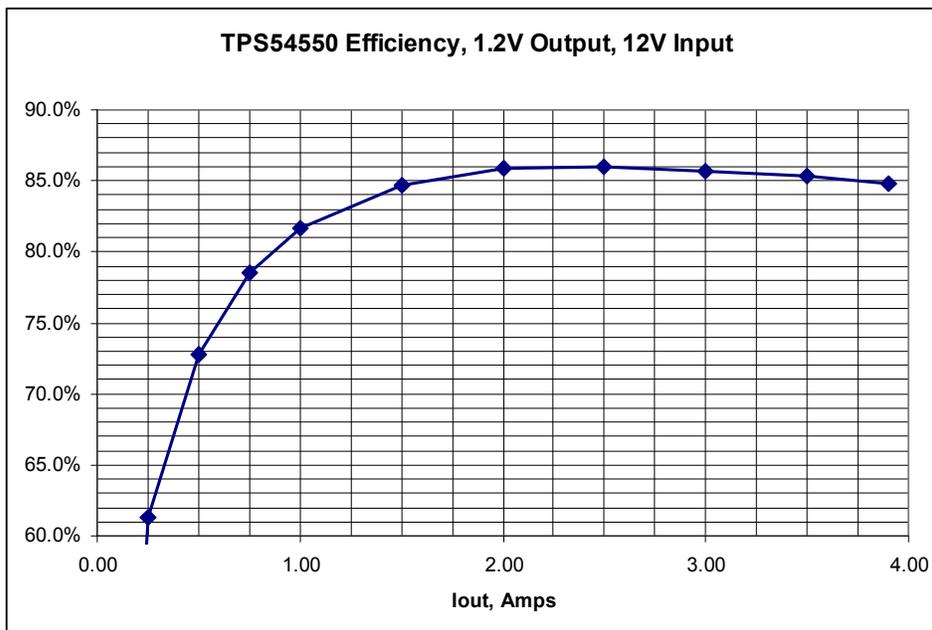
Filename: PMP3164.2_RevB_bom.xls						
Date: 03/24/2008						
PMP3164.2 REV B BOM						
COUNT	RefDes	Value	Description	Size	Part Number	MFR
3	C12, C16, C30	100pF	Capacitor, Ceramic, 50V, C0G, 5%	603	Std	Std
1	C100	220pF	Capacitor, Ceramic, 50V, C0G, 5%	603	Std	Std
1	C29	3300pF	Capacitor, Ceramic, 50V, X7R, 10%	603	Std	Std
3	C2, C15, C22	0.01uF	Capacitor, Ceramic, 50V, X7R, 10%	603	Std	Std
1	C28	0.015uF	Capacitor, Ceramic, 50V, X7R, 10%	603	Std	Std
1	C27	0.047uF	Capacitor, Ceramic, 50V, X7R, 10%	603	Std	Std
1	C19	0.1uF	Capacitor, Ceramic, 50V, X7R, 10%	603	Std	Std
6	C5, C8, C13, C14, C17, C26	1uF	Capacitor, Ceramic, 10V, X5R, 20%	603	Std	Std
3	C9, C18, C25	10uF	Capacitor, Ceramic, 6.3V, X5R, 20%	603	Std	Std
0	C10	DNP				
3	C6, C20, C21	10uF	Capacitor, Ceramic, 16V, X5R, 20%	1206	C3216X5R1C106M	TDK
1	C4	100uF	Capacitor, POSCAP, 6.3V, 55milliohm, 20%	3528(B)	6TPB100MA	Sanyo
1	C11	68uF	Capacitor, POSCAP, 16V, 50milliohm, 20%	7343(D)	16TQC68M	Sanyo
2	C23, C24	330uF	Capacitor, POSCAP, 2V, 6milliohm, 20%	7343(D)	2TPF330M	Sanyo
1	D1		Diode, Schottky, 1A, 30V	SMA	MBRA130LT3	On Semi
6	J1, J2, J3, J4, J5, J6		Terminal Block, 2-pin, 6-A, 3.5mm	0.27 x 0.25	ED555/2DS	OST
1	L1	68uH	Inductor, SMT, 1.4A, 155milliohm	10x9.7mm	VLF10040T-680M1R4	TDK
1	L2	3.3uH	Inductor, SMT, 6.2A, 10.5milliohm	10x9.7mm	VLF10040T-3R3N6R2	TDK
1	Q1		MOSFET, N-ch, 30V, 11A, 11milliohm	SO8	Si4420BDY	Vishay
2	R17, R19	0	Resistor, Chip, 1/16W, 5%	603	Std	Std
1	R102	10	Resistor, Chip, 1/16W, 5%	603	Std	Std
1	R6	24	Resistor, Chip, 1/16W, 5%	603	Std	Std
2	R1, R14	49.9	Resistor, Chip, 1/16W, 1%	603	Std	Std
1	R12	549	Resistor, Chip, 1/16W, 1%	603	Std	Std
1	R8	1K	Resistor, Chip, 1/16W, 1%	603	Std	Std
1	R3	5.76K	Resistor, Chip, 1/16W, 1%	603	Std	Std
1	R7	6.19K	Resistor, Chip, 1/16W, 1%	603	Std	Std
1	R11	9.09K	Resistor, Chip, 1/16W, 1%	603	Std	Std
4	R2, R5, R10, R13	10K	Resistor, Chip, 1/16W, 1%	603	Std	Std
1	R100	28K	Resistor, Chip, 1/16W, 1%	603	Std	Std
1	R15	28.7K	Resistor, Chip, 1/16W, 1%	603	Std	Std
1	R101	56.2K	Resistor, Chip, 1/16W, 1%	603	Std	Std
1	R9	150K	Resistor, Chip, 1/16W, 1%	603	Std	Std
0	R16, R18	DNP	Resistor, Chip, 1/16W, 1%	603	Std	Std
12	TP1, TP2, TP3, TP6, TP7, TP10, TP11, TP12, TP14, TP15, TP17, TP20		Test Point, Red, Thru Hole Color Keyed	0.100 x 0.100	5000	Keystone
8	TP4, TP5, TP8, TP9, TP13, TP16, TP18, TP19		Test Point, Black, Thru Hole Color Keyed	0.100 x 0.100	5001	Keystone
1	U1		IC, 1-A Wide Input Range Step-Down SWIFT Converter	SO8	TPS5410D	TI
1	U2		IC, 250mA, Cap Free LDO, Adj	SON-8	TPS73201DRB	TI
1	U3		IC, Programmable 1.2V, Delay Time: 1.25ms to 10s	SOT23-6	TPS3808G12DBVR	TI
1	U4		IC, 150mA LDO Regulator With Reverse Current Protection	SOT23-5	TPS73115DBV	TI
1	U5		IC, Programmable 3.3V, Delay Time: 1.25ms to 10s	SOT23-6	TPS3808G33DBVR	TI
1	U6		IC, 400mA LDO Regulator With Reverse Current Protection	QFN-8	TPS736125DRB	TI
1	U7		IC, 4.5 to 20V Input, 3A Step Down Converter	PWP16	TPS54550PWP	TI

Efficiency

The efficiency of the TPS5410 converter with a 3.3V output and 12V input:



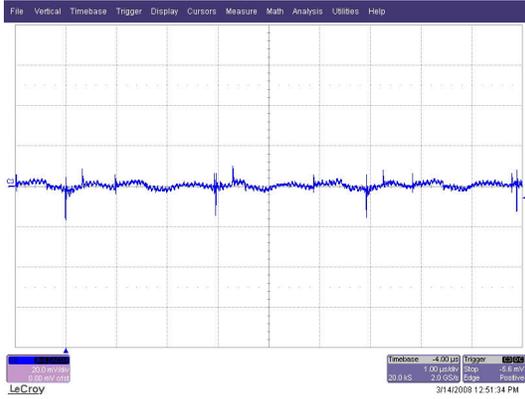
The efficiency of the TPS54550 converter with a 1.2V output and a 12V input:



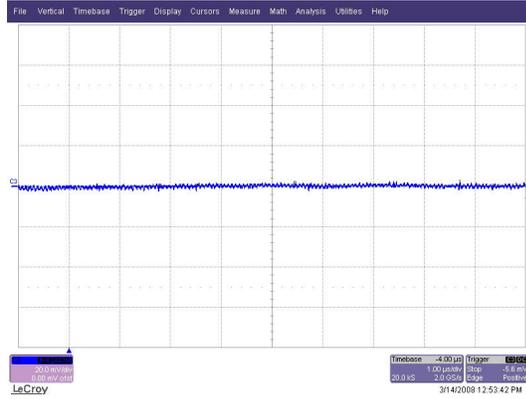
Ripple and Noise

All ripple measurements taken with a 20MHz bandwidth and maximum loading

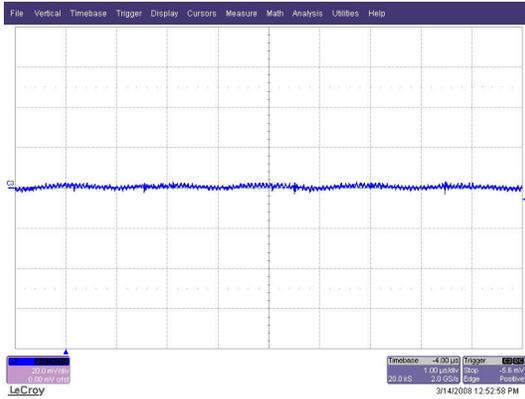
Output ripple/noise 3.3V output:



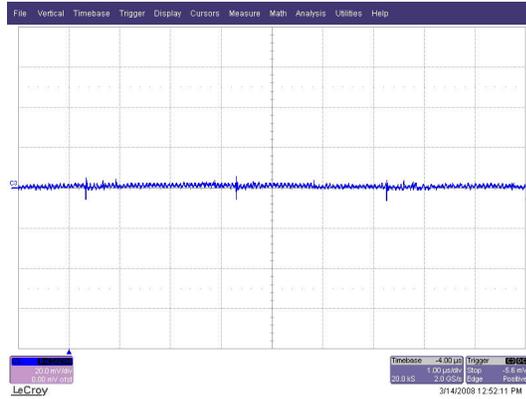
Output ripple/noise 1.8V output:



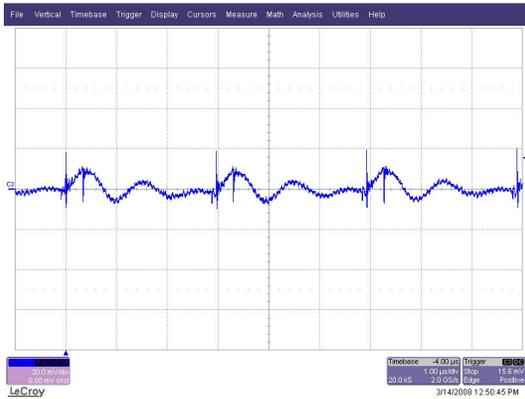
Output ripple/noise 1.5V output:



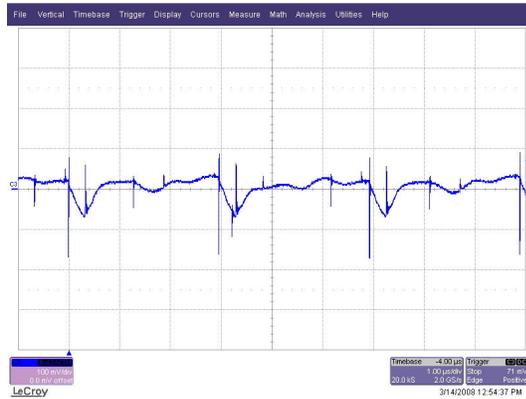
Output ripple/noise 1.25V output:



Output ripple/noise 1.2V output:

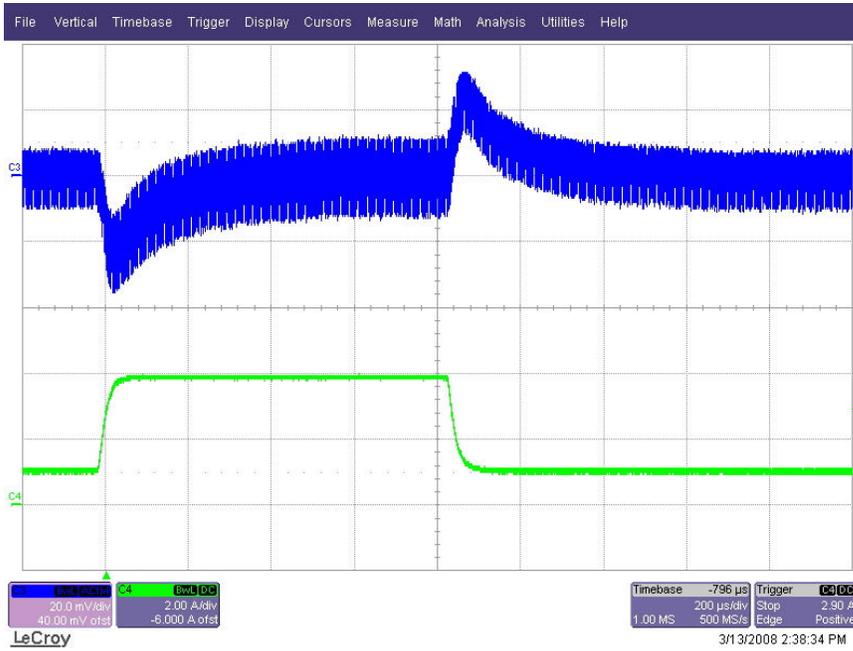


Input ripple/noise 12V input:



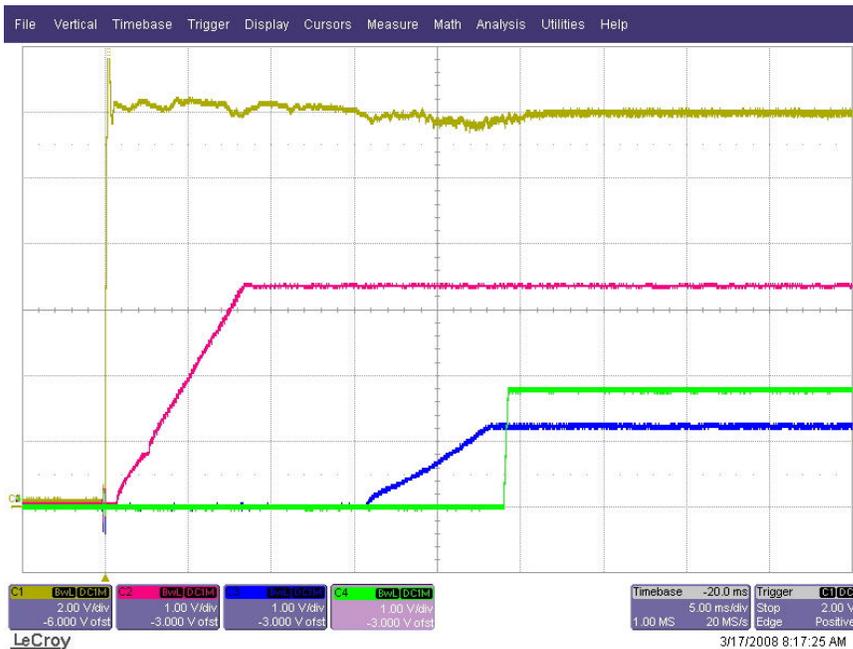
Dynamic Loading

Dynamic load response 1.2V output:

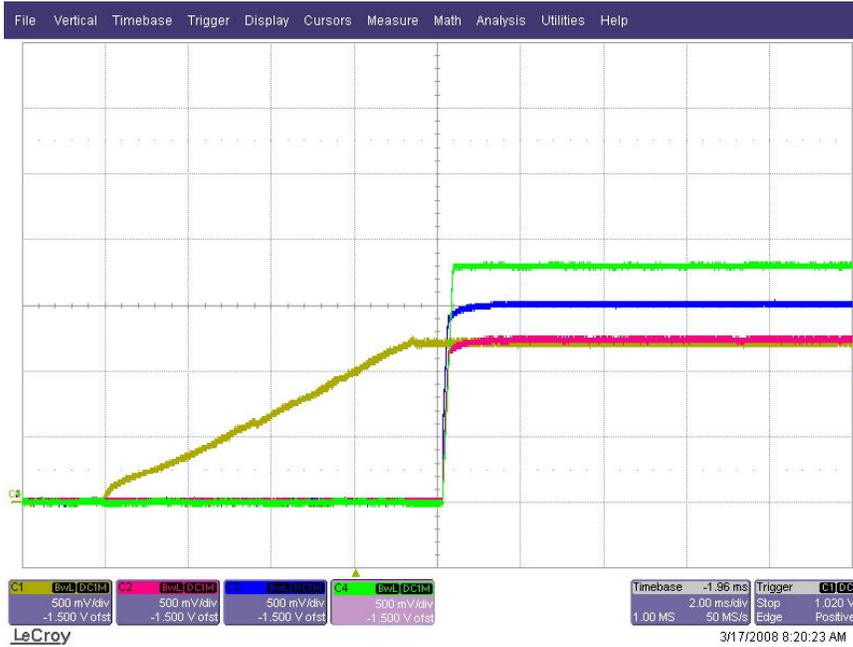


Turn On Response

Output voltage turn-on response (traces, top to bottom, are: 12VIN, 3.3V, 1.8V, 1.2V):

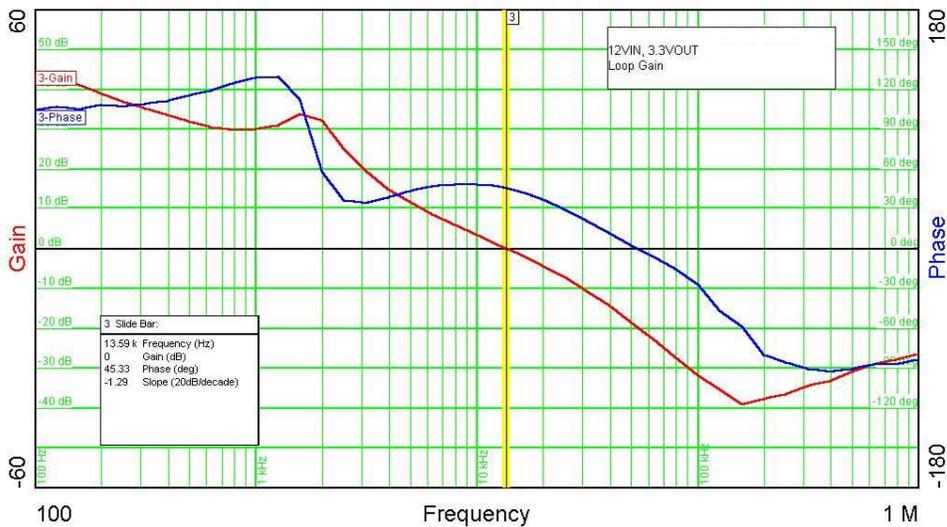


Output voltage turn-on response (traces, top to bottom, are: 1.8V, 1.5V, 1.25V, 1.2V):

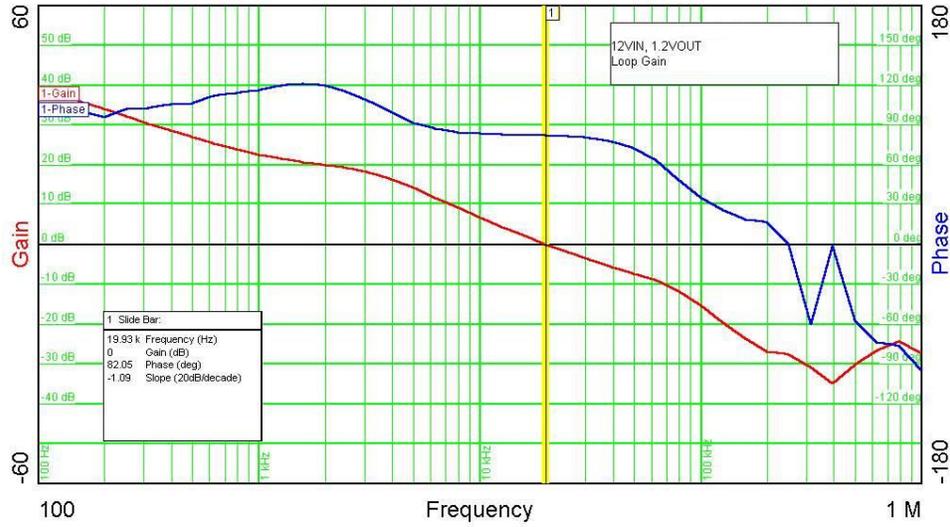


Stability Analysis (Loop Gain)

The figure below is the loop gain of the TPS5410 converter with max load. The bandwidth is 13 KHz, the phase margin is 45 degrees, and the gain margin is 20dB.



The figure below is the loop gain of the TPS54550 converter with max load. The bandwidth is 20 KHz, the phase margin is 82 degrees, and the gain margin is 28dB.



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