		bq27000	bq27200	bq27010	bq27210	bq27500	bq27510
HARDWARE	package designator	SON-DRK	SON-DRK	SON-DRK	SON-DRK	SON-DRZ	SON-DRZ
	package size	4x3mm	4x3mm	4x3mm	4x3mm	4x2.5mm	4x2.5mm
	device pin	10	10	10	10	12	12
	BAT pin Voltage Divider	No	No	No	No	Internal	Internal
	Register backup	through RBI pin	through RBI pin	through RBI pin	through RBI pin	No	No
	Thermistor	internal	internal	internal	internal	internal or external	internal or external
	GPIO count	1	1	1	1	0	0
	internal LDO	None required	None required	None required	None required	No	Yes
COMMUNICATION	interface	HDQ	I2C	HDQ	I2C	I2C	I2C
FIRMWARE		ROM code	ROM code	enhanced ROM code	enhance ROM code	Flash with ROM	Flash with ROM
GAUGING ALGORITHM	method	EDV	EDV	compensated EDV (CEDV)	compensated EDV (CEDV)	Impedance Track	Impedance Track
	OCV look up table	No OCV table. Coulometric gauge with capacity synchronization at 0%, 6.25%, and 100%	No OCV table. Coulometric gauge with capacity synchronization at 0%, 6.25%, and 100%	No OCV table. Coulometric gauge with capacity synchronization at 0%, 6.25%, and 100%	No OCV table. Coulometric gauge with capacity synchronization at 0%, 6.25%, and 100%	OCV table located in device	OCV table located in device
		Accurate coulometric measurement of remaining capacity with rate and temperature compensation. Fixed EDV1 threshold at 6.25%.	Accurate coulometric measurement of remaining capacity with rate and temperature compensation. Fixed EDV1 threshold at 6.25%.	Accurate coulometric measurement of remaining capacity with rate, temperature, and age compensation. EDV1 threshold at 6.25% compensated for rate and temperature. Improved capacity compensation at cold.	Accurate coulometric measurement of remaining capacity with rate, temperature, and age compensation. EDV1 threshold at 6.25% compensated for rate and temperature. Improved capacity compensation at cold.	accurately measuring the remaining capacity and predict run time based on impedance track algorithm	accurately measuring the remaining capacity and predict run time based on impedance track algorithm
Application	system side or pack side	pack	pack	pack	pack	system	system

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