Errata
DLP® Product Advisory for the DLPC910 Device

TEXAS INSTRUMENTS

ABSTRACT

The DLPC910 Mirror Float command should not be used!

After the first instance of the Mirror Float command, all subsequent use of either PWR_FLOAT or Mirror Float commands will not execute correctly.

This issue will cause the DLPC910 to incorrectly power down the DMD when the PWR_FLOAT command is asserted as required to correctly power down the DMD.

This issue is currently in firmware versions 1.0 and 2.0 of the DLPR910 (DLPR910 & DLPR910A).

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1 Recommended Power Down Procedure

See section 9.2 of DLPC910 datasheet.

For correct power down operation of the DMD, the following power down procedure must be executed. Prior to an anticipated power removal, assert PWR_FLOAT for a minimum of 500 µs to allow the DLPC910 to complete the power down procedure. This procedure will assure the DMD mirrors are in a flat state. Following this 500 µs time delay, power can be safely be removed from the DLP chipset.

In the event of an unanticipated power loss, the power management system must detect the input power loss, assert PWR_FLOAT to the DLPC910, and maintain all operating power levels to the DLPC910 and the DMD for a minimum of 500 µs to allow the DLPC910 to complete the power down procedure. Prior to an anticipated power removal, assert PWR_FLOAT for a minimum of 500 µs to allow the DLPC910 to complete the power down procedure.

Note

DLP® Texas Instruments is actively working on finding a solution for the DMD Mirror Float command issued by the DLPC910 Controller.

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2 Revision History

DATE	REVISION	NOTES
December 2023	*	Initial Release

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