

## SN74AUP1G14 Low-Power Single Schmitt-Trigger Inverter

### 1 Features

- Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II
- ESD Performance Tested Per JESD 22
  - 2000-V Human-Body Model (A114-B, Class II)
  - 1000-V Charged-Device Model (C101)
- Low Static-Power Consumption ( $I_{CC} = 0.9 \mu\text{A}$  Maximum)
- Low Dynamic-Power Consumption ( $C_{pd} = 4.4 \text{ pF}$  Typical at 3.3 V)
- Low Input Capacitance ( $C_i = 1.5 \text{ pF}$  Typical)
- Low Noise – Overshoot and Undershoot  $<10\%$  of  $V_{CC}$
- $I_{off}$  Supports Partial-Power-Down Mode Operation
- Includes Schmitt-Trigger Inputs
- Wide Operating  $V_{CC}$  Range of 0.8 V to 3.6 V
- Optimized for 3.3-V Operation
- 3.6-V I/O Tolerant to Support Mixed-Mode Signal Operation
- $t_{pd} = 4.9 \text{ ns}$  Maximum at 3.3 V

### 2 Applications

- AV Receivers
- Smartphones
- Blu-ray Players and Home Theaters
- DVD Recorders and Players
- Desktop or Notebook PCs
- Embedded PCs
- GPS: Personal Navigation Devices
- Mobile Internet Devices
- Portable Media Players
- Smoke Detectors
- Solid State Drive (SSD): Enterprise
- High-Definition (HDTV)
- Tablets: Enterprise
- Audio Docks: Portable
- DVR and DVS

### 3 Description

The AUP family is TI's premier solution to the industry's low power needs in battery-powered portable applications. This family assures a very low static and dynamic power consumption across the entire  $V_{CC}$  range of 0.8 V to 3.6 V, resulting in an increased battery life. This product also maintains excellent signal integrity (see [AUP – The Lowest-Power Family](#) and [Excellent Signal Integrity](#)).

This device functions as an independent gate with Schmitt-trigger inputs, which allows for slow input transition and better switching-noise immunity at the input.

This device is fully specified for partial-power-down applications using  $I_{off}$ . The  $I_{off}$  circuitry disables the outputs when the device is powered down. This inhibits current backflow into the device which prevents damage to the device.

**Device Information<sup>(1)</sup>**

| PART NUMBER    | PACKAGE    | BODY SIZE (NOM)   |
|----------------|------------|-------------------|
| SN74AUP1G14DBV | SOT-23 (5) | 2.90 mm x 1.60 mm |
| SN74AUP1G14DCK | SC70 (5)   | 2.00 mm x 1.25 mm |
| SN74AUP1G14DRL | SOT (5)    | 1.60 mm x 1.20 mm |
| SN74AUP1G14DRY | SON (6)    | 1.45 mm x 1.00 mm |
| SN74AUP1G14DSF | SON (6)    | 1.00 mm x 1.00 mm |
| SN74AUP1G14DPW | X2SON (5)  | 0.80 mm x 0.80 mm |
| SN74AUP1G14YFP | DSBGA (4)  | 0.76 mm x 0.76 mm |

(1) For all available packages, see the orderable addendum at the end of the data sheet.

**Logic Diagram (Positive Logic)  
(DBV, DCK, DRL, DRY, DSF, DPW, and YZP Packages)**



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**Logic Diagram (Positive Logic)  
(YFP Package)**



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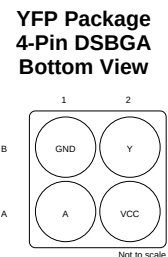
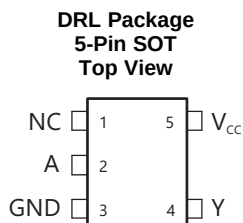
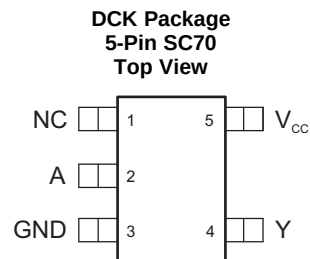
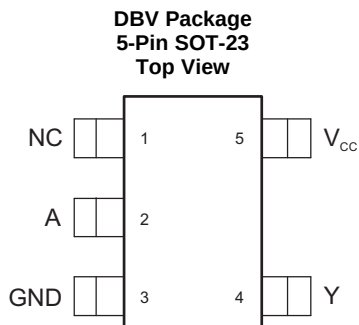
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## 4 Revision History

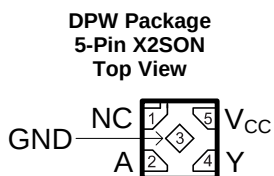
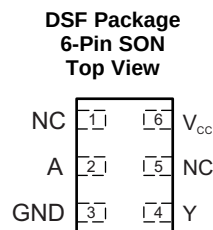
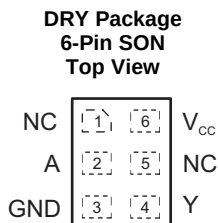
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

| Changes from Revision I (May 2010) to Revision J                                                                                                                                                                                                                                                                                                                                                                                                                        | Page |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------|
| • Added DPW (X2SON) package .....                                                                                                                                                                                                                                                                                                                                                                                                                                       | 1    |
| • Added <i>Device Information</i> table, <i>Pin Configuration and Functions</i> section, <i>ESD Ratings</i> table, <i>Thermal Information</i> table, <i>Feature Description</i> section, <i>Device Functional Modes</i> , <i>Application and Implementation</i> section, <i>Power Supply Recommendations</i> section, <i>Layout</i> section, <i>Device and Documentation Support</i> section, and <i>Mechanical, Packaging, and Orderable Information</i> section. .... | 1    |
| • Deleted <i>Ordering Information</i> table, see <i>Mechanical, Packaging, and Orderable Information</i> section at the end of the data sheet .....                                                                                                                                                                                                                                                                                                                     | 1    |
| • Changed formatting of YFP package pinout drawing .....                                                                                                                                                                                                                                                                                                                                                                                                                | 3    |
| • Deleted YZP package from data sheet .....                                                                                                                                                                                                                                                                                                                                                                                                                             | 3    |
| • Added Junction temperature, $T_J$ in <i>Absolute Maximum Ratings</i> .....                                                                                                                                                                                                                                                                                                                                                                                            | 4    |

## 5 Pin Configuration and Functions



N.C. – No internal connection.  
See mechanical drawings for dimensions.



### Pin Functions

| NAME            | PIN                |          |     | I/O | DESCRIPTION            |
|-----------------|--------------------|----------|-----|-----|------------------------|
|                 | DBV, DCK, DRL, DPW | DRY, DSF | YFP |     |                        |
| A               | 2                  | 2        | A1  | I   | Logic Input            |
| GND             | 3                  | 3        | B1  | —   | Ground                 |
| N.C.            | 1                  | 1, 5     | —   | —   | No internal connection |
| V <sub>CC</sub> | 5                  | 6        | A2  | —   | Positive Supply        |
| Y               | 4                  | 4        | B2  | O   | Output                 |

## 6 Specifications

### 6.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted)<sup>(1)</sup>

|                  |                                                                                             |                    | MIN  | MAX                   | UNIT |
|------------------|---------------------------------------------------------------------------------------------|--------------------|------|-----------------------|------|
| V <sub>CC</sub>  | Supply voltage                                                                              |                    | −0.5 | 4.6                   | V    |
| V <sub>I</sub>   | Input voltage <sup>(2)</sup>                                                                |                    | −0.5 | 4.6                   | V    |
| V <sub>O</sub>   | Voltage range applied to any output in the high-impedance or power-off state <sup>(2)</sup> |                    | −0.5 | 4.6                   | V    |
| V <sub>O</sub>   | Voltage range applied to any output in the high or low state <sup>(2)</sup>                 |                    | −0.5 | V <sub>CC</sub> + 0.5 | V    |
| I <sub>IK</sub>  | Input clamp current                                                                         | V <sub>I</sub> < 0 | −50  |                       | mA   |
| I <sub>OK</sub>  | Output clamp current                                                                        | V <sub>O</sub> < 0 | −50  |                       | mA   |
| I <sub>O</sub>   | Continuous output current                                                                   |                    | ±20  |                       | mA   |
|                  | Continuous current through V <sub>CC</sub> or GND                                           |                    | ±50  |                       | mA   |
| T <sub>J</sub>   | Junction temperature                                                                        |                    | 150  |                       | °C   |
| T <sub>stg</sub> | Storage temperature                                                                         |                    | −65  | 150                   | °C   |

- (1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

### 6.2 ESD Ratings

|                    |                         | VALUE                                                                          | UNIT |
|--------------------|-------------------------|--------------------------------------------------------------------------------|------|
| V <sub>(ESD)</sub> | Electrostatic discharge | Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001 <sup>(1)</sup>              | 2000 |
|                    |                         | Charged-device model (CDM), per JEDEC specification JESD22-C101 <sup>(2)</sup> | 1000 |

- (1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
- (2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

### 6.3 Recommended Operating Conditions

See<sup>(1)</sup>

|                 |                                | MIN                      | MAX             | UNIT |
|-----------------|--------------------------------|--------------------------|-----------------|------|
| V <sub>CC</sub> | Supply voltage                 | 0.8                      | 3.6             | V    |
| V <sub>I</sub>  | Input voltage                  | 0                        | 3.6             | V    |
| V <sub>O</sub>  | Output voltage                 | 0                        | V <sub>CC</sub> | V    |
| I <sub>OH</sub> | High-level output current      | V <sub>CC</sub> = 0.8 V  | –20             | μA   |
|                 |                                | V <sub>CC</sub> = 1.1 V  | –1.1            | mA   |
|                 |                                | V <sub>CC</sub> = 1.4 V  | –1.7            |      |
|                 |                                | V <sub>CC</sub> = 1.65 V | –1.9            |      |
|                 |                                | V <sub>CC</sub> = 2.3 V  | –3.1            |      |
|                 |                                | V <sub>CC</sub> = 3 V    | –4              |      |
| I <sub>OL</sub> | Low-level output current       | V <sub>CC</sub> = 0.8 V  | 20              | μA   |
|                 |                                | V <sub>CC</sub> = 1.1 V  | 1.1             | mA   |
|                 |                                | V <sub>CC</sub> = 1.4 V  | 1.7             |      |
|                 |                                | V <sub>CC</sub> = 1.65 V | 1.9             |      |
|                 |                                | V <sub>CC</sub> = 2.3 V  | 3.1             |      |
|                 |                                | V <sub>CC</sub> = 3 V    | 4               |      |
| T <sub>A</sub>  | Operating free-air temperature | –40                      | 85              | °C   |

- (1) All unused inputs of the device must be held at V<sub>CC</sub> or GND to assure proper device operation. See [Implications of Slow or Floating CMOS Inputs](#).

## 6.4 Thermal Information

| THERMAL METRIC <sup>(1)</sup> |                                              | SN74AUP1G14  |              |                |                |                 |               |              | UNIT |
|-------------------------------|----------------------------------------------|--------------|--------------|----------------|----------------|-----------------|---------------|--------------|------|
|                               |                                              | DRY<br>(SON) | DSF<br>(SON) | YFP<br>(DSBGA) | DPW<br>(X2SON) | DBV<br>(SOT-23) | DCK<br>(SC70) | DRL<br>(SOT) |      |
|                               |                                              | 6 PINS       | 6 PINS       | 4 PINS         | 5 PINS         | 5 PINS          | 5 PINS        | 5 PINS       |      |
| R <sub>θJA</sub>              | Junction-to-ambient thermal resistance       | 347.8        | 386.2        | 179.3          | 489.2          | 289.1           | 325.1         | 324.4        | °C/W |
| R <sub>θJC(top)</sub>         | Junction-to-case (top) thermal resistance    | 237.7        | 192.9        | 2.8            | 226.3          | 213.7           | 229.1         | 156.7        | °C/W |
| R <sub>θJB</sub>              | Junction-to-board thermal resistance         | 210.6        | 242.2        | 58.3           | 352.9          | 123.0           | 123.2         | 172.9        | °C/W |
| Ψ <sub>JT</sub>               | Junction-to-top characterization parameter   | 64.4         | 28.9         | 1.1            | 38.2           | 104.5           | 96.3          | 21.6         | °C/W |
| Ψ <sub>JB</sub>               | Junction-to-board characterization parameter | 210.6        | 241.9        | 58.6           | 352.1          | 122.3           | 122.4         | 173.6        | °C/W |
| R <sub>θJC(bot)</sub>         | Junction-to-case (bottom) thermal resistance | N/A          | N/A          | N/A            | 150.8          | N/A             | N/A           | N/A          | °C/W |

(1) For more information about traditional and new thermal metrics, see the [Semiconductor and IC Package Thermal Metrics](#) application report.

## 6.5 Electrical Characteristics

over operating free-air temperature range (unless otherwise noted)

| PARAMETER       |                                        | TEST CONDITIONS                 | V <sub>CC</sub> | MIN  | TYP | MAX  | UNIT |
|-----------------|----------------------------------------|---------------------------------|-----------------|------|-----|------|------|
| V <sub>T+</sub> | Positive-going input threshold voltage | T <sub>A</sub> = 25°C           | 0.8 V           | 0.3  |     | 0.6  | V    |
|                 |                                        | T <sub>A</sub> = −40°C to +85°C |                 |      |     |      |      |
|                 |                                        | T <sub>A</sub> = 25°C           | 1.1 V           | 0.53 |     | 0.9  |      |
|                 |                                        | T <sub>A</sub> = −40°C to +85°C |                 |      |     |      |      |
|                 |                                        | T <sub>A</sub> = 25°C           | 1.4 V           | 0.74 |     | 1.11 |      |
|                 |                                        | T <sub>A</sub> = −40°C to +85°C |                 |      |     |      |      |
|                 |                                        | T <sub>A</sub> = 25°C           | 1.65 V          | 0.91 |     | 1.29 |      |
|                 |                                        | T <sub>A</sub> = −40°C to +85°C |                 |      |     |      |      |
|                 |                                        | T <sub>A</sub> = 25°C           | 2.3 V           | 1.37 |     | 1.77 |      |
|                 |                                        | T <sub>A</sub> = −40°C to +85°C |                 |      |     |      |      |
|                 |                                        | T <sub>A</sub> = 25°C           | 3 V             | 1.88 |     | 2.29 |      |
|                 |                                        | T <sub>A</sub> = −40°C to +85°C |                 |      |     |      |      |
| V <sub>T−</sub> | Negative-going input threshold voltage | T <sub>A</sub> = 25°C           | 0.8 V           | 0.1  |     | 0.6  | V    |
|                 |                                        | T <sub>A</sub> = −40°C to +85°C |                 |      |     |      |      |
|                 |                                        | T <sub>A</sub> = 25°C           | 1.1 V           | 0.26 |     | 0.65 |      |
|                 |                                        | T <sub>A</sub> = −40°C to +85°C |                 |      |     |      |      |
|                 |                                        | T <sub>A</sub> = 25°C           | 1.4 V           | 0.39 |     | 0.75 |      |
|                 |                                        | T <sub>A</sub> = −40°C to +85°C |                 |      |     |      |      |
|                 |                                        | T <sub>A</sub> = 25°C           | 1.65 V          | 0.47 |     | 0.84 |      |
|                 |                                        | T <sub>A</sub> = −40°C to +85°C |                 |      |     |      |      |
|                 |                                        | T <sub>A</sub> = 25°C           | 2.3 V           | 0.69 |     | 1.04 |      |
|                 |                                        | T <sub>A</sub> = −40°C to +85°C |                 |      |     |      |      |
|                 |                                        | T <sub>A</sub> = 25°C           | 3 V             | 0.88 |     | 1.24 |      |
|                 |                                        | T <sub>A</sub> = −40°C to +85°C |                 |      |     |      |      |

## Electrical Characteristics (continued)

over operating free-air temperature range (unless otherwise noted)

| PARAMETER         |                                                    | TEST CONDITIONS                 |                | V <sub>CC</sub>        | MIN  | TYP  | MAX | UNIT |
|-------------------|----------------------------------------------------|---------------------------------|----------------|------------------------|------|------|-----|------|
| ΔV <sub>T</sub>   | Hysteresis<br>(V <sub>T+</sub> – V <sub>T–</sub> ) | T <sub>A</sub> = 25°C           |                | 0.8 V                  | 0.07 |      | 0.5 | V    |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                |                        |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = 25°C           |                | 1.1 V                  | 0.08 | 0.46 |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                |                        |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = 25°C           |                | 1.4 V                  | 0.18 | 0.56 |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                |                        |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = 25°C           |                | 1.65 V                 | 0.27 | 0.66 |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                |                        |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = 25°C           |                | 2.3 V                  | 0.53 | 0.92 |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                |                        |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = 25°C           |                | 3 V                    | 0.79 | 1.31 |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                |                        |      |      |     |      |
| V <sub>OH</sub>   | I <sub>OH</sub> = –20 μA                           | T <sub>A</sub> = 25°C           | 0.8 V to 3.6 V | V <sub>CC</sub> – 0.1  | V    |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                |                        |      |      |     |      |
|                   | I <sub>OH</sub> = –1.1 mA                          | T <sub>A</sub> = 25°C           | 1.1 V          | 0.75 × V <sub>CC</sub> |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 0.7 × V <sub>CC</sub>  |      |      |     |      |
|                   | I <sub>OH</sub> = –1.7 mA                          | T <sub>A</sub> = 25°C           | 1.4 V          | 1.11                   |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 1.03                   |      |      |     |      |
|                   | I <sub>OH</sub> = –1.9 mA                          | T <sub>A</sub> = 25°C           | 1.65 V         | 1.32                   |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 1.3                    |      |      |     |      |
|                   | I <sub>OH</sub> = –2.3 mA                          | T <sub>A</sub> = 25°C           | 2.3 V          | 2.05                   |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 1.97                   |      |      |     |      |
|                   | I <sub>OH</sub> = –3.1 mA                          | T <sub>A</sub> = 25°C           |                | 1.9                    |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 1.85                   |      |      |     |      |
|                   | I <sub>OH</sub> = –2.7 mA                          | T <sub>A</sub> = 25°C           | 3 V            | 2.72                   |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 2.67                   |      |      |     |      |
|                   | I <sub>OH</sub> = –4 mA                            | T <sub>A</sub> = 25°C           |                | 2.6                    |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 2.55                   |      |      |     |      |
| V <sub>OL</sub>   | I <sub>OL</sub> = 20 μA                            | T <sub>A</sub> = 25°C           | 0.8 V to 3.6 V | 0.1                    | V    |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                |                        |      |      |     |      |
|                   | I <sub>OL</sub> = 1.1 mA                           | T <sub>A</sub> = 25°C           | 1.1 V          | 0.3 × V <sub>CC</sub>  |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                |                        |      |      |     |      |
|                   | I <sub>OL</sub> = 1.7 mA                           | T <sub>A</sub> = 25°C           | 1.4 V          | 0.31                   |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 0.37                   |      |      |     |      |
|                   | I <sub>OL</sub> = 1.9 mA                           | T <sub>A</sub> = 25°C           | 1.65 V         | 0.31                   |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 0.35                   |      |      |     |      |
|                   | I <sub>OL</sub> = 2.3 mA                           | T <sub>A</sub> = 25°C           | 2.3 V          | 0.31                   |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 0.33                   |      |      |     |      |
|                   | I <sub>OL</sub> = 3.1 mA                           | T <sub>A</sub> = 25°C           |                | 0.44                   |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 0.45                   |      |      |     |      |
|                   | I <sub>OL</sub> = 2.7 mA                           | T <sub>A</sub> = 25°C           | 3 V            | 0.31                   |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 0.33                   |      |      |     |      |
|                   | I <sub>OL</sub> = 4 mA                             | T <sub>A</sub> = 25°C           |                | 0.44                   |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 0.45                   |      |      |     |      |
| I <sub>I</sub>    | A input                                            | T <sub>A</sub> = 25°C           | 0 V to 3.6 V   | 0.1                    | μA   |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 0.5                    |      |      |     |      |
| I <sub>OFF</sub>  | V <sub>I</sub> or<br>V <sub>O</sub> = 0 V to 3.6 V | T <sub>A</sub> = 25°C           | 0 V            | 0.2                    | μA   |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 0.6                    |      |      |     |      |
| ΔI <sub>OFF</sub> | V <sub>I</sub> or<br>V <sub>O</sub> = 0 V to 3.6 V | T <sub>A</sub> = 25°C           | 0 V to 0.2 V   | 0.2                    |      |      |     |      |
|                   |                                                    | T <sub>A</sub> = –40°C to +85°C |                | 0.6                    |      |      |     |      |

## Electrical Characteristics (continued)

over operating free-air temperature range (unless otherwise noted)

| PARAMETER        | TEST CONDITIONS                                                          | V <sub>CC</sub>                                                    | MIN            | TYP | MAX        | UNIT |
|------------------|--------------------------------------------------------------------------|--------------------------------------------------------------------|----------------|-----|------------|------|
| I <sub>CC</sub>  | V <sub>I</sub> = GND or (V <sub>CC</sub> to 3.6 V)<br>I <sub>O</sub> = 0 | T <sub>A</sub> = 25°C<br>T <sub>A</sub> = –40°C to +85°C           | 0.8 V to 3.6 V |     | 0.5<br>0.9 | μA   |
| ΔI <sub>CC</sub> | V <sub>I</sub> = V <sub>CC</sub> – 0.6 V<br>I <sub>O</sub> = 0           | T <sub>A</sub> = 25°C<br>T <sub>A</sub> = –40°C to +85°C           | 3.3 V          |     | 40<br>50   | μA   |
| C <sub>I</sub>   | V <sub>I</sub> = V <sub>CC</sub> or GND                                  | T <sub>A</sub> = –40°C to +85°C<br>T <sub>A</sub> = –40°C to +85°C | 0 V<br>3.6 V   | 1.5 |            | pF   |
| C <sub>O</sub>   | V <sub>O</sub> = GND                                                     | T <sub>A</sub> = –40°C to +85°C                                    | 0 V            | 2.5 |            | pF   |

### 6.6 Switching Characteristics: C<sub>L</sub> = 5 pF

over recommended operating free-air temperature range, C<sub>L</sub> = 5 pF (unless otherwise noted) (see [Figure 2](#) and [Figure 3](#))

| PARAMETER       | FROM<br>(INPUT) | TO<br>(OUTPUT) | TEST CONDITIONS                  |                                 | MIN | TYP  | MAX  | UNIT |
|-----------------|-----------------|----------------|----------------------------------|---------------------------------|-----|------|------|------|
| t <sub>pd</sub> | A               | Y              | V <sub>CC</sub> = 0.8 V          | T <sub>A</sub> = 25°C           |     | 16.3 |      | ns   |
|                 |                 |                | V <sub>CC</sub> = 1.2 V ± 0.1 V  | T <sub>A</sub> = 25°C           | 4.2 | 6.9  | 11.7 |      |
|                 |                 |                |                                  | T <sub>A</sub> = –40°C to +85°C | 0.9 |      | 15   |      |
|                 |                 |                | V <sub>CC</sub> = 1.5 V ± 0.1 V  | T <sub>A</sub> = 25°C           | 3.7 | 5.2  | 8.4  |      |
|                 |                 |                |                                  | T <sub>A</sub> = –40°C to +85°C | 1.7 |      | 10.7 |      |
|                 |                 |                | V <sub>CC</sub> = 1.8 V ± 0.15 V | T <sub>A</sub> = 25°C           | 3.3 | 4.4  | 6.9  |      |
|                 |                 |                |                                  | T <sub>A</sub> = –40°C to +85°C | 1.9 |      | 8.5  |      |
|                 |                 |                | V <sub>CC</sub> = 2.5 V ± 0.2 V  | T <sub>A</sub> = 25°C           | 2.8 | 3.5  | 4.8  |      |
|                 |                 |                |                                  | T <sub>A</sub> = –40°C to +85°C | 1.8 |      | 6.1  |      |
|                 |                 |                | V <sub>CC</sub> = 3.3 V ± 0.3 V  | T <sub>A</sub> = 25°C           | 2.5 | 3    | 4    |      |
|                 |                 |                |                                  | T <sub>A</sub> = –40°C to +85°C | 1.7 |      | 4.9  |      |

### 6.7 Switching Characteristics: C<sub>L</sub> = 10 pF

over recommended operating free-air temperature range, C<sub>L</sub> = 10 pF (unless otherwise noted) (see [Figure 2](#) and [Figure 3](#))

| PARAMETER       | FROM<br>(INPUT) | TO<br>(OUTPUT) | TEST CONDITIONS                  |                                 | MIN | TYP  | MAX  | UNIT |
|-----------------|-----------------|----------------|----------------------------------|---------------------------------|-----|------|------|------|
| t <sub>pd</sub> | A               | Y              | V <sub>CC</sub> = 0.8 V          | T <sub>A</sub> = 25°C           |     | 18.4 |      | ns   |
|                 |                 |                | V <sub>CC</sub> = 1.2 V ± 0.1 V  | T <sub>A</sub> = 25°C           | 4.6 | 7.9  | 13.4 |      |
|                 |                 |                |                                  | T <sub>A</sub> = –40°C to +85°C | 1.3 |      | 16.7 |      |
|                 |                 |                | V <sub>CC</sub> = 1.5 V ± 0.1 V  | T <sub>A</sub> = 25°C           | 4   | 6    | 9.6  |      |
|                 |                 |                |                                  | T <sub>A</sub> = –40°C to +85°C | 2.2 |      | 11.8 |      |
|                 |                 |                | V <sub>CC</sub> = 1.8 V ± 0.15 V | T <sub>A</sub> = 25°C           | 3.6 | 5    | 7.9  |      |
|                 |                 |                |                                  | T <sub>A</sub> = –40°C to +85°C | 2.4 |      | 9.5  |      |
|                 |                 |                | V <sub>CC</sub> = 2.5 V ± 0.2 V  | T <sub>A</sub> = 25°C           | 3.2 | 4    | 5.5  |      |
|                 |                 |                |                                  | T <sub>A</sub> = –40°C to +85°C | 2.3 |      | 6.8  |      |
|                 |                 |                | V <sub>CC</sub> = 3.3 V ± 0.3 V  | T <sub>A</sub> = 25°C           | 2.9 | 3.5  | 4.6  |      |
|                 |                 |                |                                  | T <sub>A</sub> = –40°C to +85°C | 2.1 |      | 5.6  |      |

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**6.8 Switching Characteristics:  $C_L = 15\text{ pF}$** 

 over recommended operating free-air temperature range,  $C_L = 15\text{ pF}$  (unless otherwise noted) (see [Figure 2](#) and [Figure 3](#))

| PARAMETER | FROM<br>(INPUT) | TO<br>(OUTPUT) | TEST CONDITIONS                           |                                                 | MIN | TYP  | MAX  | UNIT |
|-----------|-----------------|----------------|-------------------------------------------|-------------------------------------------------|-----|------|------|------|
| $t_{pd}$  | A               | Y              | $V_{CC} = 0.8\text{ V}$                   | $T_A = 25^\circ\text{C}$                        |     | 20.1 |      | ns   |
|           |                 |                | $V_{CC} = 1.2\text{ V} \pm 0.1\text{ V}$  | $T_A = 25^\circ\text{C}$                        | 5.5 | 8.7  | 14   |      |
|           |                 |                |                                           | $T_A = -40^\circ\text{C to } +85^\circ\text{C}$ | 2.5 |      | 17.3 |      |
|           |                 |                | $V_{CC} = 1.5\text{ V} \pm 0.1\text{ V}$  | $T_A = 25^\circ\text{C}$                        | 4.7 | 6.7  | 10   |      |
|           |                 |                |                                           | $T_A = -40^\circ\text{C to } +85^\circ\text{C}$ | 3   |      | 12.5 |      |
|           |                 |                | $V_{CC} = 1.8\text{ V} \pm 0.15\text{ V}$ | $T_A = 25^\circ\text{C}$                        | 4.2 | 5.6  | 8.3  |      |
|           |                 |                |                                           | $T_A = -40^\circ\text{C to } +85^\circ\text{C}$ | 3   |      | 10.1 |      |
|           |                 |                | $V_{CC} = 2.5\text{ V} \pm 0.2\text{ V}$  | $T_A = 25^\circ\text{C}$                        | 3.6 | 4.5  | 5.9  |      |
|           |                 |                |                                           | $T_A = -40^\circ\text{C to } +85^\circ\text{C}$ | 2.7 |      | 7.4  |      |
|           |                 |                | $V_{CC} = 3.3\text{ V} \pm 0.3\text{ V}$  | $T_A = 25^\circ\text{C}$                        | 3.3 | 3.9  | 5    |      |
|           |                 |                |                                           | $T_A = -40^\circ\text{C to } +85^\circ\text{C}$ | 2.5 |      | 6.1  |      |

**6.9 Switching Characteristics:  $C_L = 30\text{ pF}$** 

 over recommended operating free-air temperature range,  $C_L = 30\text{ pF}$  (unless otherwise noted) (see [Figure 2](#) and [Figure 3](#))

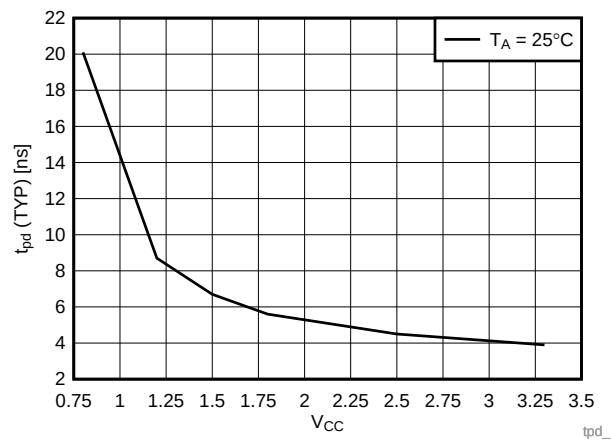
| PARAMETER                       | FROM<br>(INPUT) | TO<br>(OUTPUT) | TEST CONDITIONS                  |                                 | MIN  | TYP  | MAX  | UNIT |
|---------------------------------|-----------------|----------------|----------------------------------|---------------------------------|------|------|------|------|
| t <sub>pd</sub>                 | A               | Y              | V <sub>CC</sub> = 0.8 V          | T <sub>A</sub> = 25°C           | 25.7 |      |      | ns   |
|                                 |                 |                | V <sub>CC</sub> = 1.2 V ± 0.1 V  | T <sub>A</sub> = 25°C           | 7.4  | 11.2 | 17.1 |      |
|                                 |                 |                |                                  | T <sub>A</sub> = −40°C to +85°C | 4.5  | 20.5 |      |      |
|                                 |                 |                | V <sub>CC</sub> = 1.5 V ± 0.1 V  | T <sub>A</sub> = 25°C           | 6.1  | 8.5  | 12.3 |      |
|                                 |                 |                |                                  | T <sub>A</sub> = −40°C to +85°C | 4.6  | 14.7 |      |      |
|                                 |                 |                | V <sub>CC</sub> = 1.8 V ± 0.15 V | T <sub>A</sub> = 25°C           | 5.4  | 7.2  | 10.3 |      |
|                                 |                 |                |                                  | T <sub>A</sub> = −40°C to +85°C | 4.1  | 12   |      |      |
|                                 |                 |                | V <sub>CC</sub> = 2.5 V ± 0.2 V  | T <sub>A</sub> = 25°C           | 4.7  | 5.7  | 7.4  |      |
|                                 |                 |                |                                  | T <sub>A</sub> = −40°C to +85°C | 3.7  | 8.8  |      |      |
|                                 |                 |                | V <sub>CC</sub> = 3.3 V ± 0.3 V  | T <sub>A</sub> = 25°C           | 4.2  | 5    | 6.2  |      |
| T <sub>A</sub> = −40°C to +85°C | 3.5             | 7.3            |                                  |                                 |      |      |      |      |

**6.10 Operating Characteristics**
 $T_A = 25^\circ\text{C}$ 

| PARAMETER |                               | TEST CONDITIONS     | $V_{CC}$                         | TYP | UNIT |
|-----------|-------------------------------|---------------------|----------------------------------|-----|------|
| $C_{pd}$  | Power dissipation capacitance | $f = 10\text{ MHz}$ | 0.8 V                            | 4   | pF   |
|           |                               |                     | $1.2\text{ V} \pm 0.1\text{ V}$  | 4   |      |
|           |                               |                     | $1.5\text{ V} \pm 0.1\text{ V}$  | 4.1 |      |
|           |                               |                     | $1.8\text{ V} \pm 0.15\text{ V}$ | 4.1 |      |
|           |                               |                     | $2.5\text{ V} \pm 0.2\text{ V}$  | 4.3 |      |
|           |                               |                     | $3.3\text{ V} \pm 0.3\text{ V}$  | 4.4 |      |



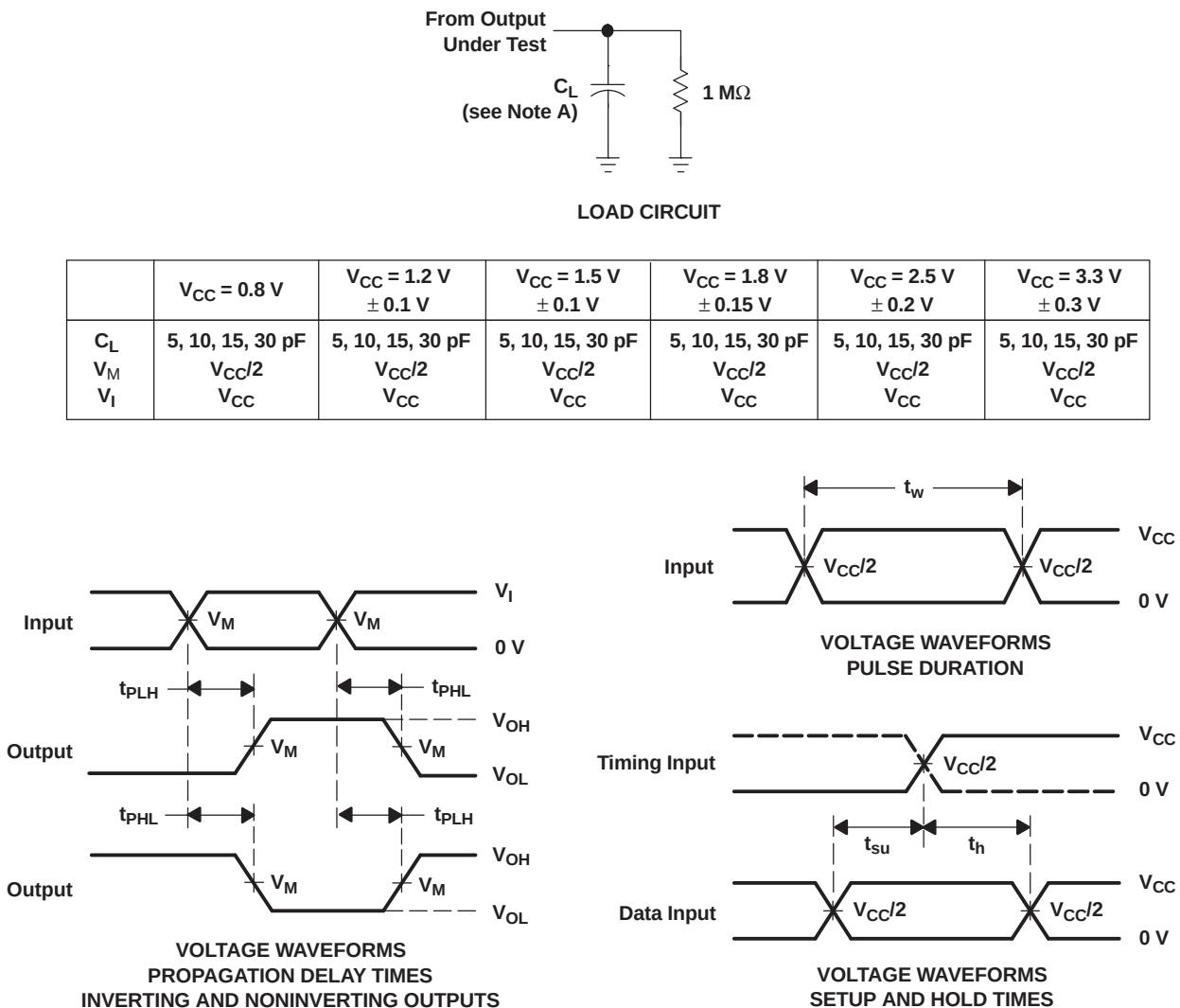
## 6.11 Typical Characteristics



**Figure 1. Typical Propagation Delay vs. Supply Voltage ( $C_L = 15\text{ pF}$ )**

## 7 Parameter Measurement Information

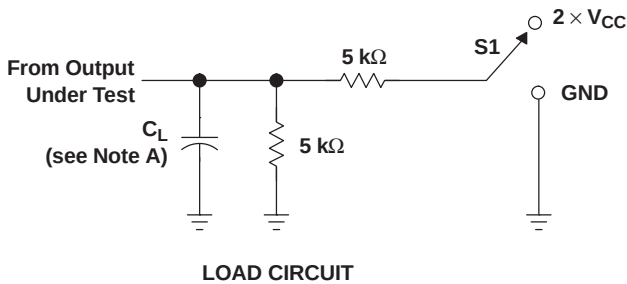
### 7.1 (Propagation Delays, Setup and Hold Times, and Pulse Width)



- NOTES:
- A.  $C_L$  includes probe and jig capacitance.
  - B. All input pulses are supplied by generators having the following characteristics:  $PRR \leq 10 \text{ MHz}$ ,  $Z_O = 50 \Omega$ ,  $t_r/t_f = 3 \text{ ns}$ .
  - C. The outputs are measured one at a time, with one transition per measurement.
  - D.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{pd}$ .
  - E. All parameters and waveforms are not applicable to all devices.

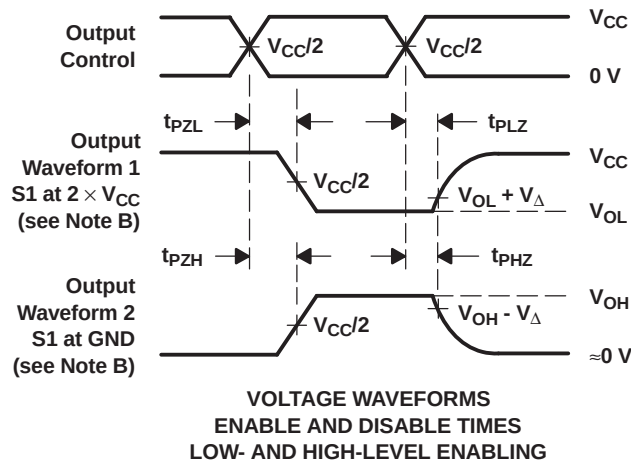
**Figure 2. Load Circuit And Voltage Waveforms**

## 7.2 (Enable and Disable Times)



| TEST                                   | S1                         |
|----------------------------------------|----------------------------|
| $t_{PLZ}/t_{PZL}$<br>$t_{PHZ}/t_{PZH}$ | 2 $\times$ $V_{CC}$<br>GND |

|              | $V_{CC} = 0.8 \text{ V}$ | $V_{CC} = 1.2 \text{ V}$<br>$\pm 0.1 \text{ V}$ | $V_{CC} = 1.5 \text{ V}$<br>$\pm 0.1 \text{ V}$ | $V_{CC} = 1.8 \text{ V}$<br>$\pm 0.15 \text{ V}$ | $V_{CC} = 2.5 \text{ V}$<br>$\pm 0.2 \text{ V}$ | $V_{CC} = 3.3 \text{ V}$<br>$\pm 0.3 \text{ V}$ |
|--------------|--------------------------|-------------------------------------------------|-------------------------------------------------|--------------------------------------------------|-------------------------------------------------|-------------------------------------------------|
| $C_L$        | 5, 10, 15, 30 pF         | 5, 10, 15, 30 pF                                | 5, 10, 15, 30 pF                                | 5, 10, 15, 30 pF                                 | 5, 10, 15, 30 pF                                | 5, 10, 15, 30 pF                                |
| $V_M$        | $V_{CC}/2$               | $V_{CC}/2$                                      | $V_{CC}/2$                                      | $V_{CC}/2$                                       | $V_{CC}/2$                                      | $V_{CC}/2$                                      |
| $V_I$        | $V_{CC}$                 | $V_{CC}$                                        | $V_{CC}$                                        | $V_{CC}$                                         | $V_{CC}$                                        | $V_{CC}$                                        |
| $V_{\Delta}$ | 0.1 V                    | 0.1 V                                           | 0.1 V                                           | 0.15 V                                           | 0.15 V                                          | 0.3 V                                           |



- NOTES:
- A.  $C_L$  includes probe and jig capacitance.
  - B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.
  - C. All input pulses are supplied by generators having the following characteristics:  $PRR \leq 10 \text{ MHz}$ ,  $Z_O = 50 \Omega$ ,  $t_r/t_f = 3 \text{ ns}$ .
  - D. The outputs are measured one at a time, with one transition per measurement.
  - E.  $t_{PLZ}$  and  $t_{PHZ}$  are the same as  $t_{dis}$ .
  - F.  $t_{PZL}$  and  $t_{PZH}$  are the same as  $t_{en}$ .
  - G. All parameters and waveforms are not applicable to all devices.

**Figure 3. Load Circuit And Voltage Waveforms**

## 8 Detailed Description

### 8.1 Overview

This device functions as an independent gate with Schmitt-trigger inputs, which allows for slow input transition and better switching-noise immunity at the input.

This device is fully specified for partial-power-down applications using  $I_{off}$ . The  $I_{off}$  circuitry disables the outputs when the device is powered down. This inhibits current backflow into the device which prevents damage to the device.

### 8.2 Functional Block Diagrams



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**Figure 4. Logic Diagram (Positive Logic)**  
(DBV, DCK, DRL, DRY, DSF, DPW, and YZP Packages)



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**Figure 5. Logic Diagram (Positive Logic)**  
(YFP Package)

### 8.3 Feature Description

#### 8.3.1 Balanced CMOS Push-Pull Outputs

A balanced output allows the device to sink and source similar currents. The drive capability of this device creates fast edges into light loads so routing and load conditions should be considered to prevent ringing. Additionally, the outputs of this device are capable of driving larger currents than the device can sustain without being damaged. It is important for the power output of the device to be limited to avoid thermal runaway and damage due to over-current. The electrical and thermal limits defined in the [Absolute Maximum Ratings](#) table must be followed at all times.

#### 8.3.2 Schmitt-Trigger Inputs

Standard CMOS inputs are high impedance and are typically modeled as a resistor in parallel with the input capacitance given in the [Electrical Characteristics](#) table. The worst case resistance is calculated with the maximum input voltage, given in the [Absolute Maximum Ratings](#) table, and the maximum input leakage current, given in the [Electrical Characteristics](#) table, using ohm's law ( $R = V \div I$ ).

The Schmitt-trigger input architecture provides hysteresis as defined by  $\Delta V_T$  in the [Electrical Characteristics](#) table, which makes this device extremely tolerant to slow or noisy inputs. While the inputs can be driven much slower than standard CMOS inputs, it is still recommended to properly terminate unused inputs. Driving the inputs slowly will also increase dynamic current consumption of the device. For additional information regarding Schmitt-trigger inputs, see [Understanding Schmitt Triggers](#).

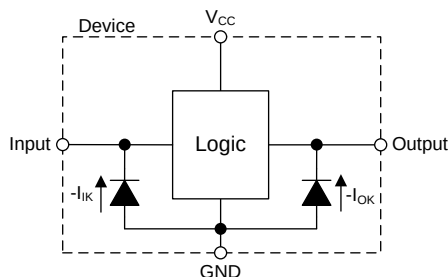
## Feature Description (continued)

### 8.3.3 Clamp Diodes

The inputs and outputs to this device have negative clamping diodes.

#### CAUTION

Voltages beyond the values specified in the [Absolute Maximum Ratings](#) table can cause damage to the device. The input negative-voltage and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.



**Figure 6. Electrical Placement of Clamping Diodes for Each Input and Output**

### 8.3.4 Partial Power Down ( $I_{off}$ )

The inputs and outputs for this device enter a high impedance state when the supply voltage is 0 V. The maximum leakage into or out of any input or output pin on the device is specified by  $I_{off}$  in the [Electrical Characteristics](#) table.

### 8.3.5 Over-Voltage Tolerant Inputs

Input signals to this device can be driven above the supply voltage so long as they remain below the maximum input voltage value specified in the [Absolute Maximum Ratings](#) table.

## 8.4 Device Functional Modes

[Table 1](#) lists the functional modes of the SN74AUP1G14.

**Table 1. Function Table**

| INPUT<br>A | OUTPUT<br>Y |
|------------|-------------|
| H          | L           |
| L          | H           |

## 9 Application and Implementation

### NOTE

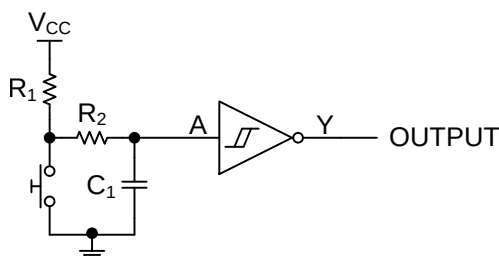
Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

### 9.1 Application Information

Mechanical input elements, such as push buttons or rotary knobs, offer simple ways to interact with electronic systems. Typically, these elements have recoil or bouncing, where the mechanical element makes and breaks contact multiple times during human interaction. This bouncing can cause one or more repeated signals to be passed, triggering multiple actions when only a single input was intended. One potential solution to mitigating these multiple inputs is by utilizing a Schmitt-trigger to create a debounce circuit. [Figure 7](#) shows an example of this solution.

### 9.2 Typical Application

The input due to the push button switches multiple times, causing the output of a non Schmitt-trigger device to trigger multiple times, while the Schmitt-trigger input device with RC delay limits the output pulse to a single pulse desired by the user. The separated positive and negative input voltage threshold values prevent multiple triggers from occurring, see the [Electrical Characteristics](#) table for  $V_{T+}$ ,  $V_{T-}$ , and  $V_{hys}$  values.



**Figure 7. Push Button Debounce Circuit Schematic**

#### 9.2.1 Design Requirements

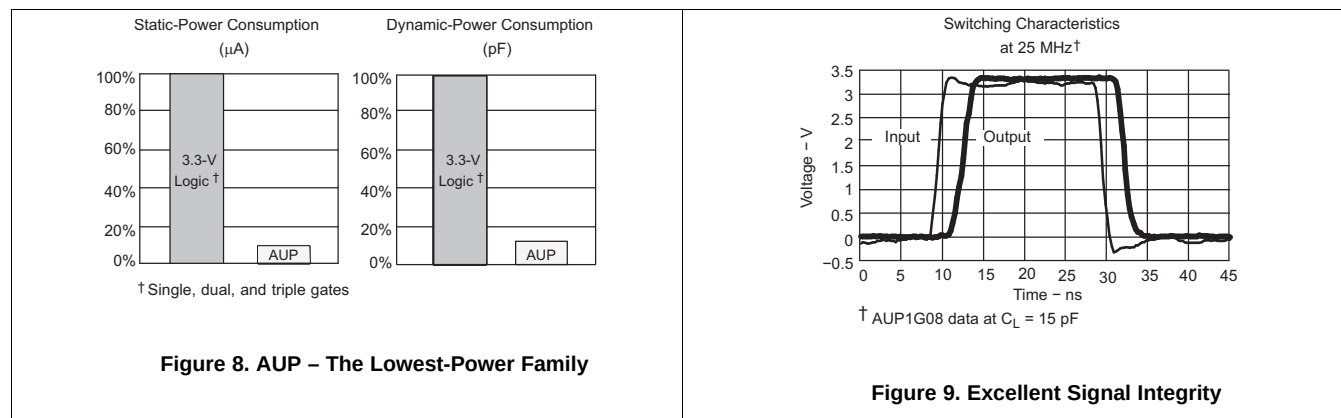
This device uses CMOS technology and has balanced output drive. Take care to avoid bus contention because it can drive currents that would exceed maximum limits. The drive strength also creates fast edges into light loads so routing and load conditions should be considered to prevent ringing.

#### 9.2.2 Detailed Design Procedure

1. Recommended Input Conditions:
  - For specified high and low levels, see ( $V_{T+}$  and  $V_{T-}$ ) in the [Electrical Characteristics](#) table.
  - Inputs are overvoltage tolerant allowing them to go as high as ( $V_I$  max) in the [Recommended Operating Conditions](#) table at any valid  $V_{CC}$ .
2. Recommended Output Conditions:
  - Load currents should not exceed ( $I_O$  max) per output and should not exceed (Continuous current through  $V_{CC}$  or GND) total current for the part. These limits are located in the [Absolute Maximum Ratings](#) table.

## Typical Application (continued)

### 9.2.3 Application Curves



## 10 Power Supply Recommendations

The power supply can be any voltage between the min and max supply voltage rating located in the [Recommended Operating Conditions](#) table.

Each  $V_{CC}$  pin should have a good bypass capacitor to prevent power disturbance. It is ok to parallel multiple bypass caps to reject different frequencies of noise. 0.1- $\mu$ F and 1- $\mu$ F capacitors are commonly used in parallel. The bypass capacitor should be installed as close to the power pin as possible for best results.

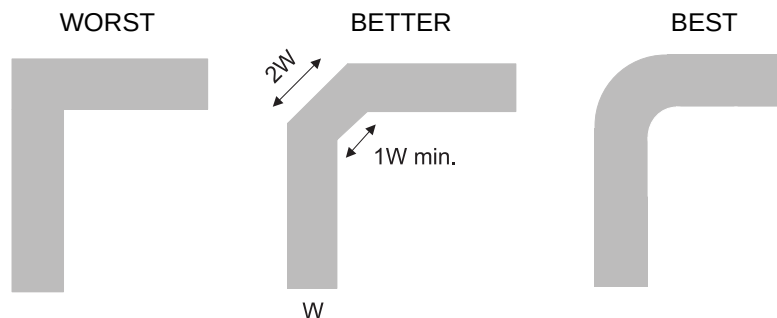
## 11 Layout

### 11.1 Layout Guidelines

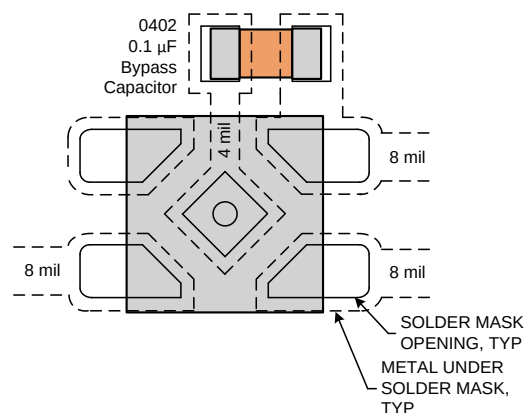
Even low data rate digital signals can contain high-frequency signal components due to fast edge rates. When a printed-circuit board (PCB) trace turns a corner at a 90° angle, a reflection can occur. A reflection occurs primarily because of the change of width of the trace. At the apex of the turn, the trace width increases to 1.414 times the width. This increase upsets the transmission-line characteristics, especially the distributed capacitance and self-inductance of the trace which results in the reflection. Not all PCB traces can be straight and therefore some traces must turn corners. [Figure 10](#) shows progressively better techniques of rounding corners. Only the last example (BEST) maintains constant trace width and minimizes reflections.

An example layout is given in [Figure 11](#) for the DPW (X2SON-5) package. This example layout includes a 0402 (metric) capacitor and uses the measurements found in the example board layout appended to this end of this datasheet. A via of diameter 0.1 mm (3.973 mil) is placed directly in the center of the device. This via can be used to trace out the center pin connection through another board layer, or it can be left out of the layout

### 11.2 Layout Example



**Figure 10. Trace Example**



**Figure 11. Example Layout With DPW (X2SON-5) Package**



## 12 Device and Documentation Support

### 12.1 Documentation Support

#### 12.1.1 Related Documentation

For related documentation see the following:

[Implications of Slow or Floating CMOS Inputs](#)

### 12.2 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on ti.com. In the upper right corner, click on *Alert me* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

### 12.3 Community Resources

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### 12.5 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### 12.6 Glossary

[SLYZ022](#) — *TI Glossary*.

This glossary lists and explains terms, acronyms, and definitions.

## 13 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

## PACKAGING INFORMATION

| Orderable part number              | Status<br>(1) | Material type<br>(2) | Package   Pins    | Package qty   Carrier | RoHS<br>(3) | Lead finish/<br>Ball material<br>(4) | MSL rating/<br>Peak reflow<br>(5) | Op temp (°C) | Part marking<br>(6)   |
|------------------------------------|---------------|----------------------|-------------------|-----------------------|-------------|--------------------------------------|-----------------------------------|--------------|-----------------------|
| <a href="#">SN74AUP1G14DBVR</a>    | Active        | Production           | SOT-23 (DBV)   5  | 3000   LARGE T&R      | Yes         | NIPDAU   SN                          | Level-1-260C-UNLIM                | -40 to 85    | H14R                  |
| SN74AUP1G14DBVR.B                  | Active        | Production           | SOT-23 (DBV)   5  | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | H14R                  |
| SN74AUP1G14DBVRG4                  | Active        | Production           | SOT-23 (DBV)   5  | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | H14R                  |
| SN74AUP1G14DBVRG4.B                | Active        | Production           | SOT-23 (DBV)   5  | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | H14R                  |
| <a href="#">SN74AUP1G14DBVT</a>    | Active        | Production           | SOT-23 (DBV)   5  | 250   SMALL T&R       | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | H14R                  |
| SN74AUP1G14DBVT.B                  | Active        | Production           | SOT-23 (DBV)   5  | 250   SMALL T&R       | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | H14R                  |
| <a href="#">SN74AUP1G14DCKR</a>    | Active        | Production           | SC70 (DCK)   5    | 3000   LARGE T&R      | Yes         | NIPDAU   SN                          | Level-1-260C-UNLIM                | -40 to 85    | (HF5, HFF, HFK, HF R) |
| SN74AUP1G14DCKR.B                  | Active        | Production           | SC70 (DCK)   5    | 3000   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | (HF5, HFF, HFK, HF R) |
| <a href="#">SN74AUP1G14DCKR1G4</a> | Active        | Production           | SC70 (DCK)   5    | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | (HF5, HFF, HFK, HF R) |
| SN74AUP1G14DCKR1G4.B               | Active        | Production           | SC70 (DCK)   5    | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | (HF5, HFF, HFK, HF R) |
| SN74AUP1G14DCKRG4                  | Active        | Production           | SC70 (DCK)   5    | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | (HF5, HFF, HFK, HF R) |
| <a href="#">SN74AUP1G14DCKT</a>    | Active        | Production           | SC70 (DCK)   5    | 250   SMALL T&R       | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | (HF5, HFK, HFR)       |
| SN74AUP1G14DCKT.B                  | Active        | Production           | SC70 (DCK)   5    | 250   SMALL T&R       | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | (HF5, HFK, HFR)       |
| <a href="#">SN74AUP1G14DPWR</a>    | Active        | Production           | X2SON (DPW)   5   | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | (B, BZ)               |
| SN74AUP1G14DPWR.B                  | Active        | Production           | X2SON (DPW)   5   | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | (B, BZ)               |
| <a href="#">SN74AUP1G14DRLR</a>    | Active        | Production           | SOT-5X3 (DRL)   5 | 4000   LARGE T&R      | Yes         | NIPDAU   NIPDAUAG                    | Level-1-260C-UNLIM                | -40 to 85    | (HF7, HFR)            |
| SN74AUP1G14DRLR.B                  | Active        | Production           | SOT-5X3 (DRL)   5 | 4000   LARGE T&R      | Yes         | NIPDAUAG                             | Level-1-260C-UNLIM                | -40 to 85    | (HF7, HFR)            |
| <a href="#">SN74AUP1G14DRYR</a>    | Active        | Production           | SON (DRY)   6     | 5000   LARGE T&R      | Yes         | NIPDAU   NIPDAUAG                    | Level-1-260C-UNLIM                | -40 to 85    | HF                    |
| SN74AUP1G14DRYR.B                  | Active        | Production           | SON (DRY)   6     | 5000   LARGE T&R      | Yes         | NIPDAUAG                             | Level-1-260C-UNLIM                | -40 to 85    | HF                    |
| SN74AUP1G14DRYRG4                  | Active        | Production           | SON (DRY)   6     | 5000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | HF                    |
| SN74AUP1G14DRYRG4.B                | Active        | Production           | SON (DRY)   6     | 5000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | HF                    |
| <a href="#">SN74AUP1G14DSFR</a>    | Active        | Production           | SON (DSF)   6     | 5000   LARGE T&R      | Yes         | NIPDAU   NIPDAUAG                    | Level-1-260C-UNLIM                | -40 to 85    | HF                    |
| SN74AUP1G14DSFR.B                  | Active        | Production           | SON (DSF)   6     | 5000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | HF                    |
| SN74AUP1G14DSFRG4                  | Active        | Production           | SON (DSF)   6     | 5000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | HF                    |
| SN74AUP1G14DSFRG4.B                | Active        | Production           | SON (DSF)   6     | 5000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | HF                    |

| Orderable part number           | Status<br>(1) | Material type<br>(2) | Package   Pins  | Package qty   Carrier | RoHS<br>(3) | Lead finish/<br>Ball material<br>(4) | MSL rating/<br>Peak reflow<br>(5) | Op temp (°C) | Part marking<br>(6) |
|---------------------------------|---------------|----------------------|-----------------|-----------------------|-------------|--------------------------------------|-----------------------------------|--------------|---------------------|
| <a href="#">SN74AUP1G14YFPR</a> | Active        | Production           | DSBGA (YFP)   4 | 3000   LARGE T&R      | Yes         | SNAGCU                               | Level-1-260C-UNLIM                | -            | HF<br>N             |
| SN74AUP1G14YFPR.B               | Active        | Production           | DSBGA (YFP)   4 | 3000   LARGE T&R      | Yes         | SNAGCU                               | Level-1-260C-UNLIM                | -40 to 85    | HF<br>N             |

<sup>(1)</sup> **Status:** For more details on status, see our [product life cycle](#).

<sup>(2)</sup> **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

<sup>(3)</sup> **RoHS values:** Yes, No, RoHS Exempt. See the [TI RoHS Statement](#) for additional information and value definition.

<sup>(4)</sup> **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

<sup>(5)</sup> **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

<sup>(6)</sup> **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

**TAPE AND REEL INFORMATION**

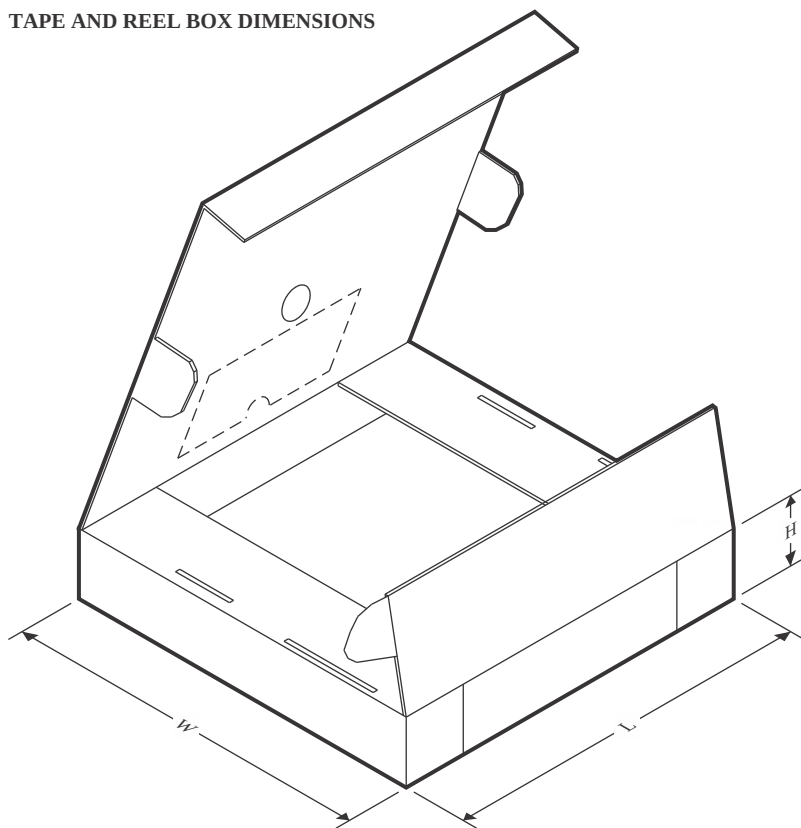

\*All dimensions are nominal

| Device             | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|--------------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| SN74AUP1G14DBVR    | SOT-23       | DBV             | 5    | 3000 | 178.0              | 8.4                | 3.2     | 3.2     | 1.4     | 4.0     | 8.0    | Q3            |
| SN74AUP1G14DBVRG4  | SOT-23       | DBV             | 5    | 3000 | 180.0              | 8.4                | 3.2     | 3.2     | 1.4     | 4.0     | 8.0    | Q3            |
| SN74AUP1G14DBVT    | SOT-23       | DBV             | 5    | 250  | 180.0              | 8.4                | 3.2     | 3.2     | 1.4     | 4.0     | 8.0    | Q3            |
| SN74AUP1G14DCKR    | SC70         | DCK             | 5    | 3000 | 180.0              | 8.4                | 2.3     | 2.5     | 1.2     | 4.0     | 8.0    | Q3            |
| SN74AUP1G14DCKR    | SC70         | DCK             | 5    | 3000 | 178.0              | 8.4                | 2.25    | 2.45    | 1.2     | 4.0     | 8.0    | Q3            |
| SN74AUP1G14DCKR1G4 | SC70         | DCK             | 5    | 3000 | 178.0              | 9.2                | 2.4     | 2.4     | 1.22    | 4.0     | 8.0    | Q3            |
| SN74AUP1G14DCKR1G4 | SC70         | DCK             | 5    | 3000 | 180.0              | 8.4                | 2.47    | 2.3     | 1.25    | 4.0     | 8.0    | Q3            |
| SN74AUP1G14DCKRG4  | SC70         | DCK             | 5    | 3000 | 178.0              | 9.2                | 2.4     | 2.4     | 1.22    | 4.0     | 8.0    | Q3            |
| SN74AUP1G14DCKT    | SC70         | DCK             | 5    | 250  | 178.0              | 9.2                | 2.4     | 2.4     | 1.22    | 4.0     | 8.0    | Q3            |
| SN74AUP1G14DCKT    | SC70         | DCK             | 5    | 250  | 180.0              | 8.4                | 2.47    | 2.3     | 1.25    | 4.0     | 8.0    | Q3            |
| SN74AUP1G14DPWR    | X2SON        | DPW             | 5    | 3000 | 180.0              | 8.4                | 0.91    | 0.91    | 0.5     | 2.0     | 8.0    | Q3            |
| SN74AUP1G14DRLR    | SOT-5X3      | DRL             | 5    | 4000 | 180.0              | 8.4                | 1.98    | 1.78    | 0.69    | 4.0     | 8.0    | Q3            |
| SN74AUP1G14DRYR    | SON          | DRY             | 6    | 5000 | 180.0              | 9.5                | 1.15    | 1.6     | 0.75    | 4.0     | 8.0    | Q1            |
| SN74AUP1G14DRYRG4  | SON          | DRY             | 6    | 5000 | 180.0              | 9.5                | 1.15    | 1.6     | 0.75    | 4.0     | 8.0    | Q1            |
| SN74AUP1G14DSFR    | SON          | DSF             | 6    | 5000 | 180.0              | 8.4                | 1.16    | 1.16    | 0.5     | 4.0     | 8.0    | Q2            |
| SN74AUP1G14DSFRG4  | SON          | DSF             | 6    | 5000 | 180.0              | 8.4                | 1.16    | 1.16    | 0.5     | 4.0     | 8.0    | Q2            |

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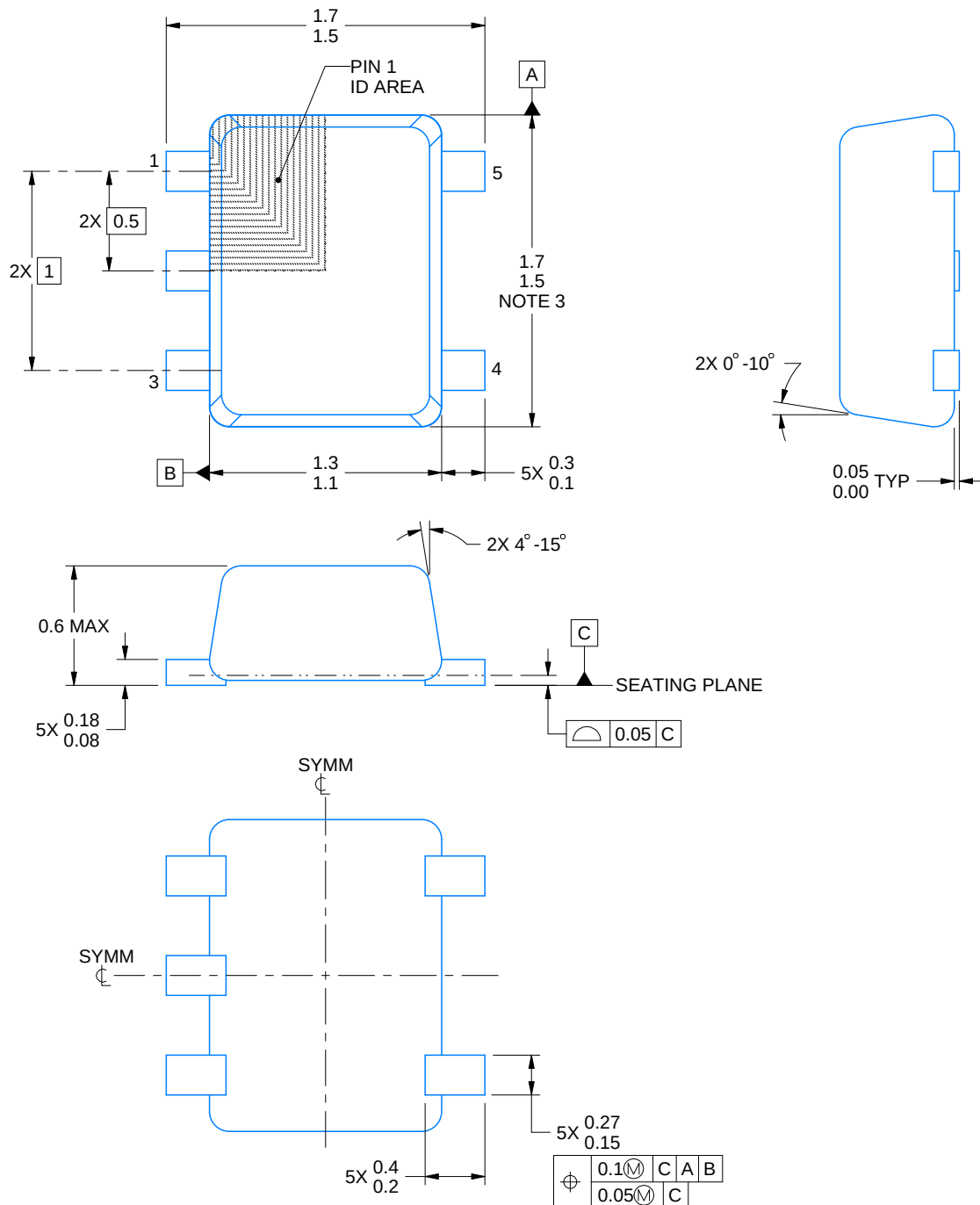
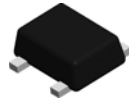
| Device          | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|-----------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| SN74AUP1G14YFPR | DSBGA        | YFP             | 4    | 3000 | 178.0              | 9.2                | 0.89    | 0.89    | 0.58    | 4.0     | 8.0    | Q1            |

## TAPE AND REEL BOX DIMENSIONS



\*All dimensions are nominal

| Device             | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|--------------------|--------------|-----------------|------|------|-------------|------------|-------------|
| SN74AUP1G14DBVR    | SOT-23       | DBV             | 5    | 3000 | 208.0       | 191.0      | 35.0        |
| SN74AUP1G14DBVRG4  | SOT-23       | DBV             | 5    | 3000 | 210.0       | 185.0      | 35.0        |
| SN74AUP1G14DBVT    | SOT-23       | DBV             | 5    | 250  | 210.0       | 185.0      | 35.0        |
| SN74AUP1G14DCKR    | SC70         | DCK             | 5    | 3000 | 210.0       | 185.0      | 35.0        |
| SN74AUP1G14DCKR    | SC70         | DCK             | 5    | 3000 | 208.0       | 191.0      | 35.0        |
| SN74AUP1G14DCKR1G4 | SC70         | DCK             | 5    | 3000 | 180.0       | 180.0      | 18.0        |
| SN74AUP1G14DCKR1G4 | SC70         | DCK             | 5    | 3000 | 202.0       | 201.0      | 28.0        |
| SN74AUP1G14DCKRG4  | SC70         | DCK             | 5    | 3000 | 180.0       | 180.0      | 18.0        |
| SN74AUP1G14DCKT    | SC70         | DCK             | 5    | 250  | 180.0       | 180.0      | 18.0        |
| SN74AUP1G14DCKT    | SC70         | DCK             | 5    | 250  | 202.0       | 201.0      | 28.0        |
| SN74AUP1G14DPWR    | X2SON        | DPW             | 5    | 3000 | 210.0       | 185.0      | 35.0        |
| SN74AUP1G14DRLR    | SOT-5X3      | DRL             | 5    | 4000 | 202.0       | 201.0      | 28.0        |
| SN74AUP1G14DRYR    | SON          | DRY             | 6    | 5000 | 184.0       | 184.0      | 19.0        |
| SN74AUP1G14DRYRG4  | SON          | DRY             | 6    | 5000 | 184.0       | 184.0      | 19.0        |
| SN74AUP1G14DSFR    | SON          | DSF             | 6    | 5000 | 210.0       | 185.0      | 35.0        |
| SN74AUP1G14DSFRG4  | SON          | DSF             | 6    | 5000 | 210.0       | 185.0      | 35.0        |
| SN74AUP1G14YFPR    | DSBGA        | YFP             | 4    | 3000 | 220.0       | 220.0      | 35.0        |



4220753/E 11/2024

## NOTES:

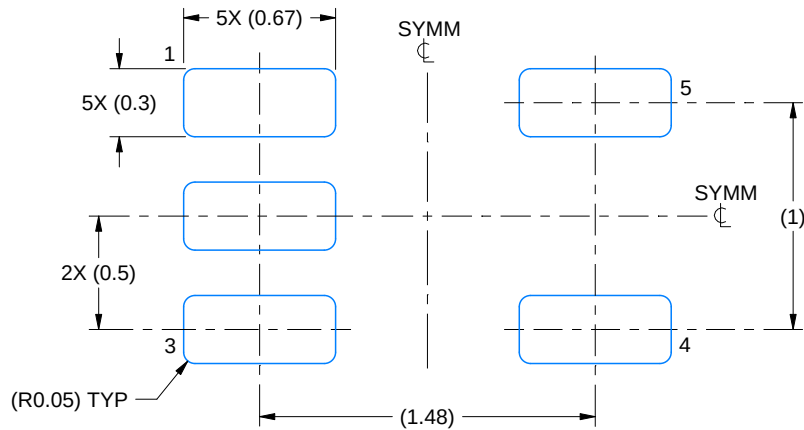
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 mm per side.
4. Reference JEDEC registration MO-293 Variation UAAD-1

# EXAMPLE BOARD LAYOUT

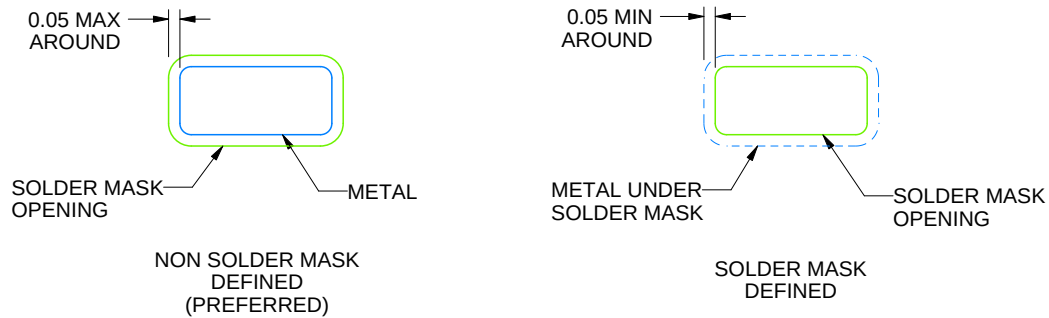
DRL0005A

SOT - 0.6 mm max height

PLASTIC SMALL OUTLINE



LAND PATTERN EXAMPLE  
SCALE:30X



SOLDERMASK DETAILS

4220753/E 11/2024

NOTES: (continued)

5. Publication IPC-7351 may have alternate designs.
6. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

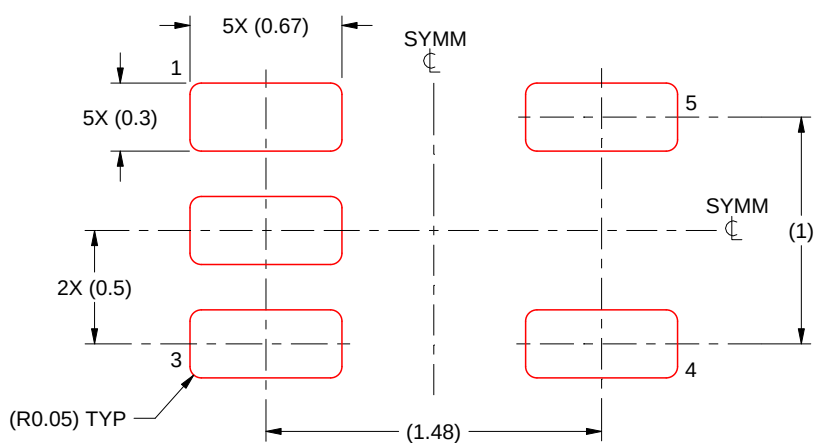


## EXAMPLE STENCIL DESIGN

DRL0005A

SOT - 0.6 mm max height

PLASTIC SMALL OUTLINE



SOLDER PASTE EXAMPLE  
BASED ON 0.1 mm THICK STENCIL  
SCALE:30X

4220753/E 11/2024

NOTES: (continued)

7. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
8. Board assembly site may have different recommendations for stencil design.

## GENERIC PACKAGE VIEW

**DPW 5**

**X2SON - 0.4 mm max height**

PLASTIC SMALL OUTLINE - NO LEAD



Images above are just a representation of the package family, actual package may vary.  
Refer to the product data sheet for package details.

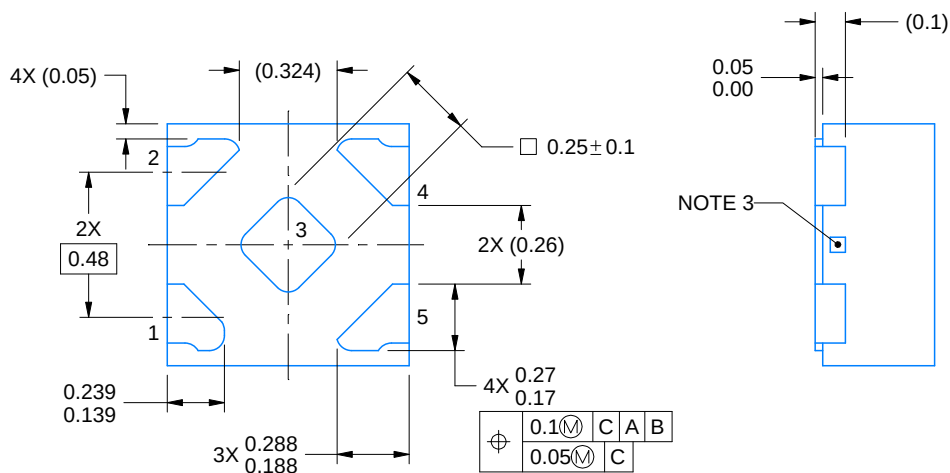
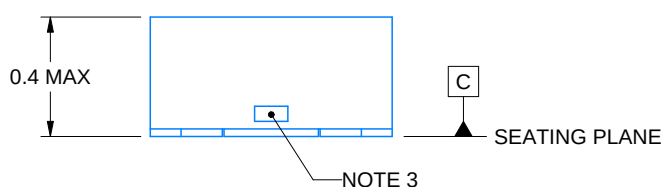
4211218-3/D



**X2SON - 0.4 mm max height**

Diagram illustrating the dimensions and location of the PIN 1 INDEX AREA on a rectangular component:

- The component is a rectangle with a width of 0.85 and a height of 0.75.
- The PIN 1 INDEX AREA is a shaded square region located in the top-left corner of the component.
- The distance from the left edge of the component to the right edge of the PIN 1 INDEX AREA is 0.85.
- The distance from the top edge of the component to the bottom edge of the PIN 1 INDEX AREA is 0.75.
- Labels A and B are positioned at the top corners of the component, with arrows indicating the dimensions.



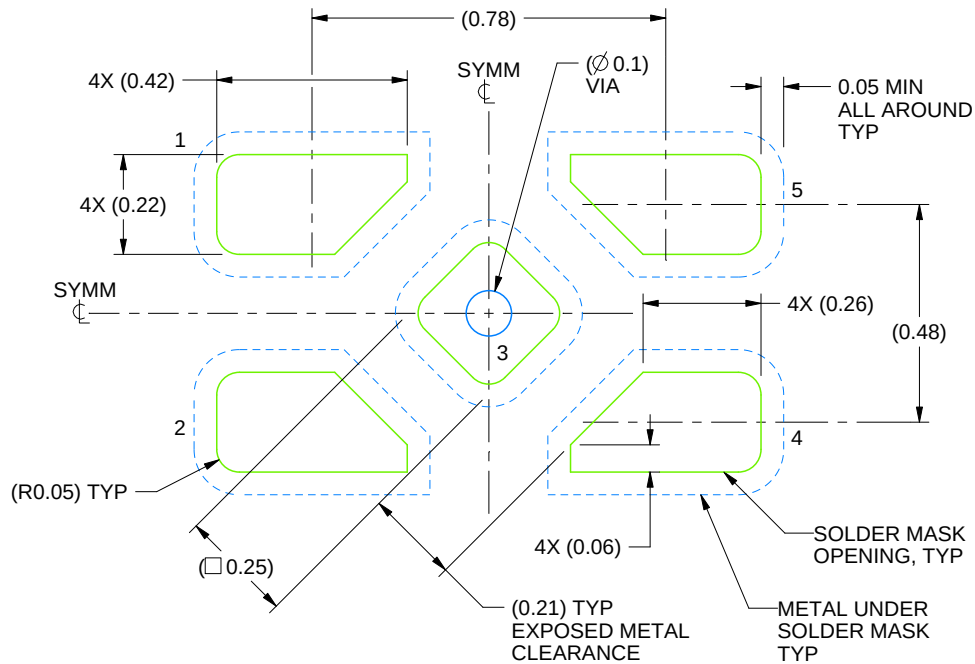
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The size and shape of this feature may vary.

**DPW0005A**

**X2SON - 0.4 mm max height**

PLASTIC SMALL OUTLINE - NO LEAD



LAND PATTERN EXAMPLE  
SOLDER MASK DEFINED  
SCALE:60X

4223102/D 03/2022

NOTES: (continued)

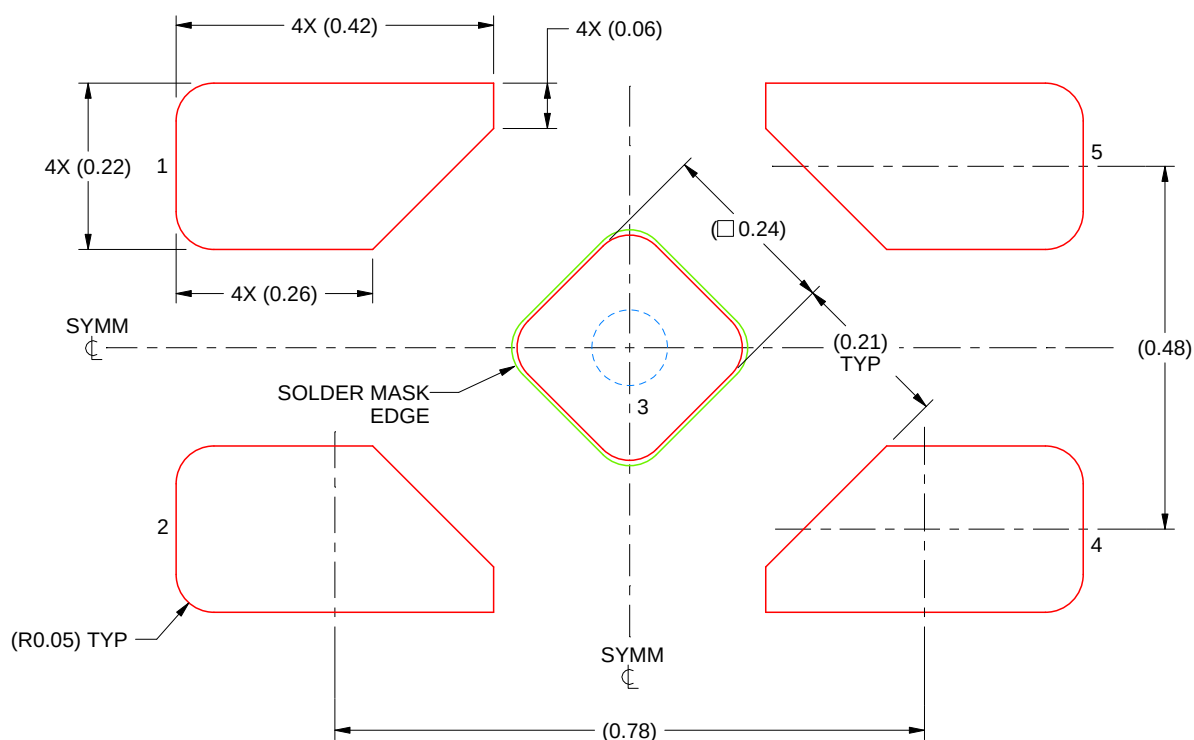
4. This package is designed to be soldered to a thermal pad on the board. For more information, refer to QFN/SON PCB application note in literature No. SLUA271 ([www.ti.com/lit/slua271](http://www.ti.com/lit/slua271)).

# EXAMPLE STENCIL DESIGN

DPW0005A

X2SON - 0.4 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



SOLDER PASTE EXAMPLE  
BASED ON 0.1 mm THICK STENCIL

EXPOSED PAD 3  
92% PRINTED SOLDER COVERAGE BY AREA UNDER PACKAGE  
SCALE:100X

4223102/D 03/2022

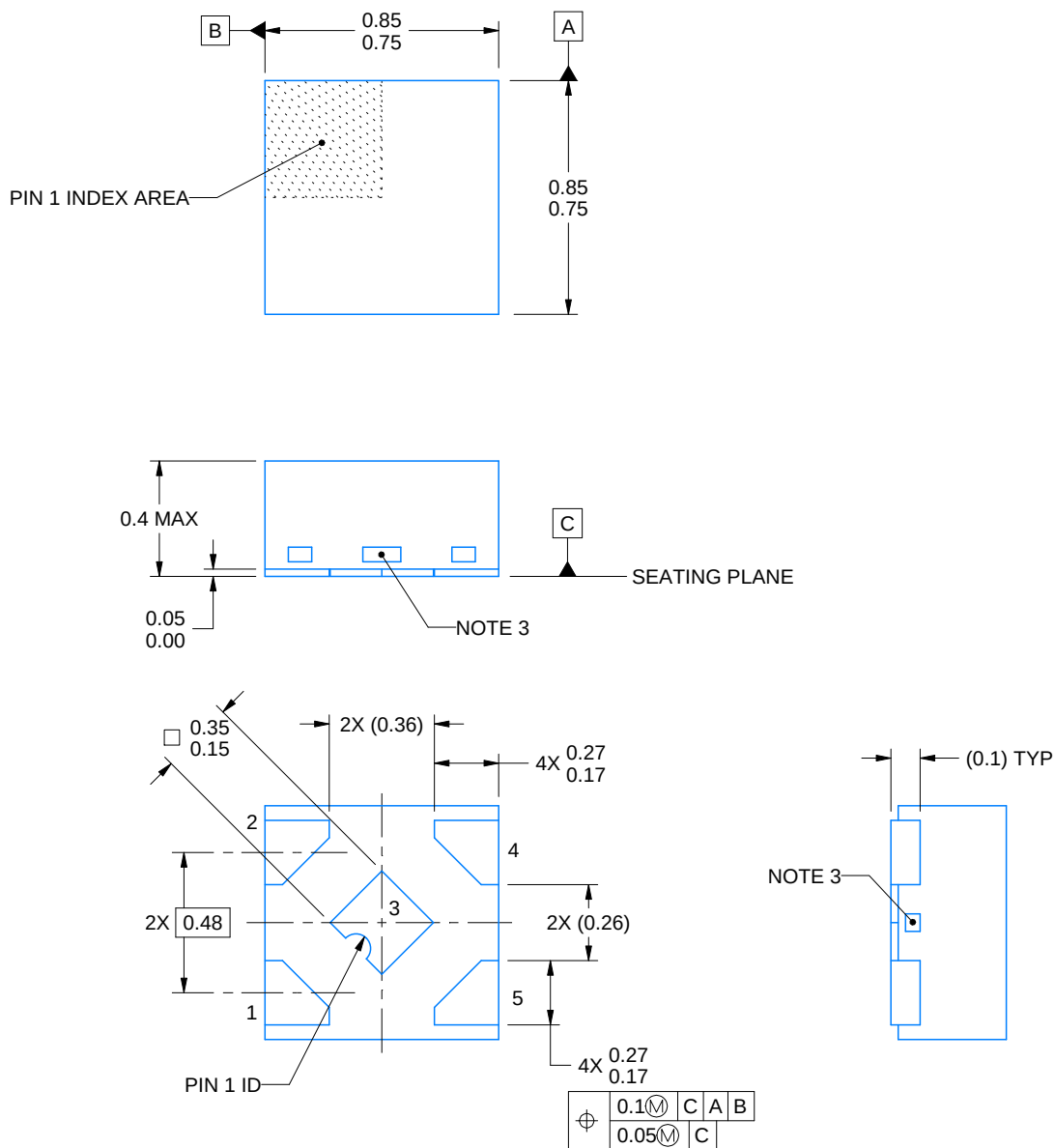
NOTES: (continued)

5. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.



**X2SON - 0.4 mm max height**

PLASTIC SMALL OUTLINE - NO LEAD



4228233/D 09/2023

NOTES:

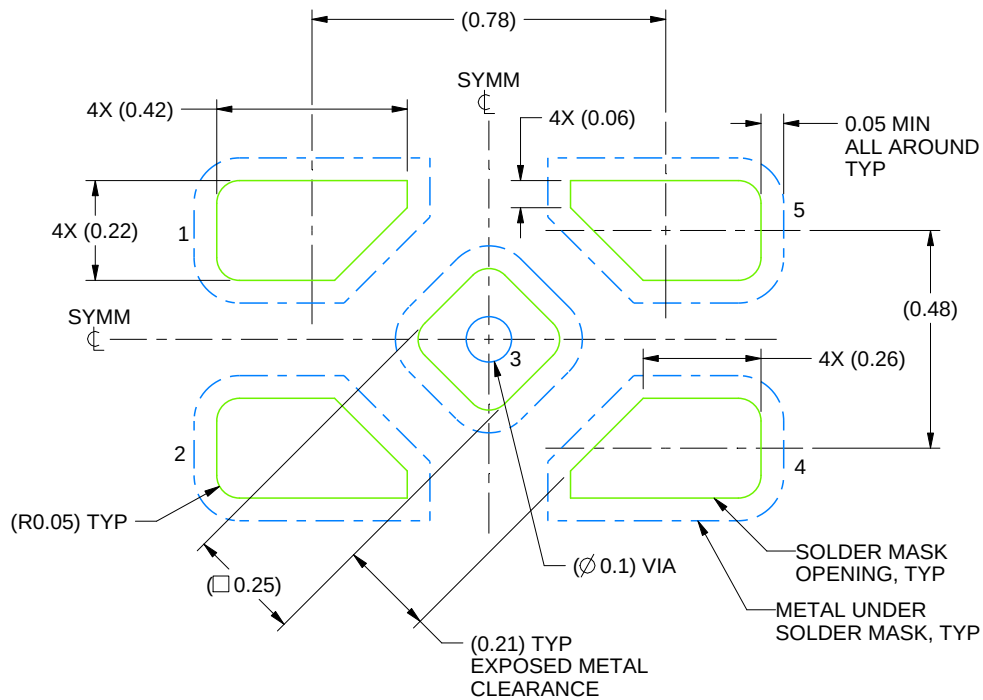
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The size and shape of this feature may vary.

# EXAMPLE BOARD LAYOUT

DPW0005B

X2SON - 0.4 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



LAND PATTERN EXAMPLE  
SOLDER MASK DEFINED  
SCALE: 60X

4228233/D 09/2023

NOTES: (continued)

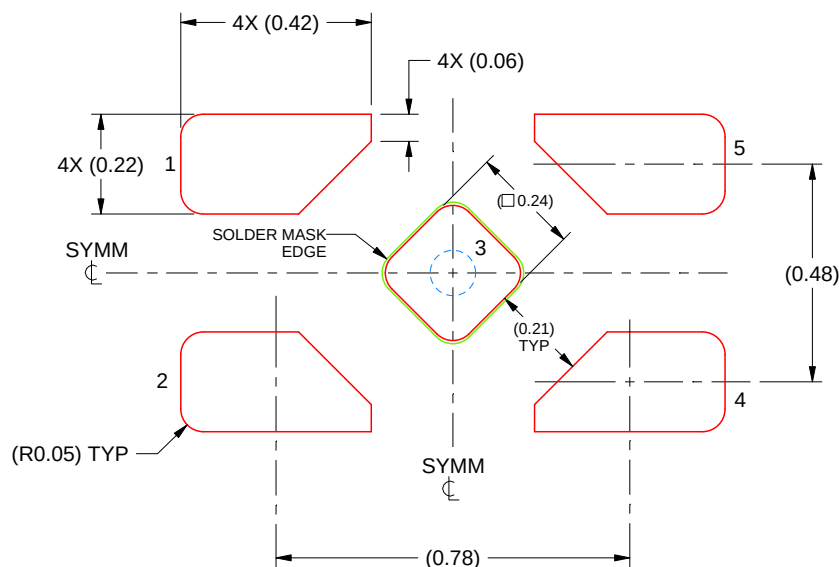
4. This package is designed to be soldered to a thermal pad on the board. For more information, refer to QFN/SON PCB application note in literature No. SLUA271 ([www.ti.com/lit/sl原因271](http://www.ti.com/lit/sl原因271)).

# EXAMPLE STENCIL DESIGN

DPW0005B

X2SON - 0.4 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



SOLDER PASTE EXAMPLE  
BASED ON 0.1 mm THICK STENCIL

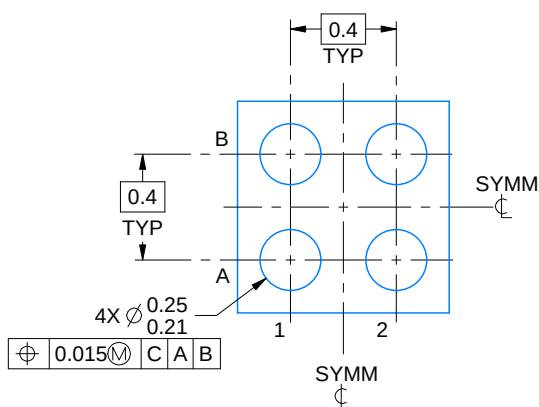
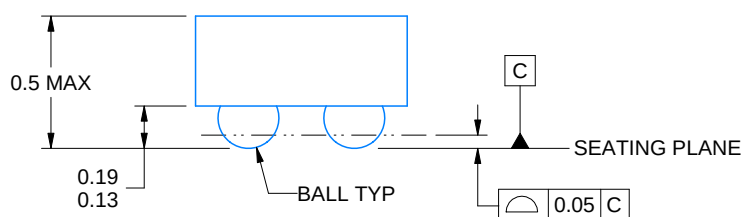
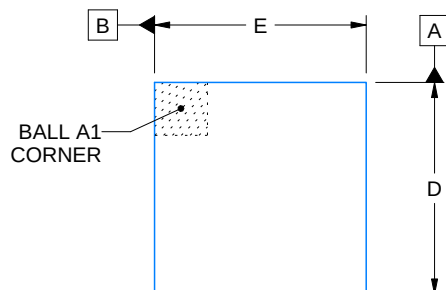
EXPOSED PAD 5  
92% PRINTED SOLDER COVERAGE BY AREA UNDER PACKAGE  
SCALE:60X

4228233/D 09/2023

NOTES: (continued)

5. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.





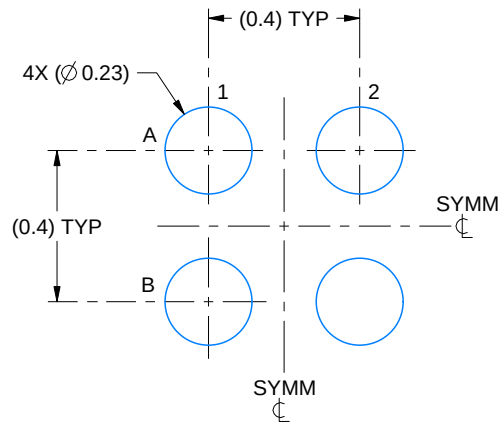
D: Max = 0.79 mm, Min = 0.73 mm

E: Max = 0.79 mm, Min = 0.73 mm

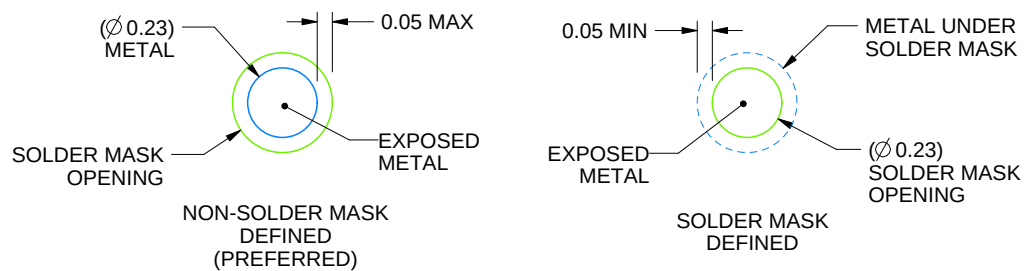
4223507/A 01/2017

#### NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.



LAND PATTERN EXAMPLE  
EXPOSED METAL SHOWN  
SCALE:50X



SOLDER MASK DETAILS  
NOT TO SCALE

4223507/A 01/2017

NOTES: (continued)

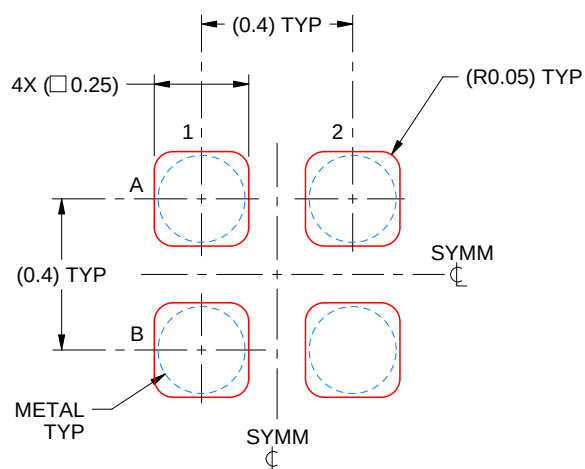
- Final dimensions may vary due to manufacturing tolerance considerations and also routing constraints. For more information, see Texas Instruments literature number SNVA009 ([www.ti.com/lit/snva009](http://www.ti.com/lit/snva009)).

## EXAMPLE STENCIL DESIGN

YFP0004

DSBGA - 0.5 mm max height

DIE SIZE BALL GRID ARRAY

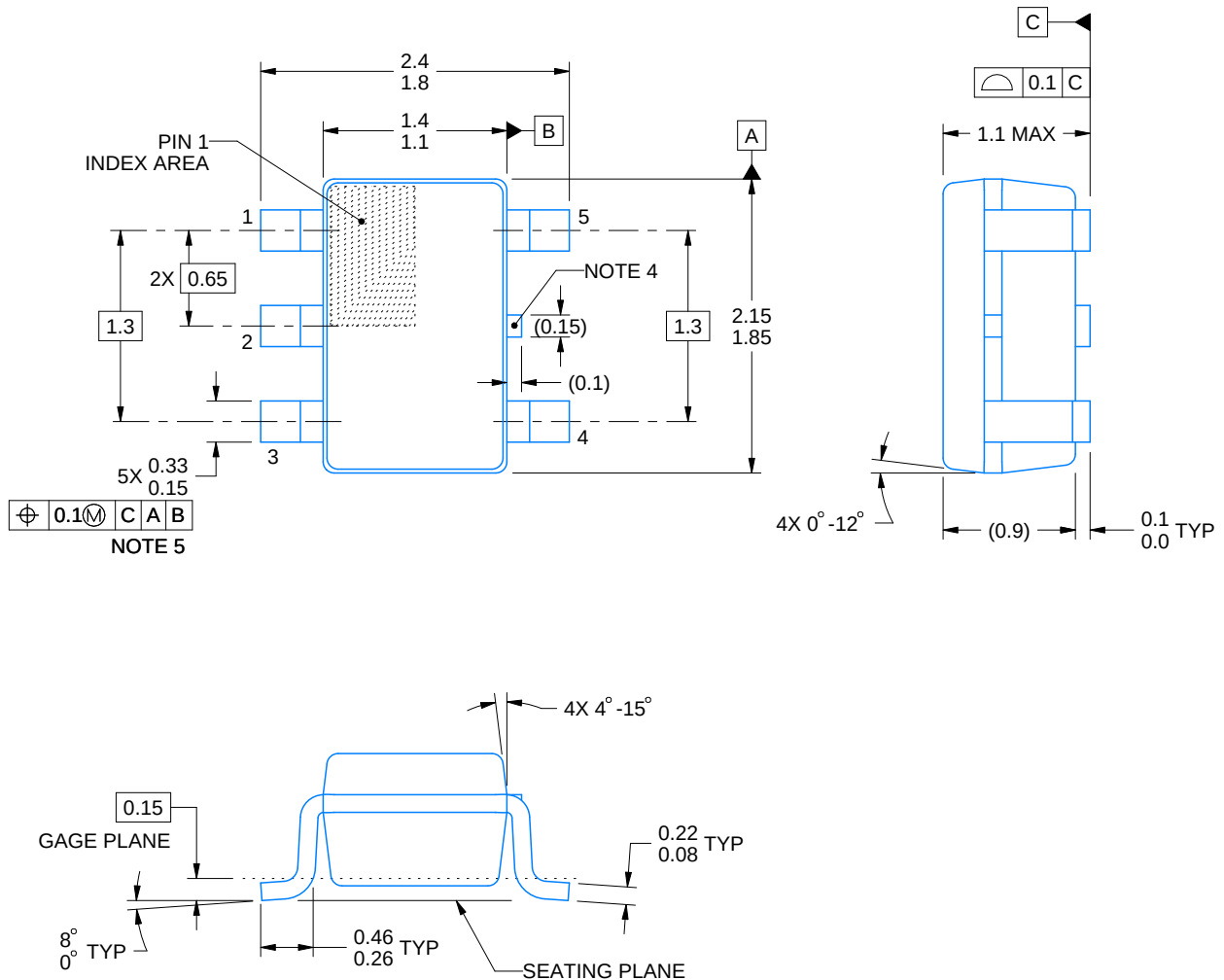


SOLDER PASTE EXAMPLE  
BASED ON 0.1 mm THICK STENCIL  
SCALE:50X

4223507/A 01/2017

NOTES: (continued)

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release.



4214834/G 11/2024

## NOTES:

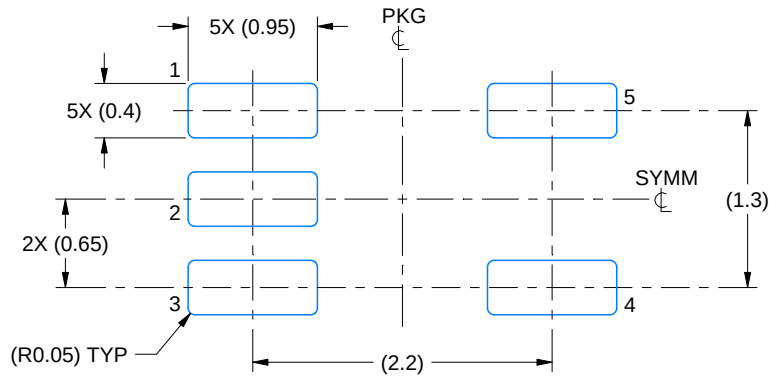
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC MO-203.
4. Support pin may differ or may not be present.
5. Lead width does not comply with JEDEC.
6. Body dimensions do not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.25mm per side

# EXAMPLE BOARD LAYOUT

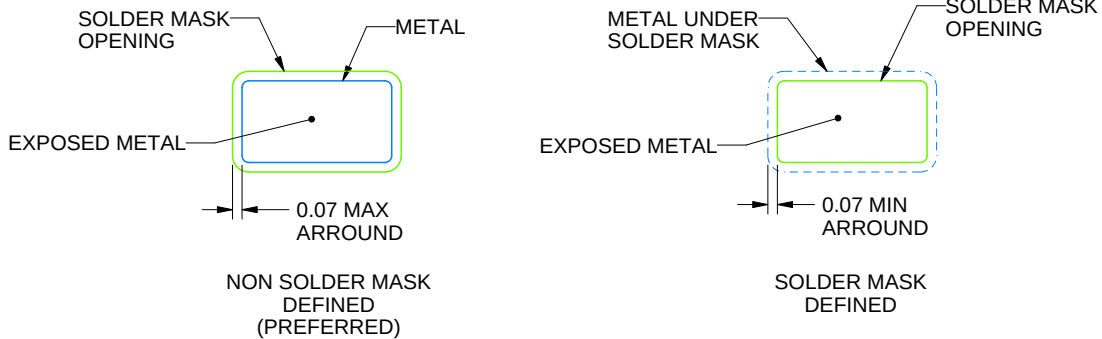
DCK0005A

SOT - 1.1 max height

SMALL OUTLINE TRANSISTOR



LAND PATTERN EXAMPLE  
EXPOSED METAL SHOWN  
SCALE:18X



SOLDER MASK DETAILS

4214834/G 11/2024

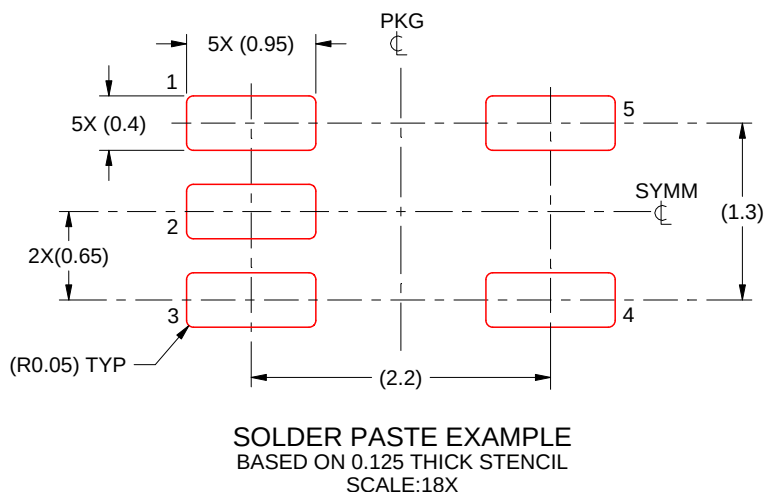
NOTES: (continued)

7. Publication IPC-7351 may have alternate designs.
8. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

**DCK0005A**

### SOT - 1.1 max height

### SMALL OUTLINE TRANSISTOR



4214834/G 11/2024

NOTES: (continued)

9. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
10. Board assembly site may have different recommendations for stencil design.

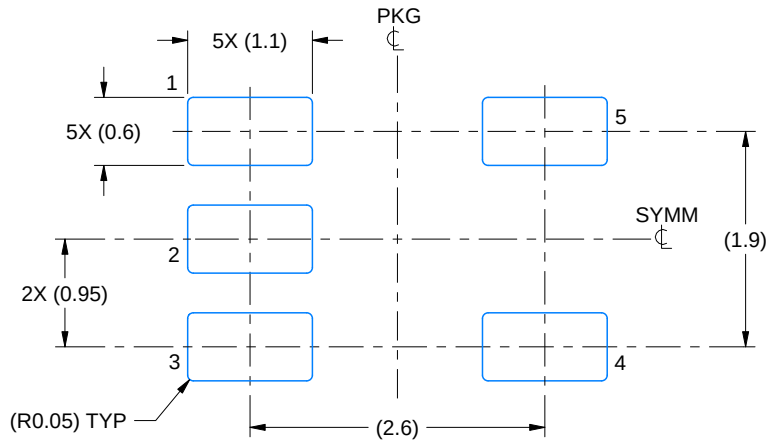


# EXAMPLE BOARD LAYOUT

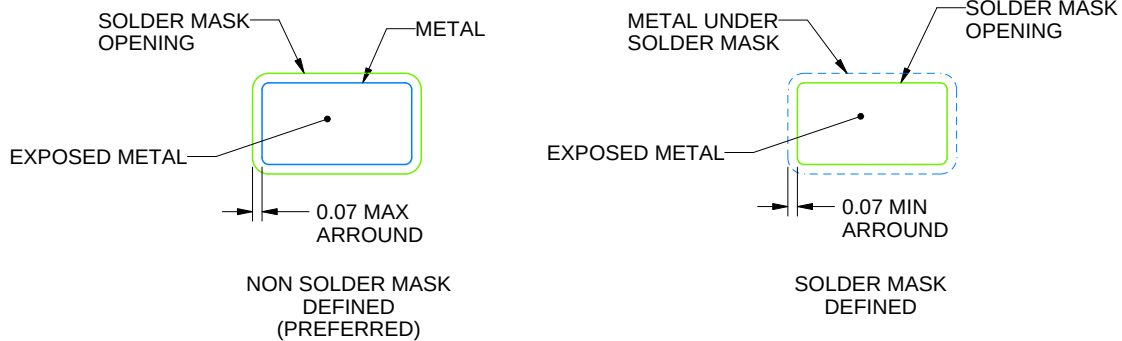
DBV0005A

SOT-23 - 1.45 mm max height

SMALL OUTLINE TRANSISTOR



LAND PATTERN EXAMPLE  
EXPOSED METAL SHOWN  
SCALE:15X



SOLDER MASK DETAILS

4214839/K 08/2024

NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

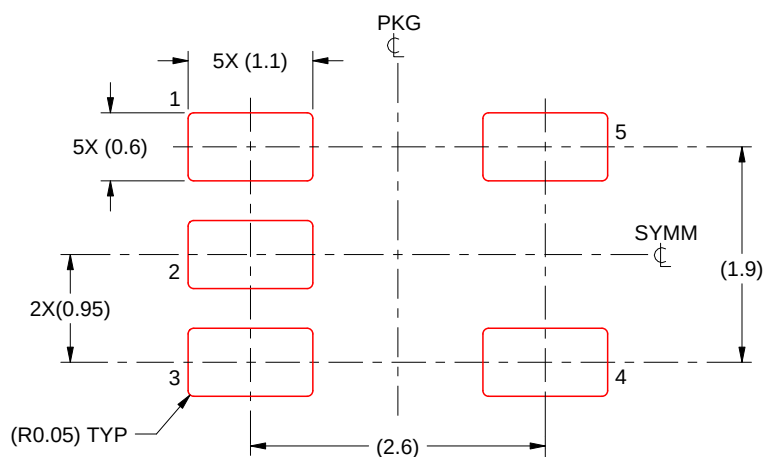


## EXAMPLE STENCIL DESIGN

DBV0005A

SOT-23 - 1.45 mm max height

SMALL OUTLINE TRANSISTOR



SOLDER PASTE EXAMPLE  
BASED ON 0.125 mm THICK STENCIL  
SCALE:15X

4214839/K 08/2024

NOTES: (continued)

8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
9. Board assembly site may have different recommendations for stencil design.

## GENERIC PACKAGE VIEW

**DRY 6**

**USON - 0.6 mm max height**

PLASTIC SMALL OUTLINE - NO LEAD



Images above are just a representation of the package family, actual package may vary.  
Refer to the product data sheet for package details.

4207181/G

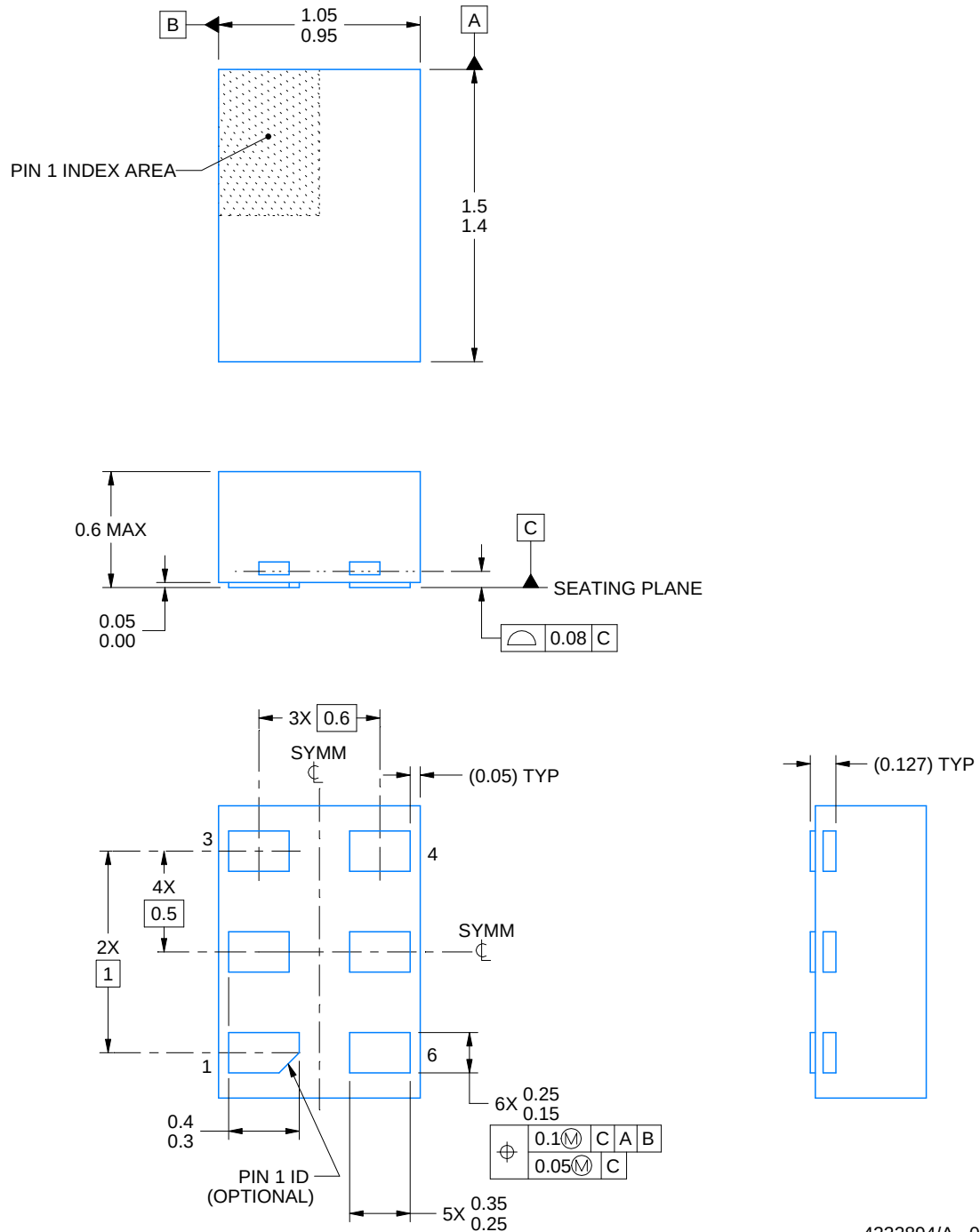
DRY0006A



## PACKAGE OUTLINE

USON - 0.6 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



4222894/A 01/2018

### NOTES:

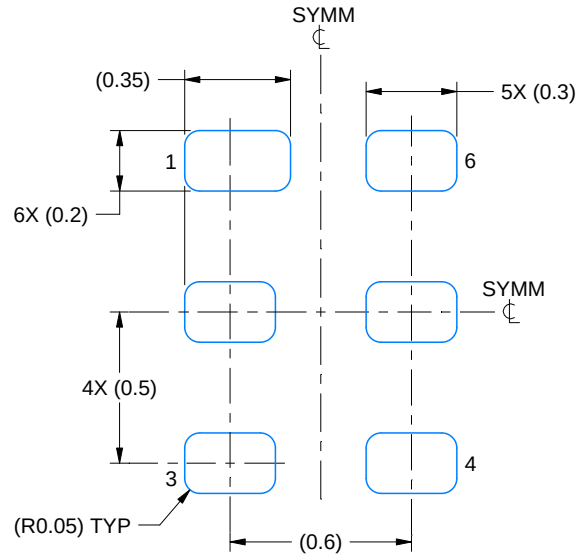
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.

# EXAMPLE BOARD LAYOUT

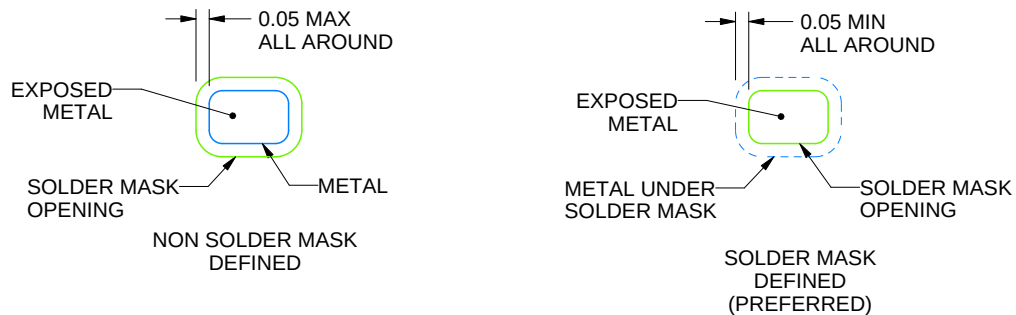
DRY0006A

USON - 0.6 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



**LAND PATTERN EXAMPLE**  
1:1 RATIO WITH PKG SOLDER PADS  
EXPOSED METAL SHOWN  
SCALE:40X



**SOLDER MASK DETAILS**

4222894/A 01/2018

NOTES: (continued)

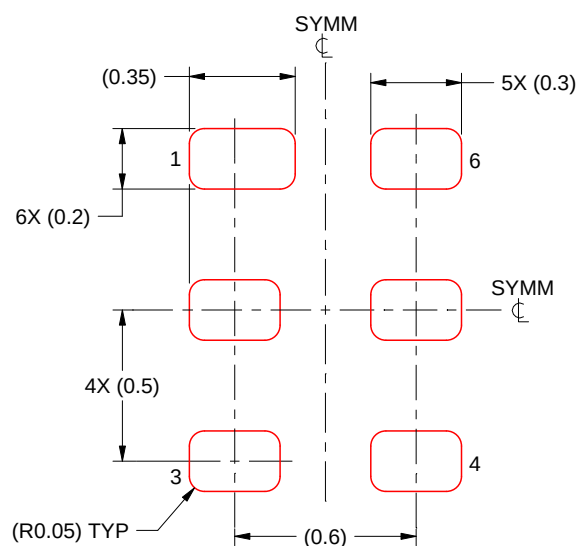
3. For more information, see QFN/SON PCB application report in literature No. SLUA271 ([www.ti.com/lit/slue271](http://www.ti.com/lit/slue271)).

## EXAMPLE STENCIL DESIGN

DRY0006A

USON - 0.6 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



SOLDER PASTE EXAMPLE  
BASED ON 0.075 - 0.1 mm THICK STENCIL  
SCALE:40X

4222894/A 01/2018

NOTES: (continued)

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

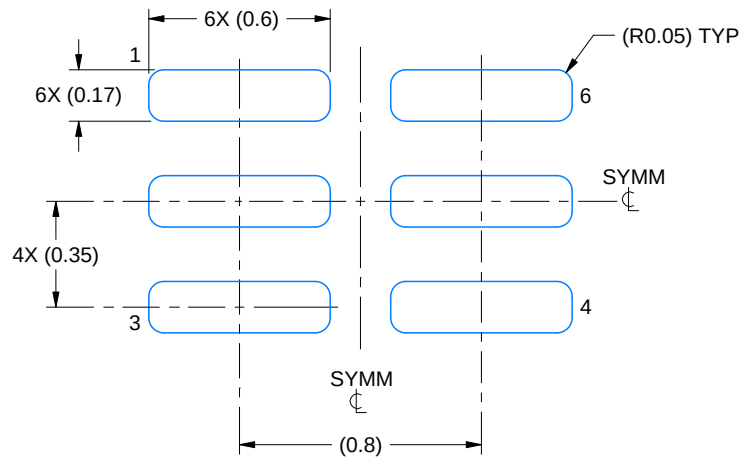


# EXAMPLE BOARD LAYOUT

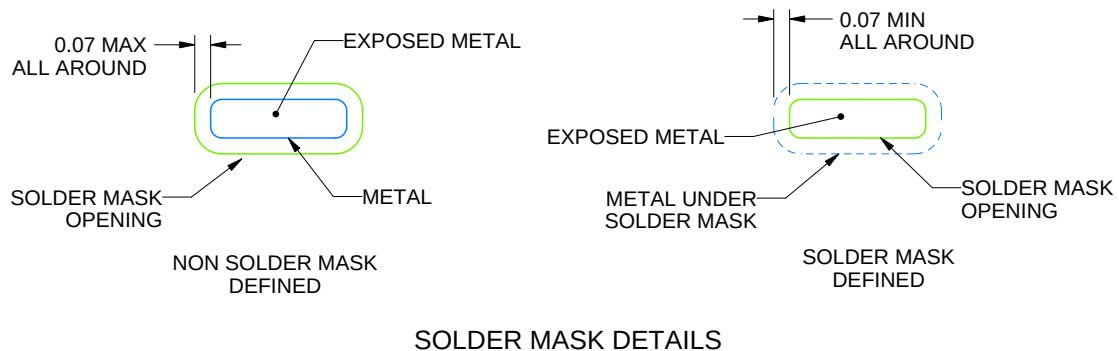
DSF0006A

X2SON - 0.4 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



LAND PATTERN EXAMPLE  
EXPOSED METAL SHOWN  
SCALE:40X



SOLDER MASK DETAILS

4220597/B 06/2022

NOTES: (continued)

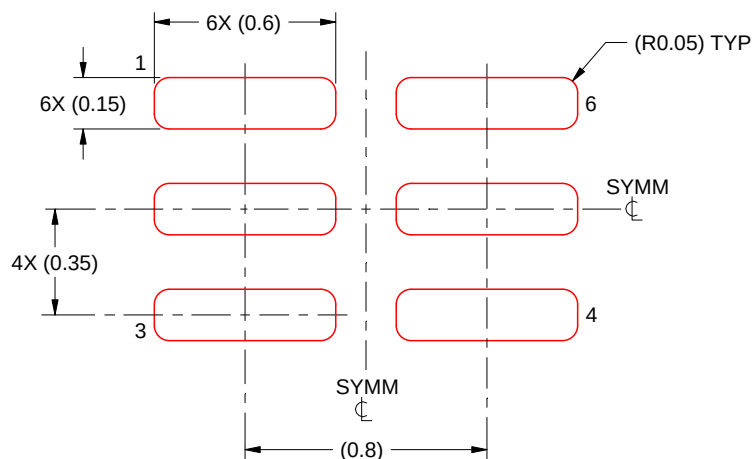
4. For more information, see Texas Instruments literature number SLUA271 ([www.ti.com/lit/sluea271](http://www.ti.com/lit/sluea271)).

## EXAMPLE STENCIL DESIGN

DSF0006A

X2SON - 0.4 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



SOLDER PASTE EXAMPLE  
BASED ON 0.09 mm THICK STENCIL

PRINTED SOLDER COVERAGE BY AREA UNDER PACKAGE  
SCALE:40X

4220597/B 06/2022

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.



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