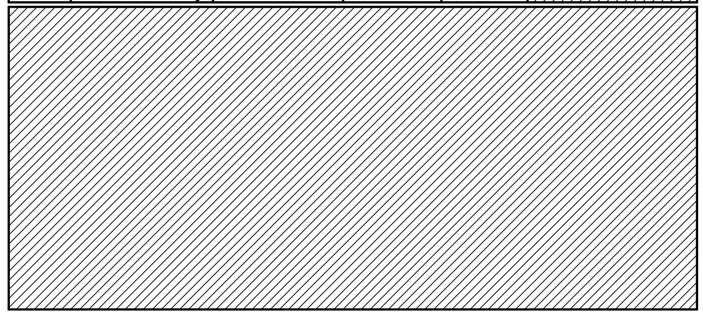
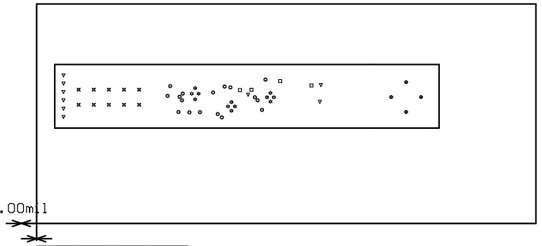
Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0.40mil	3.5	
3	Top Layer	Copper	1.40mil		
4	Dielectric 1	FR-4	8.00mil	4.2	
5	Signal Layer 1	Copper	1.40mil		
6	Dielectric 2	FR-4	40.00mil	4.2	
7	Signal Layer 2	Copper	1.40mil		
8	Dielectric 3	FR-4	8.00mil	4.2	
9	Bottom Layer	Copper	1.40mil		
10	Bottom Solder	Solder Resist	0.40mil	3.5	
11	Bottom Overlay				





Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
	12	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
	17	10.00mil (0.254mm)	PTH	Round	Top Layer - Bottom Layer	
	4	12.00mil (0.305mm)	PTH	Round	Top Layer - Bottom Layer	
	9	15.00mil (0.381mm)	PTH	Round	Top Layer - Bottom Layer	
	10	40.16mil (1.020mm)	PTH	Round	Top Layer - Bottom Layer	
	4	49.21mil (1.250mm)	PTH	Round	Top Layer - Bottom Layer	

DESIGN INFORMATION

MIN. TRACK WIDTH: 8 MIL
MIN. CLEARANCE: 0.2 mm
MIN. VIA PAD SIZE: 20 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL
HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3 MIL

MATERIAL:
[ ] FR-408 [X] FR-4 High Tg [ ] OTHER _____
THICKNESS: [X] 62 MIL (1.6mm) +/-10% [ ] OTHER _____
TOLERANCE: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/- _____
BOW & TWIST: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/- _____

DRILLING:
REFERENCE: [X] AS SHOWN [X] NC_DRILL FILES
PTH COPPER THICKNESS: [X] 20-30 um [ ] OTHER _____

BOARD FINISH:
SILKSCREEN: [X] TOP [X] BOTTOM
SILKSCREEN COLOR: [X] WHITE [ ] OTHER _____
SOLDER RESIST COLOR: [X] GREEN [ ] OTHER _____
[X] MATTE [ ] SEMI-GLOSS
SURFACE FINISH: [X] IMMERSION GOLD (ENIG) [ ] ENEPIG
[ ] IMM. TIN/SILVER OR EQUIV [ ] OTHER _____
ARRAY/PANEL: [X] CUT AND TRIM PER M1 BOARD OUTLINE
[ ] N.C. ROUTE [X] V. SCORE
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF:
[X] ANSI IPC-A-600F CLASS -> [ ] 1 [X] 2 [ ] 3
[X] RoHS [ ] OTHER PER ORDER
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS.
PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER

ADDITIONAL REQUIREMENTS:
MICROSECTION: [ ] YES
BARE BOARD ELEC. TEST: [ ] NONE [X] REQUIRED [ ] PER ORDER

TEXAS INSTRUMENTS

PROJECT TITLE: Change in menu Project|Project Options|Parameters
DESIGNED FOR: Public Release
FILE NAME: TIDA-01478_PCB.PcbDoc
ENGINEER: Dushmantha Rajapaksha
LAYOUT BY: Daniel Noel
SCALE: 1.00
ALTIUM DESIGNER VERSION: 16.1.12.290

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-01478	REV:	SVN REV: Not In VersionControl
LAYER NAME = [illegible]ions	TID #: TIDA-01478		
PLOT NAME = Fabrication Drawing	GENERATED : 6/2/2017 9:18:52 AM	TEXAS INSTRUMENTS	

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