**TIDA-01401-Altium Design Changes**

Notes:

The latest changes are always at the beginning of the revision history

Notation: Schematics-version**.**PCB-version

Rev Date Who What

1.0 5/05 KNM changed L1 change due to obsolete inductor new inductor is 74479775210 from wurth

1.0 5/05 KNM swapped signal 3V3\_RX\_DATA and 3V3\_TX\_DATA on J4

1.0 5/05 KNM Changed package on U2 from DRV to DBV, DRV does not exist

1.0 5/05 KNM Removed UART Isolator

1.0 5/05 KNM Added GND test points for measurements TP

1.0 5/05 KNM Added connector 1x8 pins for the encoder connector.

1.0 5/16 KNM Updated PCB rules and board layer per email from GCD

1.0 5/31 KNM Added two resistors on U5 RX and TX path to be able to swap pins when using board as

SPI master

1.0 6/01 KNM Change capacitor C20 and C30 to resistor R12 and R49 0Ohm

1.0 6/19 KNM Updated Annotation and DNP option of board

1.0 7/05 KNM Changed enable circuit for load switch

1.0 7/05 KNM ADDed 0ohm resistor between sub-d and 8 pin connector

2.0 8/09 KNM R48 connected to 3v3 and not GND

2.0 8/09 KNM Changed C1 and C8 to capacitors who are RoHS and Reach compliant