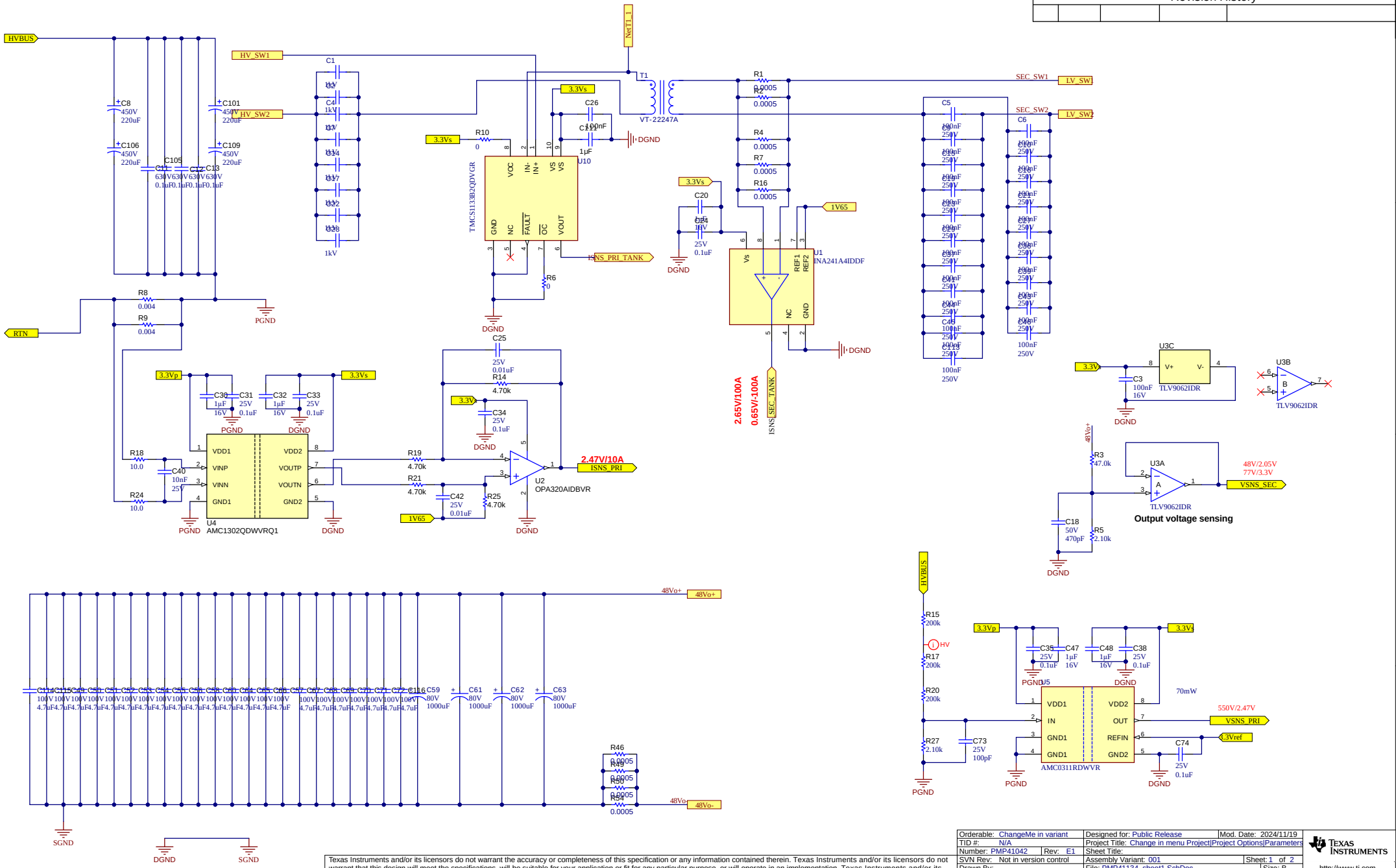
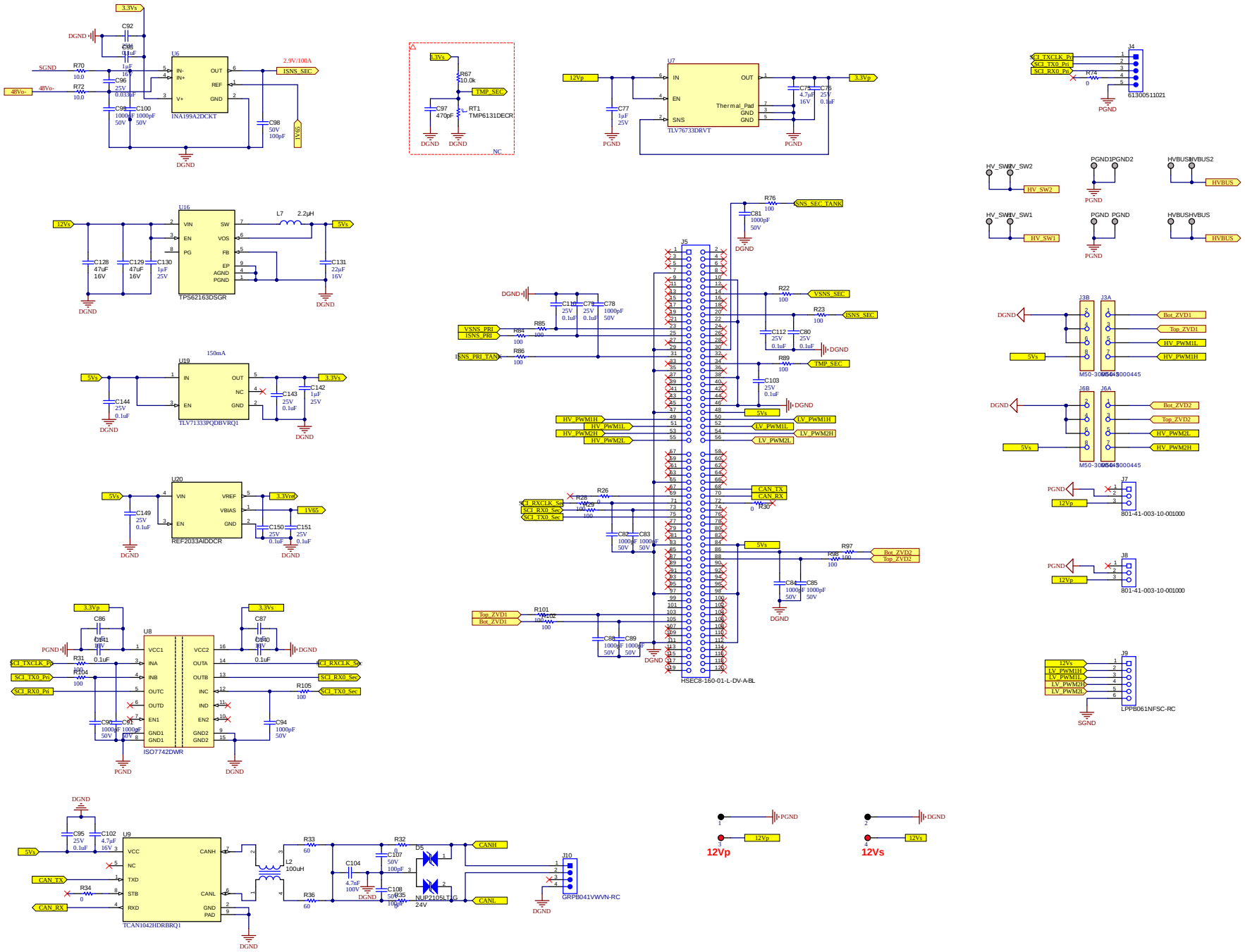

Revision History
HVBUS
HV_SW1
HV_SW2
RTN
PGND
DGND
SGND
3.3Vs
3.3Vp
1V65
T1
VT-22247A
U10
TMCS1133B2QDVGR
U1
INA241A4IDDF
U2
OPA320AIDBVR
U3
AMC1302QDWVRQ1
U3A
U3B
U3C
TLV9062IDR
U4
AMC1302QDWVRQ1
U5
AMC0311RDWVR
ISNS_PRI_TANK
ISNS_SEC_TANK
2.65V/100A
0.65V/-100A
2.47V/10A
ISNS_PRI
SEC_SW1
SEC_SW2
LV_SW
LV_SW2
48Vo+
48Vo-
48V/2.05V
77V/3.3V
VSNS_SEC
VSNS_PRI
550V/2.47V
4.3Vref
70mW
Output voltage sensing
Orderable: ChangeMe in variant
TID #: N/A
Number: PMP41042 Rev: E1
SVN Rev: Not in version control
Drawn By:
Engineer: Guangzhi Cui
Designed for: Public Release
Mod. Date: 2024/11/19
Project Title: Change in menu Project|Project Options|Parameters
Sheet Title:
Assembly Variant: 001
Sheet: 1 of 2
File: PMP41134_sheet1.SchDoc
Size: B
Contact: http://www.ti.com/support
http://www.ti.com
Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.
© Texas Instruments 2018



U6
INA199A2DCKT
U7
TLV76733DRVT
U16
TPS62163DSGR
U19
TLV71333PQDBVRQ1
U20
REF2033AIDDCR
U8
ISO7742DWR
U9
TCAN1042HDRBRQ1
J5
HSEC8-160-01-L-DV-A-BL
J4
61300511021
J3B
J3A
J6B
J6A
J7
801-41-003-10-001000
J8
801-41-003-10-001000
J9
LPPB061NFSC-RC
J10
GRPB041VWVN-RC
D5
NUP2105LT1G
12Vp
12Vs