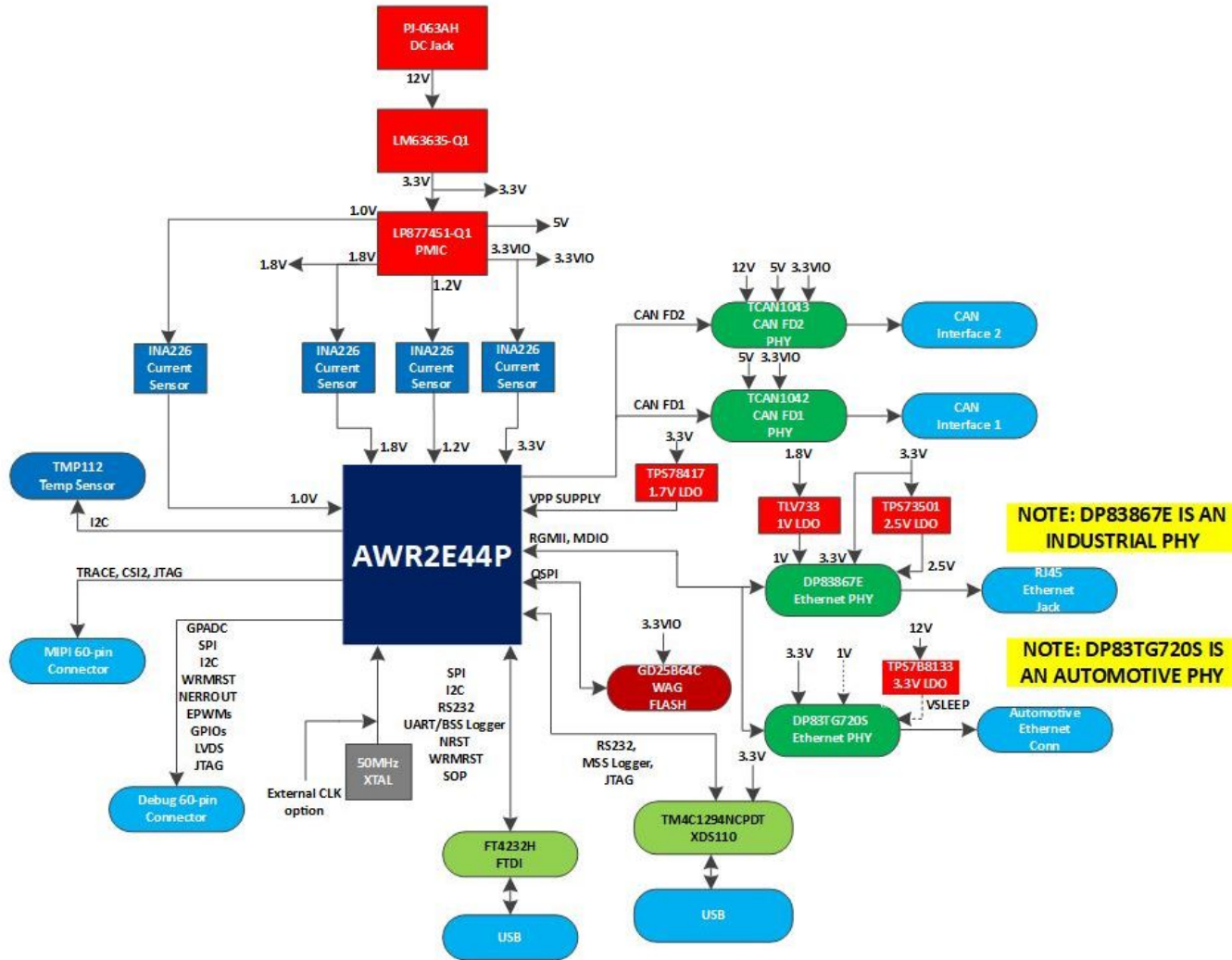


## BLOCK DIAGRAM



## Revision History

Rev	ECN #	Approved Date	Approved by	Notes
REV A	01	30-04-2024		Taken AWR2E44PEVM REVA Design and updated to AWR2E44P
REV A	02	21-05-2024		Changed U4 to 1Gbps AUTO ETH PHY with BOM Updates
REV A	03	25-06-2024		Changed Y3 to 50MHz R301, R309 made mountable and R303, R312 DNP
REV A	04	30-08-2024		Updated Block Diagram with 50MHz Updated L6& D11 part numbers

## TABLE OF CONTENTS

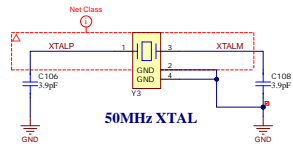
SHEET NO.	SHEET NAME
1	COVER SHEET
2	IO REFERENCE
3	PWR REFERENCE
4	DECOUPLING REFERENCE
5	QSPI FLASH REFERENCE
6	PMIC REFERENCE
7	3V3 SUPPLY REFERENCE
8	SOP REFERENCE
9	PWR_RST_LED
10	VPP LDO
11	ETHERNET_PWR
12	ETHERNET_PHY
13	ETHERNET_MAGNETICS
14	AUTO_ETHERNET_PHY
15	AUTO_ETHERNET_CONN
16	FTDI_PWR
17	FTDI
18	XDS110_INTERFACE_1A
19	XDS110_INTERFACE_1B
20	JTAG_EMU_CONNECTOR
21	DEBUG_CONNECTOR
22	CAN_INTERFACE
23	CURRENT_SENSORS
24	TEMP_SENSORS
25	HARDWARE

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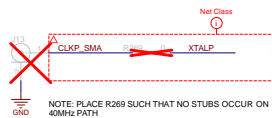
Orderable: AWR2E44PEVM	Designed for: Public Release	Mod. Date: 8/30/2024
TID #: N/A	Project Title: AWR2E44PEVM	
Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 25
Drawn By:	File: PROC196A_CoverSheet_SchDoc	Size: B
Engineer: Sami Mardini	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	


## AWR2E44P IO REFERENCE

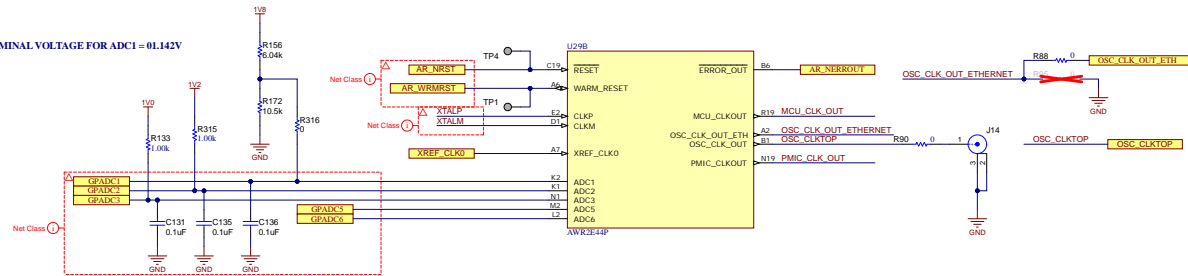
## 50MHZ CLOCK SOURCES



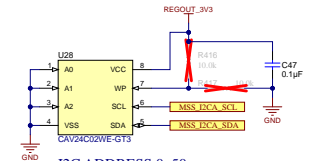
## RESET, ERROR, CLKOUT, GPADC, CLK



 NOTE: PLACE R269 SUCH THAT NO STUBS OCCUR ON 40MHz PATH

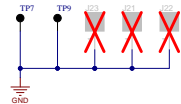


## BOARD ID EEPROM

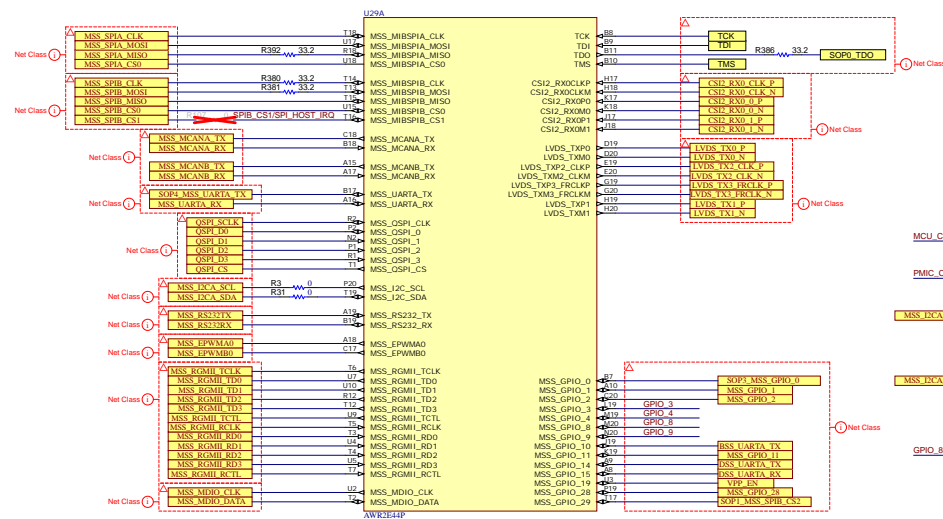


I2C ADDRESS 0x50

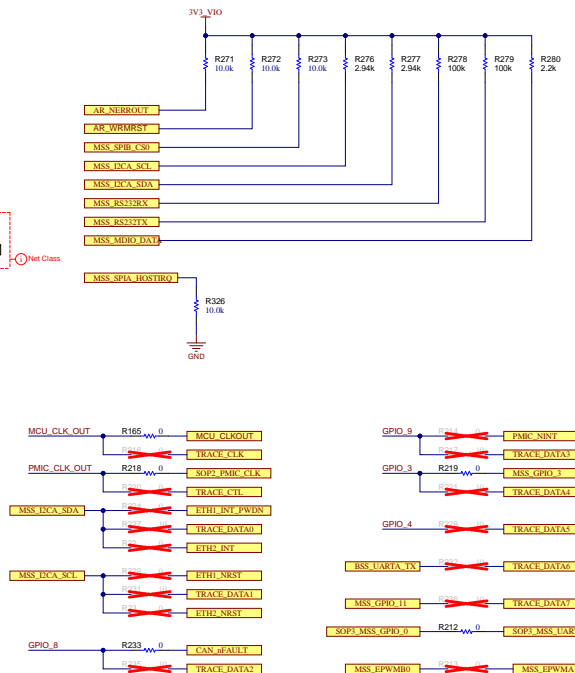
## GND TEST POINTS



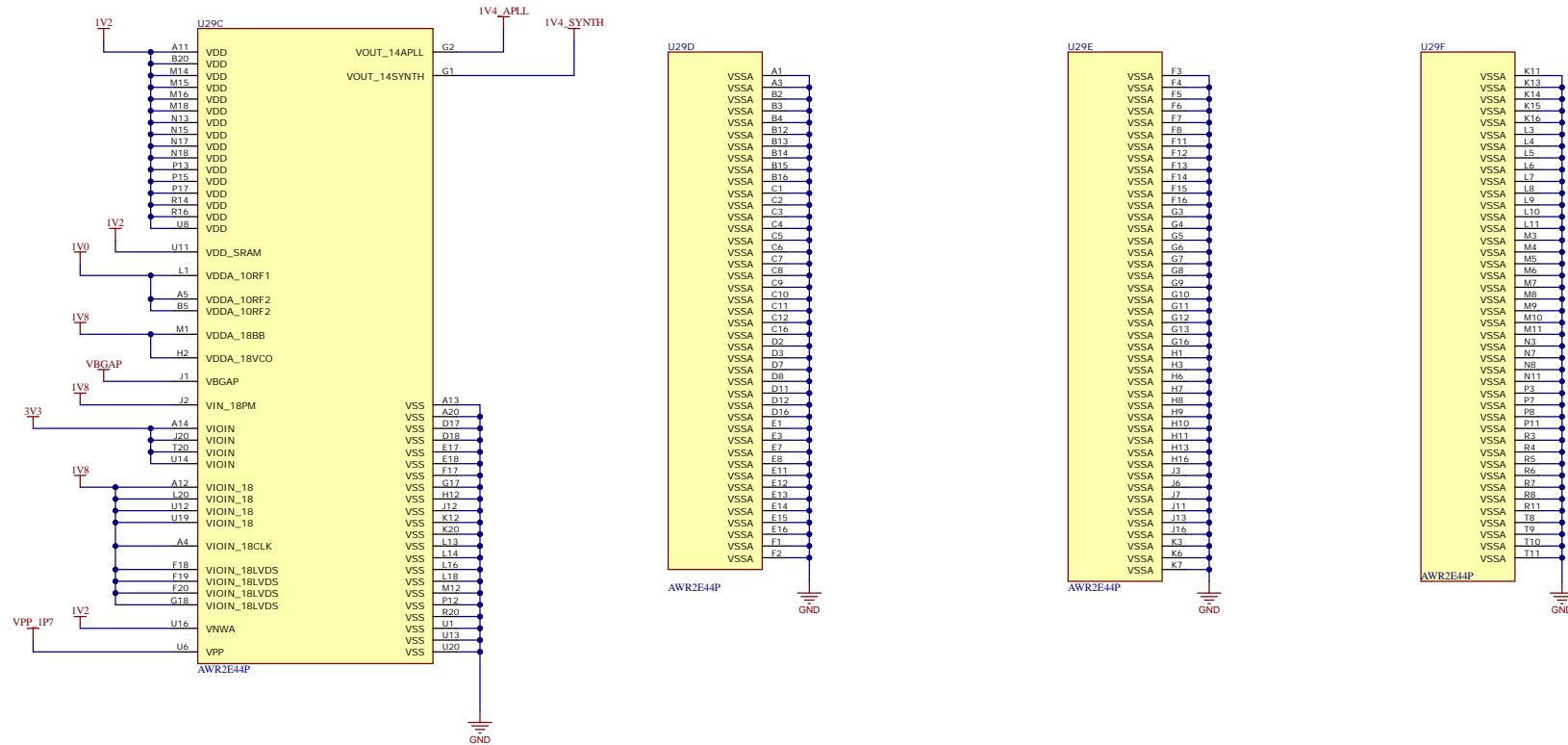
**CAN, MDIO, SPI, QSPI, UART, EPWM, RGMII, CSI, LVDS, GPIO, JTAG**



## PULLUPS/DOWNS



# AWR2E44P POWER REFERENCE

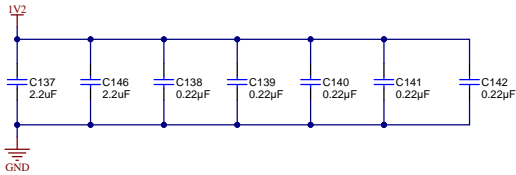


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TID #: N/A	Project Title: AWR2E44PEVM	
Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 25
Drawn By:	File: PROC196A_PWR_Reference.SchDoc	Size: B
Engineer: Sami Mardini	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>

DECOUPLING REFERENCE

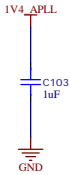
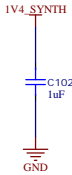
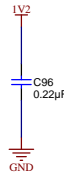
1.2V DIGITAL SUPPLY



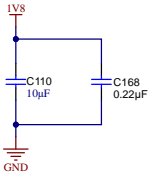
SRAM SUPPLY



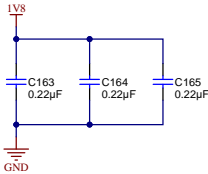
VNWA SUPPLY



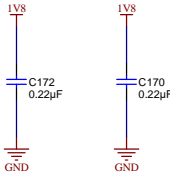
1.8V CLOCK SUPPLY



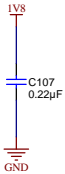
1.8V IO SUPPLY



1.8V LVDS SUPPLY



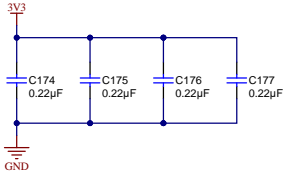
1.8V PM SUPPLY



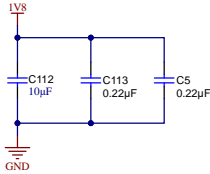
1.8V VCO SUPPLY



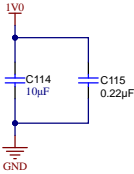
3.3V IO SUPPLY



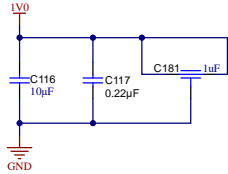
1.8V BB SUPPLY



RF1 SUPPLY



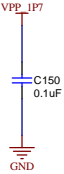
RF2 SUPPLY



BANDGAP SUPPLY



VPP SUPPLY




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TID #: N/A	Project Title: AWR2E44PEVM	
Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 4 of 25
Drawn By: Sami Mardini	File: PROC196A_Decoupling_Reference.SchDoc	Size: B
Engineer: Sami Mardini	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>

## QSPI FLASH REFERENCE

[illegible]

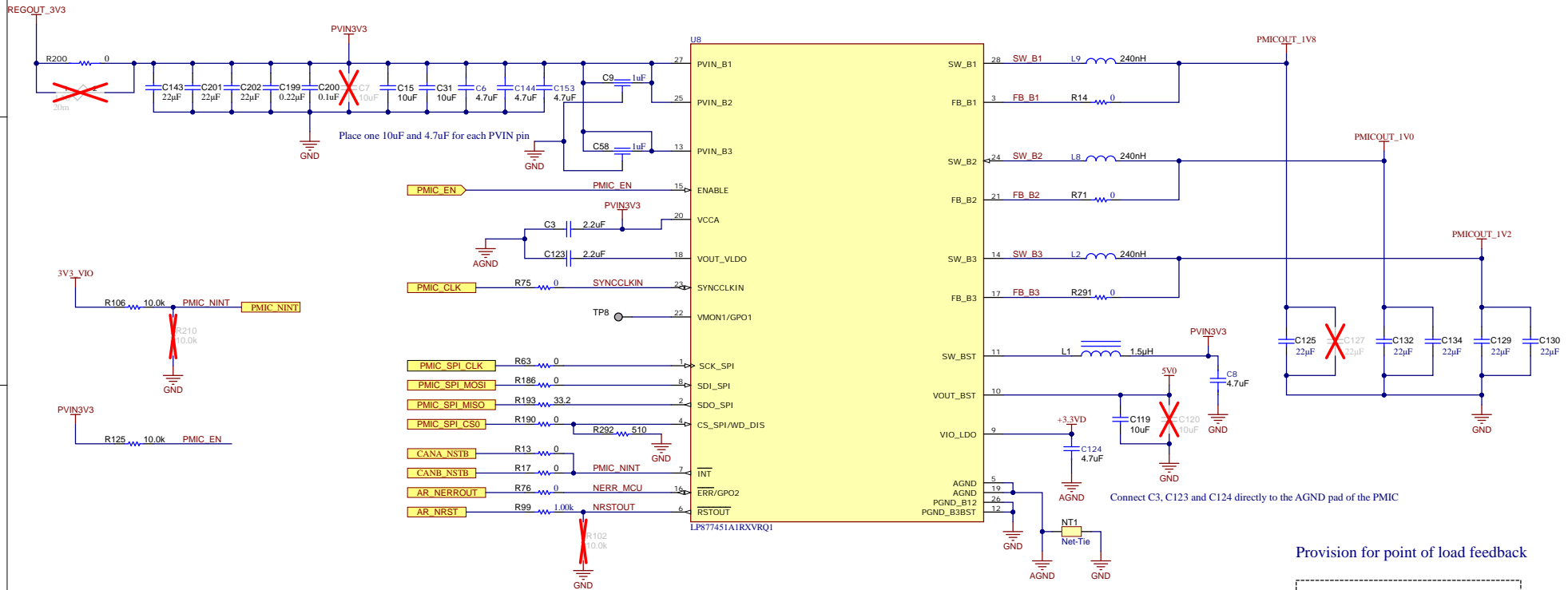
Orderable: <b>AWR2E44PEVM</b>	Designed for: <b>Public Release</b>	Mod. Date: 8/30/2024
TID #:	Project Title: <b>AWR2E44PEVM</b>	
Number: <b>PROC196</b>	Rev: <b>A</b>	
SVN Rev: Not in version control	Assembly Variant: <b>001</b>	Sheet <b>5</b> of <b>25</b>
Drawn By:	File: <b>PROC196A_GSPI_Flash_Reference_SchDoc</b>	Size: B
Engineer: <b>Sami Mardini</b>	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	 <b>TEXAS INSTRUMENTS</b> <a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2024



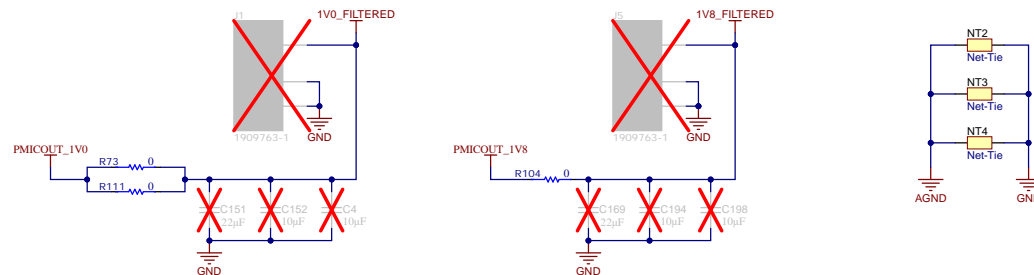
## References

## PMIC REFERENCE

### DEBUG TEST PINS

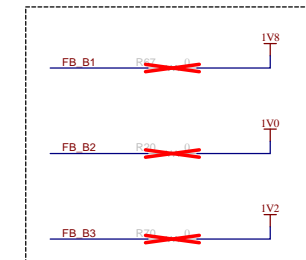


## PMIC LC FILTER



Connect AGND to PGND in inner layer GND. In any case, PGND should not be connected to power pad on layer on which PMIC is placed

### Provision for point of load feedback

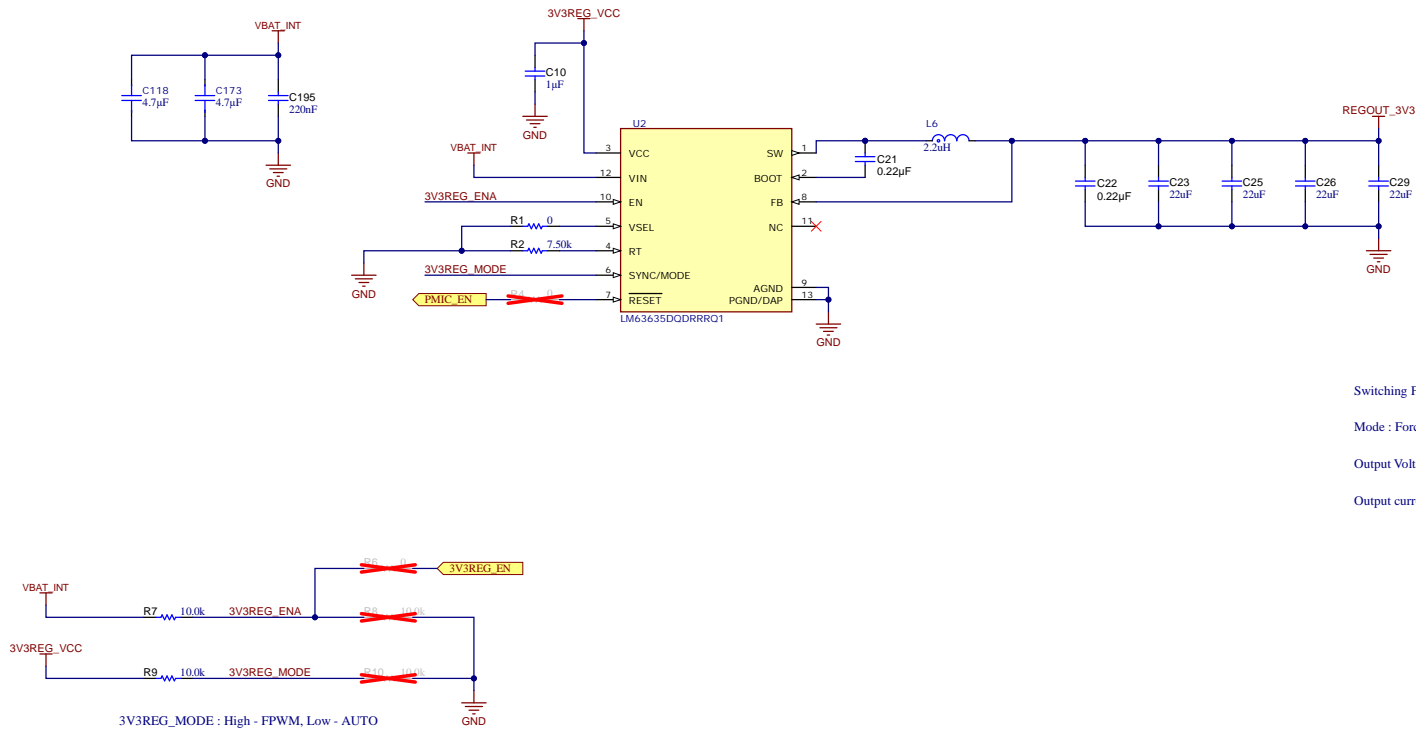


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Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 6 of 25
Drawn By:	File: PROC196A_PMIC_Reference.SchDoc	Size: B
Engineer: Sami Mardini	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

References

3V3 SUPPLY REFERENCE



Switching Frequency : 2.1 MHz  
Mode : Forced PWM  
Output Voltage : Fixed 3.3  
Output current limit : 3.25A

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Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 7 of 25
Drawn By:	File: PROC196A_3V3_Supply_Reference.SchDoc	Size: B
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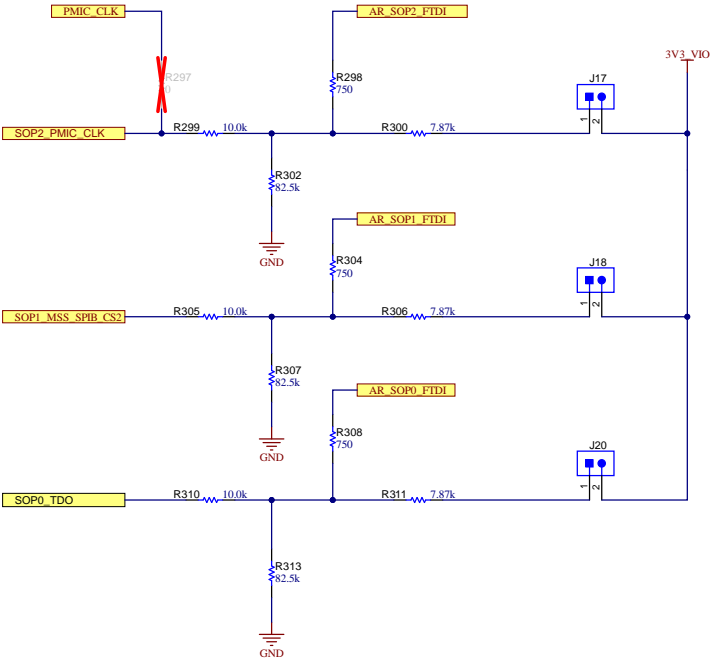
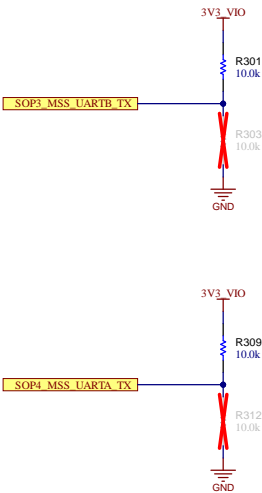
SOP REFERENCE

XTAL DETECT SOP CONFIG

SOP4, SOP3	
40 MHz	00
45.1584 MHz	01
49.152 MHz	10
50 MHz	11

SOP2, SOP1, SOP0

SOP_MODE1	SCAN/ATPG	010
SOP_MODE2	DEV/FLED/ORBIT	011
SOP_MODE3	THB	000
SOP_MODE4	FUNC	001
SOP_MODE5	DEV MANAGEMENT	101



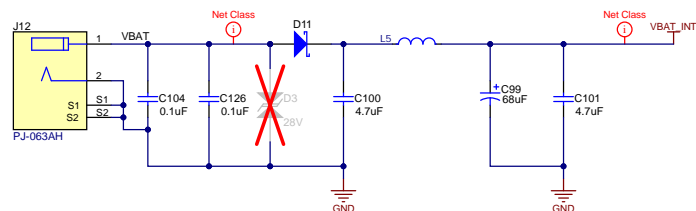
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TID #: N/A	Project Title: AWR2E44PEVM	
Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 8 of 25
Drawn By:	File: PROC196A_SOP_Reference.SchDoc	Size: B
Engineer: Sami Mardini	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>

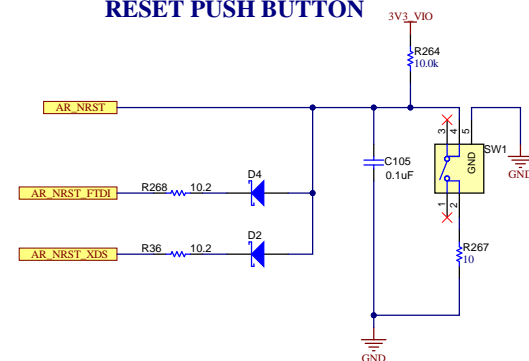


## POWER IN, RESETS, AND LEDS

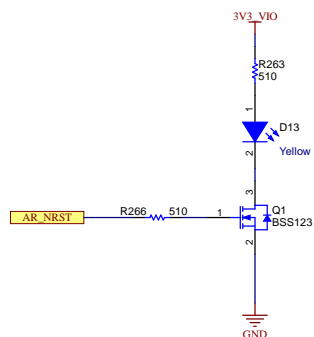
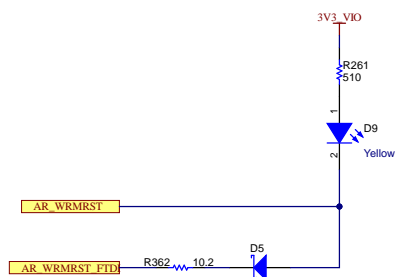
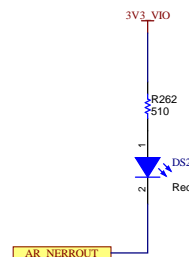
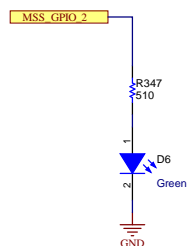
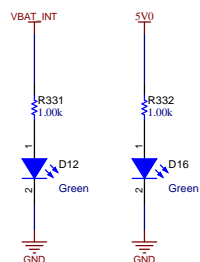
## POWER JACK



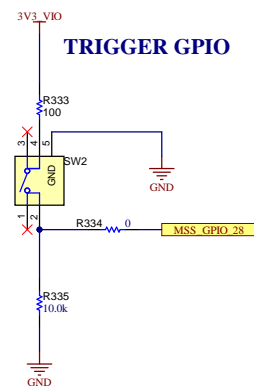
## RESET PUSH BUTTON




## INDICATION LEDS



## TRIGGER GPIO

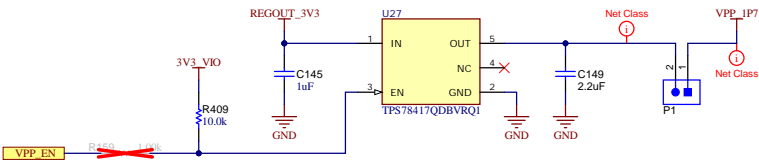


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Number: <b>PROC196</b>	Rev: <b>A</b>	
SVN Rev: <b>Not in version control</b>	Assembly Variant: <b>001</b>	Sheet: <b>9</b> of <b>25</b>
Drawn By:	File: <b>PROC196A_PWR_RST_LED_SchDoc</b>	Size: <b>B</b>
Engineer: <b>Sami Mardini</b>	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	 <a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2024

References

VPP LDO



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Orderable: AWR2E44PEVM	Designed for: Public Release	Mod. Date: 8/30/2024
TID #: N/A	Project Title: AWR2E44PEVM	
Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 10 of 25
Drawn By:	File: PROC196A_VPP_LDO.SchDoc	Size: B
Engineer: Sami Mardini	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>



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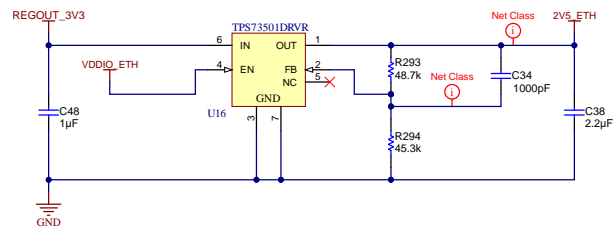
# ETHERNET POWER

## References

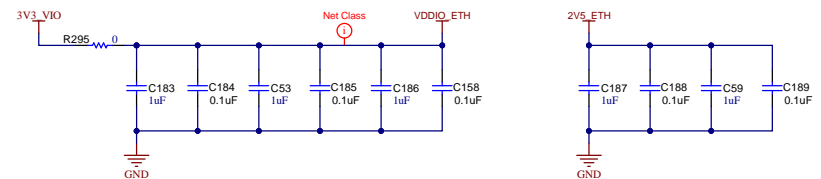
[TPS73501 Datasheet](#)

[TLV733P Datasheet](#)

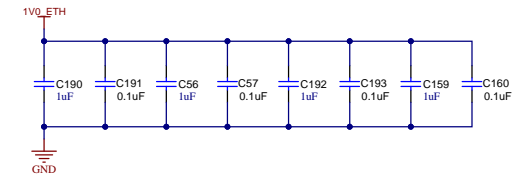
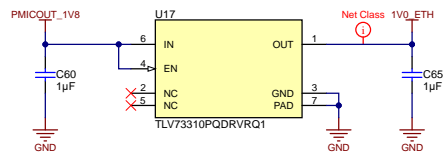
## 2.5V ANALOG SUPPLY



## DECOUPLING CAPS



## 1V ANALOG SUPPLY



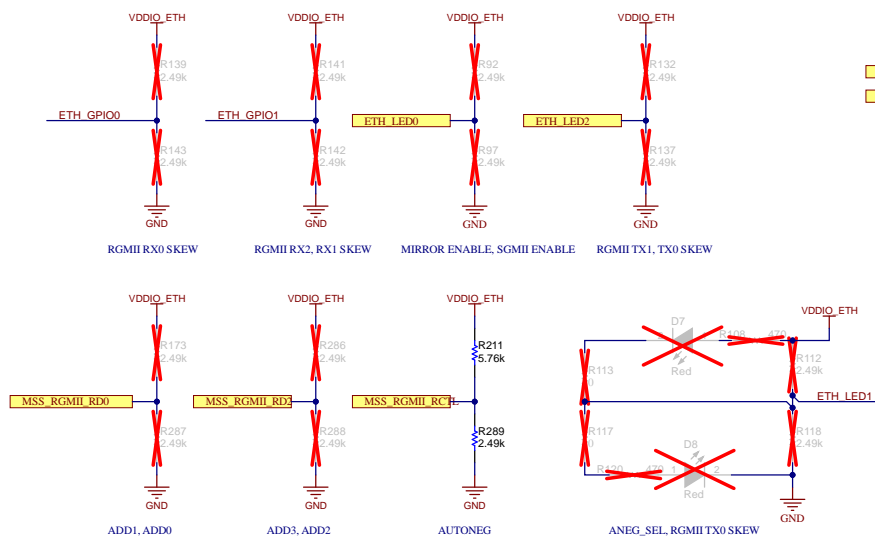
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Orderable: AWR2E44PEVM	Designed for: Public Release	Mod. Date: 8/30/2024
TID #: N/A	Project Title: AWR2E44PEVM	
Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 11 of 25
Drawn By:	File: PROC196A_Ethernet_PWR_SchDoc	Size: B
Engineer: Sami Mardini	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>

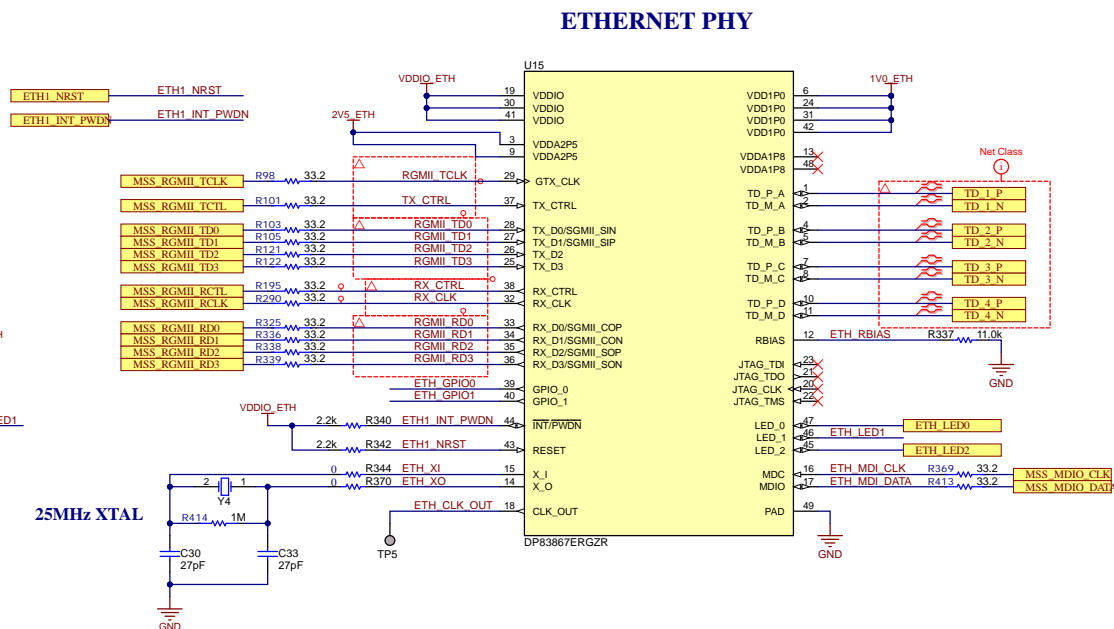
[P83867E Datasheet](#)

## BOOTSTRAP CONFIGURATION PINS

Resistor Values must be changed to change Modes, refer to datasheet for proper values




```
ADD1, ADD0 = 0
ADD3, ADD2 = 0
AUTONEG = 1
RGMII RX0 SKEW = 0
RGMII RX2, RX1 SKEW = 0, 0
RGMII TX1, TX0 SKEW = 0, 0
ANEG_SEL, RGMII TX0 SKEW = 0, 0
MIRROR_ENABLE, SGMII_ENABLE = 0, 0
```



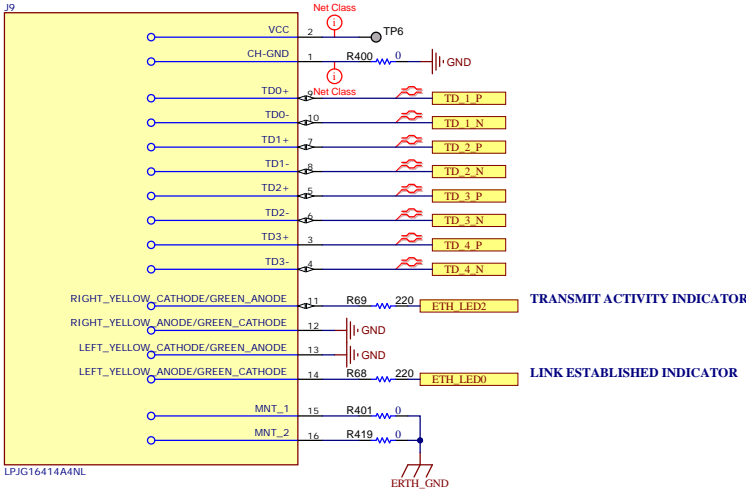
Place R98, R101, R103, R105, R121 and R122 close to U29

Place R195, R290, R325, R336, R338 and R339 close to U15

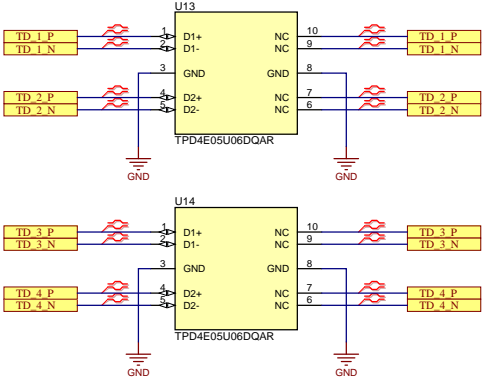
Orderable: <b>AWR2E44PEVM</b>	Designed for: <b>Public Release</b>	Mod. Date: 8/30/2024	 <b>TEXAS INSTRUMENTS</b> <a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2024
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Number: <b>PROC196</b>	Rev: <b>A</b>	Sheet Title: <b>N/A</b>	
SVN Rev: <b>Not in version control</b>	Assembly Variant: <b>001</b>	Sheet: <b>12 of 25</b>	
Drawn By:	File: <b>PROC196A_Ethernet_PHY_SchDoc</b>	Size: <b>B</b>	
Engineer: <b>Sami Mardini</b>	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>		

# ETHERNET MAGNETICS

## RJ45 WITH MAGJACK



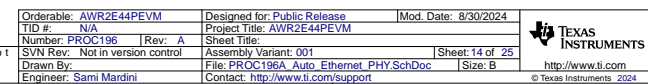
## ETHERNET ESD PROTECTION



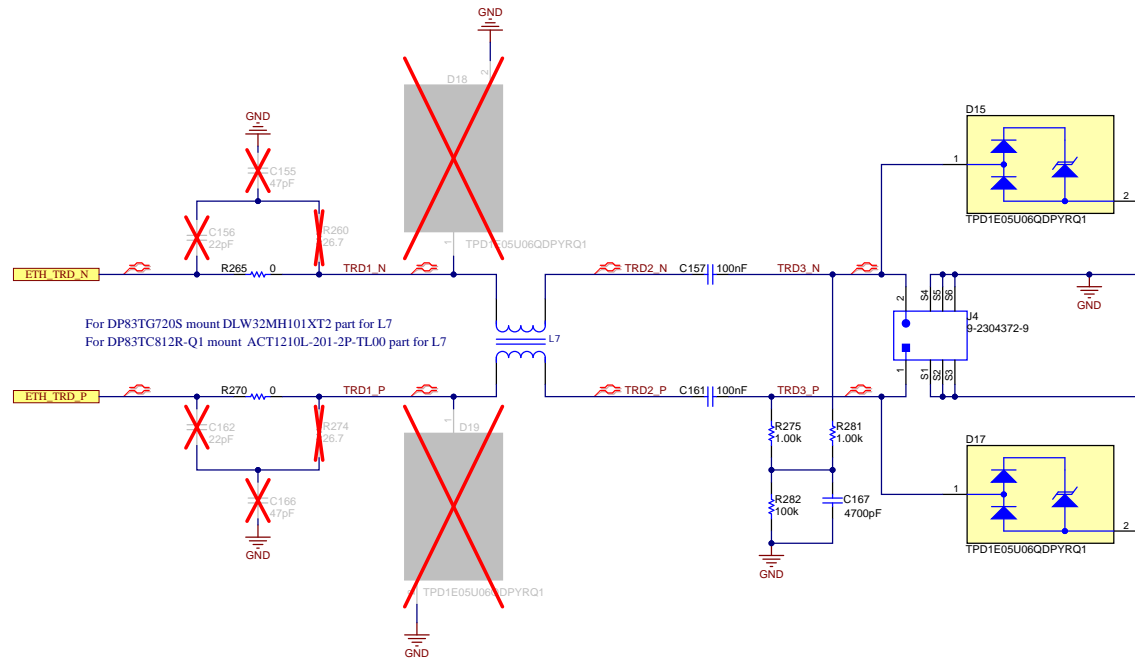
## ETHERNET PHY



Resistor Values must be changed to change Modes, refer to datasheet for proper values



# ETHERNET CONNECTOR

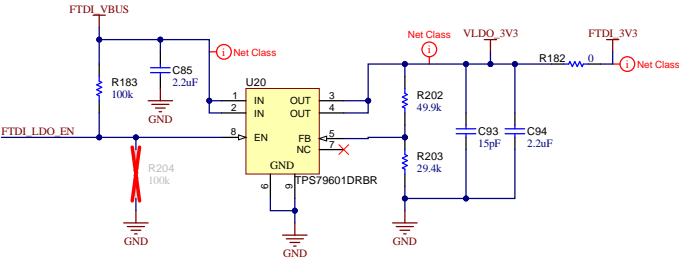


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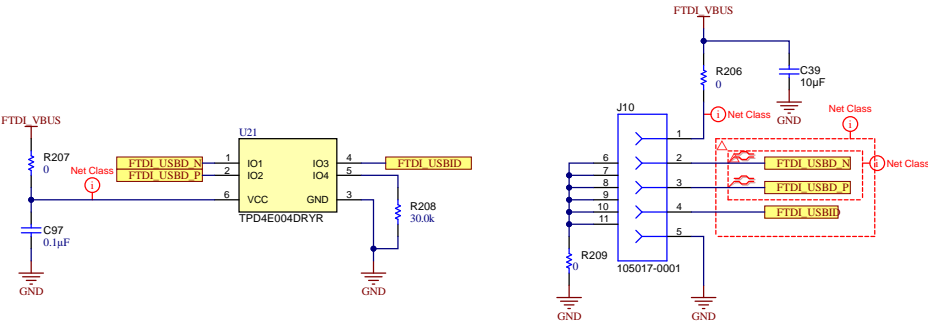
Orderable: AWR2E44PEVM	Designed for: Public Release	Mod. Date: 8/30/2024
TID #: N/A	Project Title: AWR2E44PEVM	
Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 15 of 25
Drawn By:	File: PROC196A_Auto_Ethernet_conn.SchDoc	Size: B
Engineer: Sami Mardini	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>

FTDI (1/2)

3.3V LDO FOR FTDI



FTDI USB PORT



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Orderable: AWR2E44PEVM	Designed for: Public Release	Mod. Date: 8/30/2024
TID #: N/A	Project Title: AWR2E44PEVM	
Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 16 of 25
Drawn By:	File: PROC196A_FTDI_PWR_SchDoc	Size: B
Engineer: Sami Mardini	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>



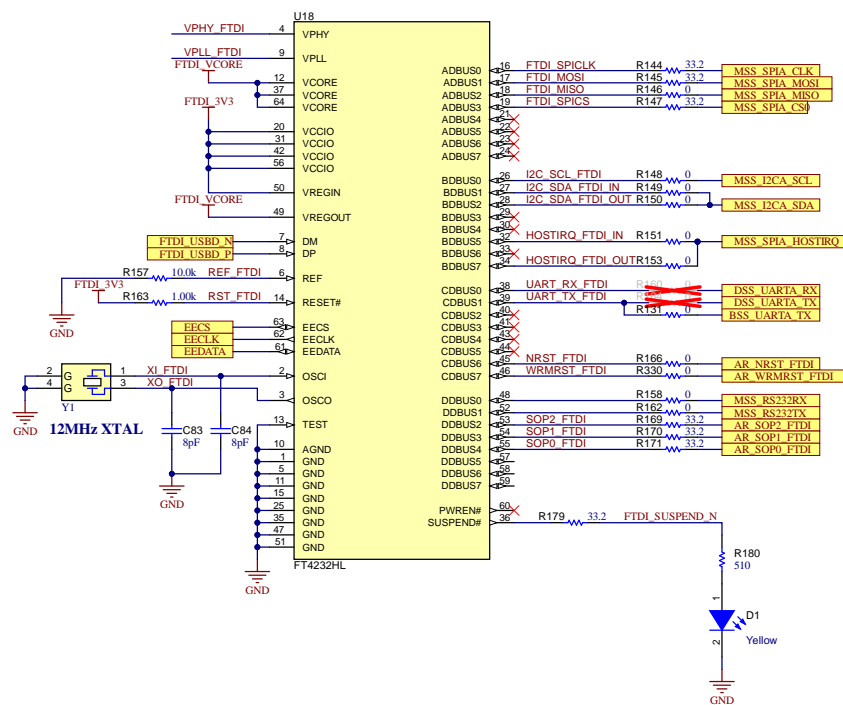
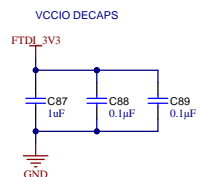
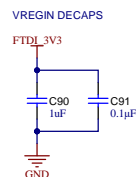
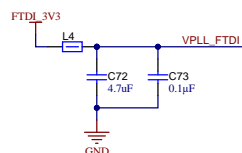
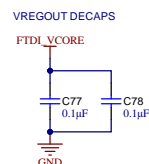
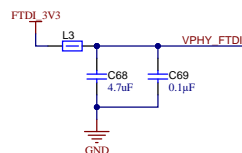
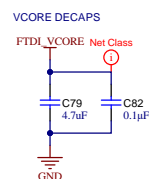


## References

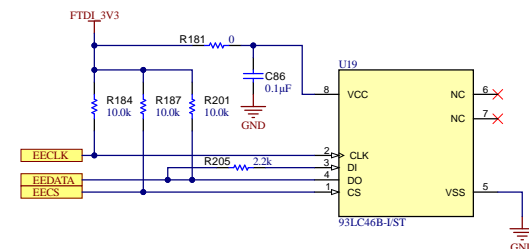
[FT4232H Datasheet](#)

## FTDI (2/2)

### FTDI SUPPLY DECAPS



### FTDI EEPROM

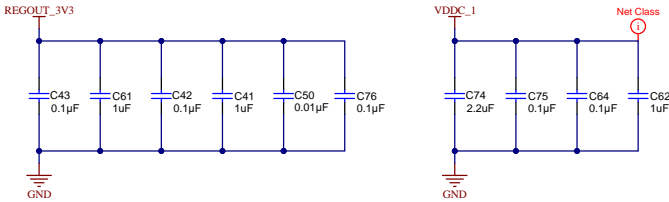


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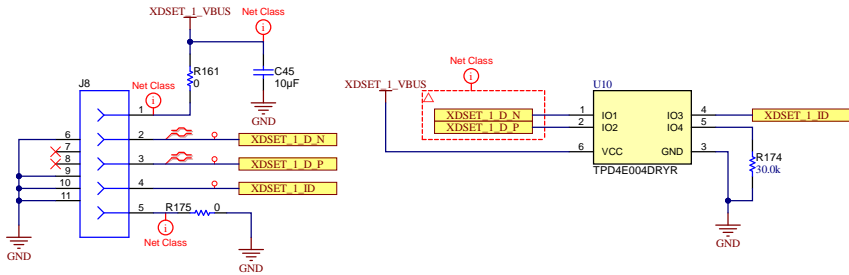
Orderable: AWR2E44PEVM	Designed for: Public Release	Mod. Date: 8/30/2024
TID #: N/A	Project Title: AWR2E44PEVM	
Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 17 of 25
Drawn By:	File: PROC196A_FTDI_SchDoc	Size: B
Engineer: Sami Mardini	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

XDS110(1/2)

XDS110 DECOUPLING CAPS



XDS110 USB PORT



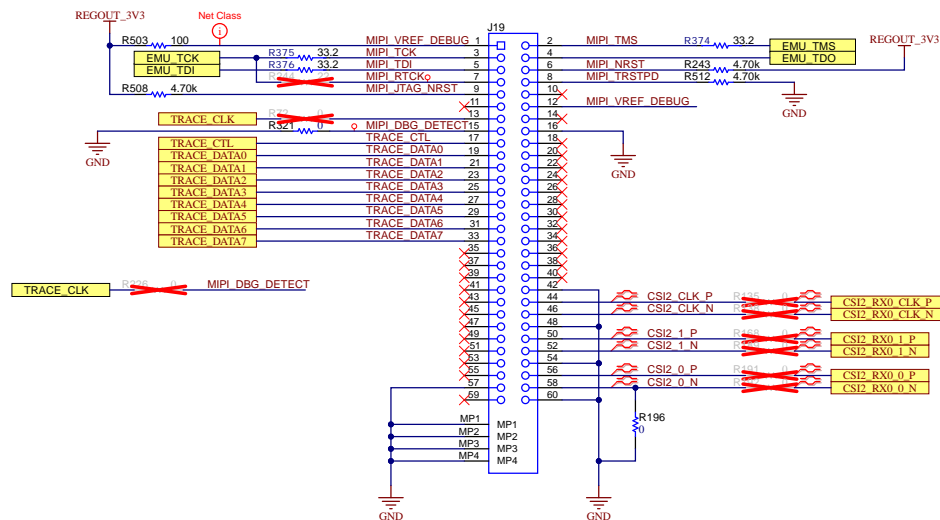
## XDS110(2/2)


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## MIPI 60 PIN HEADER

## JTAG MUX BETWEEN XDS110 AND MIPI 60 PIN

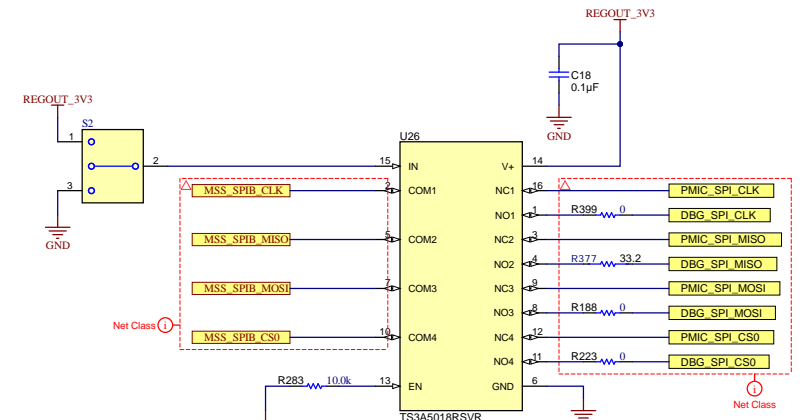
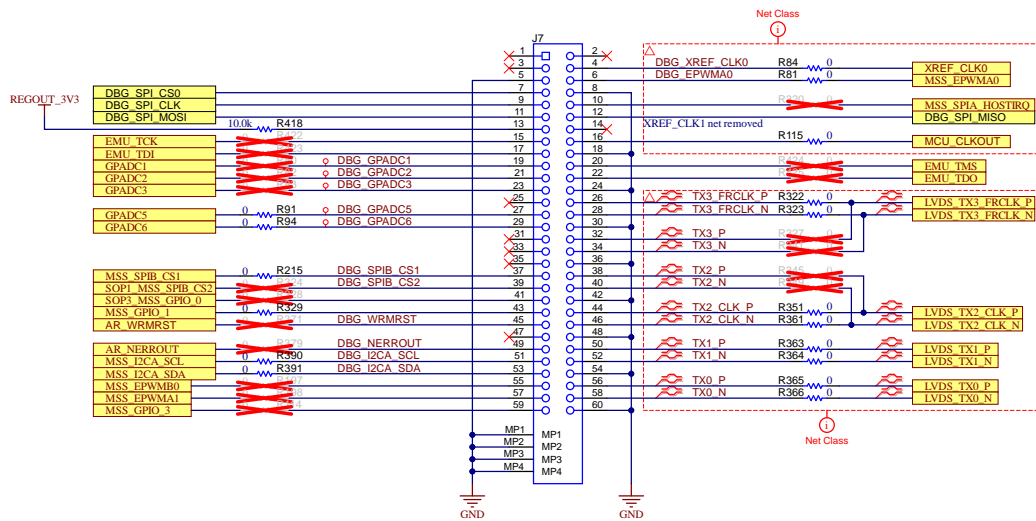


Orderable: <a href="#">AWR2E44PEVM</a>	Designed for: <a href="#">Public Release</a>	Mod. Date: 8/30/2024	 <b>TEXAS</b> <b>INSTRUMENTS</b>
Project Title: <a href="#">AWR2E44PEVM</a>			
Rev: <a href="#">A</a>	Sheet Title: <a href="#">AWR2E44PEVM</a>		
Part Number: <a href="#">PROC196</a>	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">20</a> of <a href="#">25</a>	
SVN Rev: Not in version control	File: <a href="#">PROC196A_JTAG_EMU_Connector_SchDoc</a>	Size: B	
Drawn By: <a href="#">Sami Mardini</a>	Contact: <a href="#">http://www.ti.com/support</a>	<a href="#">http://www.ti.com</a>	© Texas Instruments 2024



# 60 PIN DEBUG CONNECTOR

## SPI MUX BETWEEN PMIC AND 60 PIN DEBUG CONNECTOR



PLACE DBG SERIES RESISTORS NEAR 60 PIN CONNECTOR

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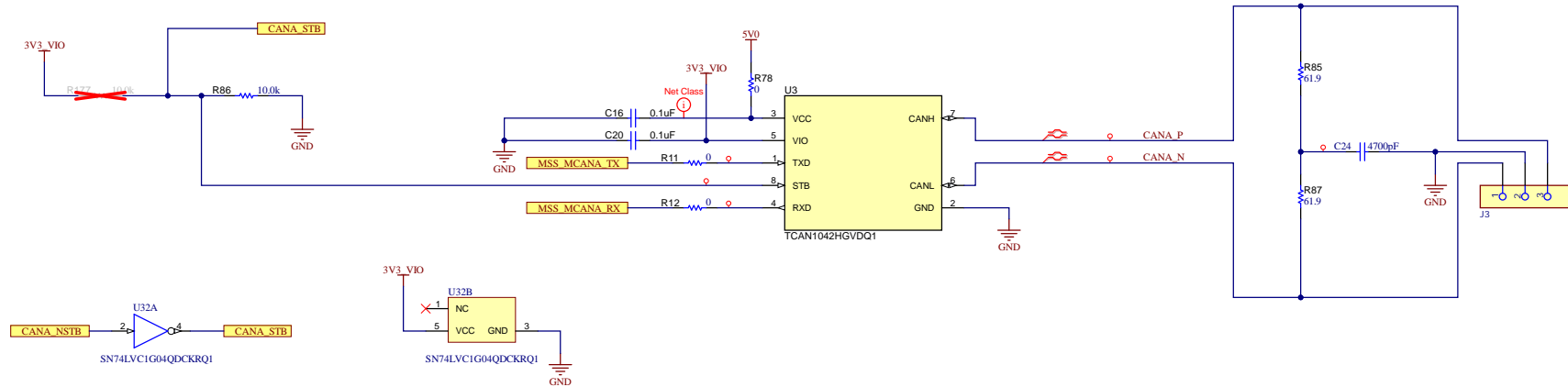
Orderable: AWR2E44PEVM	Designed for: Public Release	Mod. Date: 8/30/2024
TID #: N/A	Project Title: AWR2E44PEVM	
Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 21 of 25
Drawn By:	File: PROC196A_Debug_Connector.SchDoc	Size: B
Engineer: Sami Mardini	Contact: http://www.ti.com/support	http://www.ti.com

## References

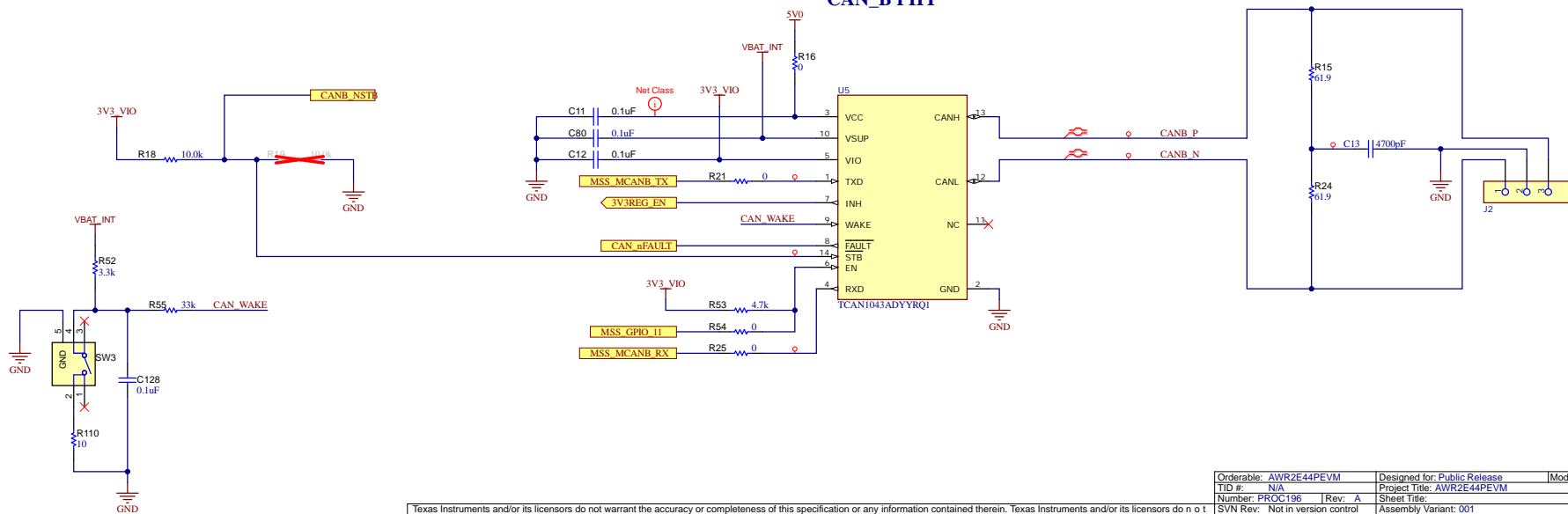
[TCAN1042 Datasheet](#)

## CAN INTERFACE

### CAN\_A PHY



### CAN\_B PHY



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TID #: N/A	Project Title: AWR2E44PEVM	
Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 22 of 25
Drawn By:	File: PROC196A_CAN_Interface.SchDoc	Size: B
Engineer: Sami Mardini	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>

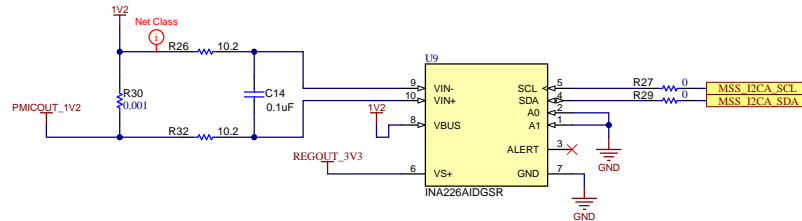
## CURRENT SENSORS

### References

[INA226 Datasheet](#)

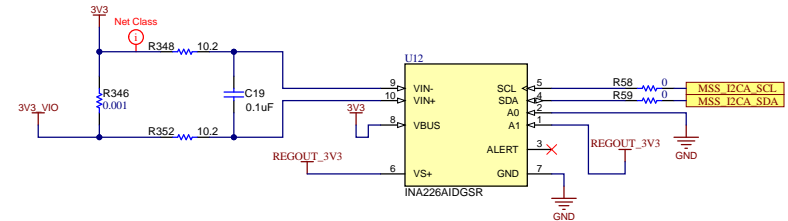
#### 1.2V SUPPLY CURRENT SENSOR

I2C ADDRESS 0x40



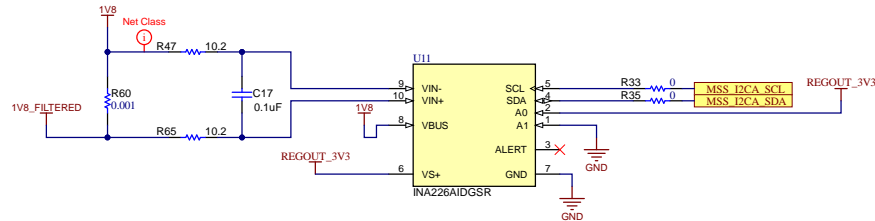
#### 3.3V SUPPLY CURRENT SENSOR

I2C ADDRESS 0x44



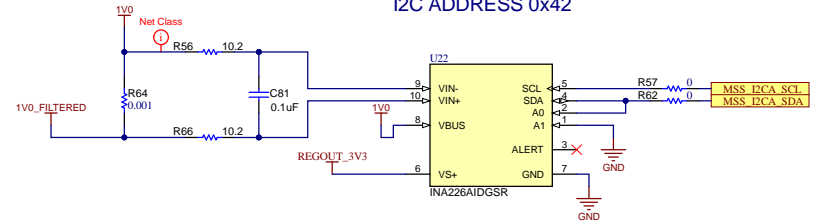
#### 1.8V SUPPLY CURRENT SENSOR

I2C ADDRESS 0x41



#### 1.0V SUPPLY CURRENT SENSOR

I2C ADDRESS 0x42



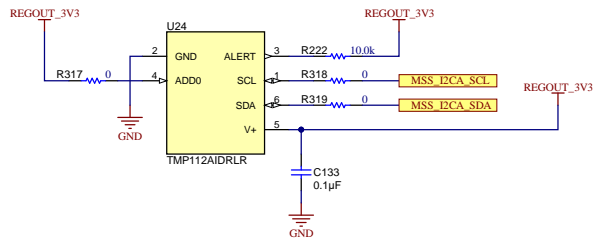
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TID #: N/A	Project Title: AWR2E44PEVM	
Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 23 of 25
Drawn By:	File: PROC196A_Current_Sensors.SchDoc	Size: B
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TEMP SENSOR

References  
[TMP112 Datasheet](#)

I2C ADDRESS 0x49



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TID #: N/A	Project Title: AWR2E44PEVM	
Number: PROC196	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 24 of 25
Drawn By:	File: PROC196A_Temp_Sensor.SchDoc	Size: B
Engineer: Sami Mardini	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>



