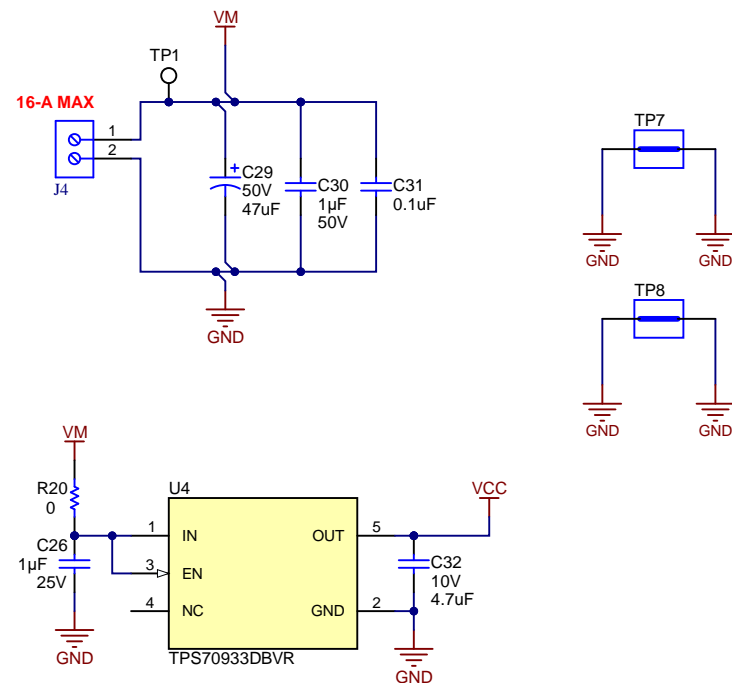
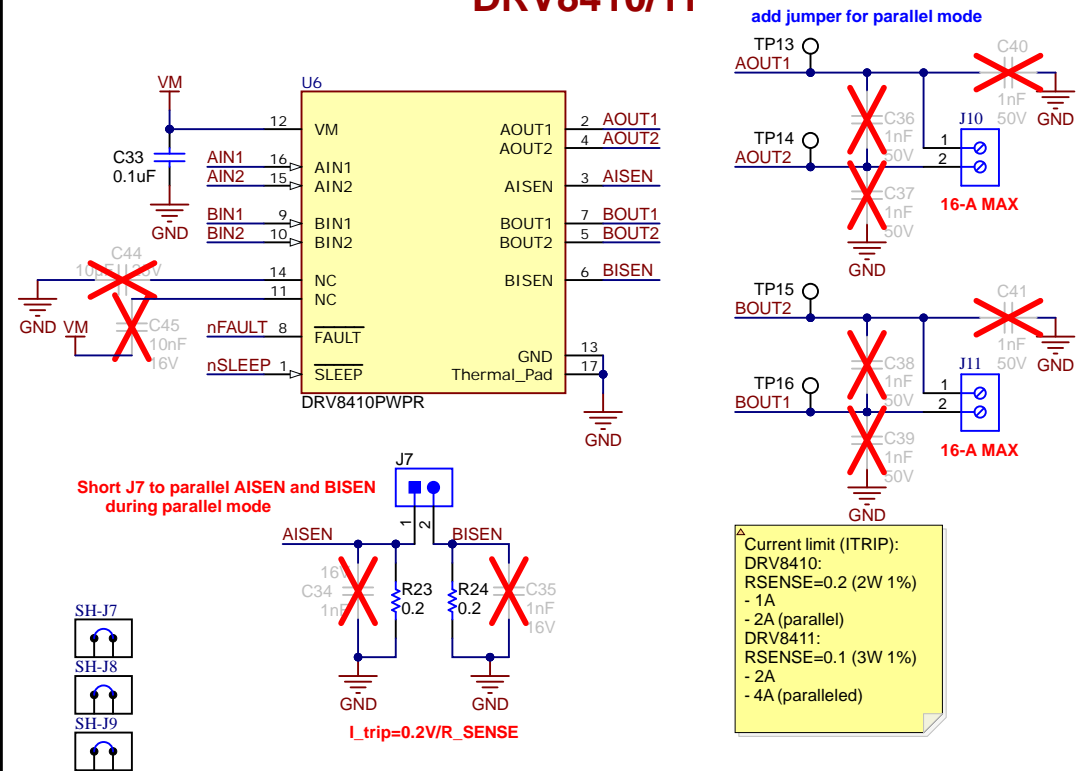


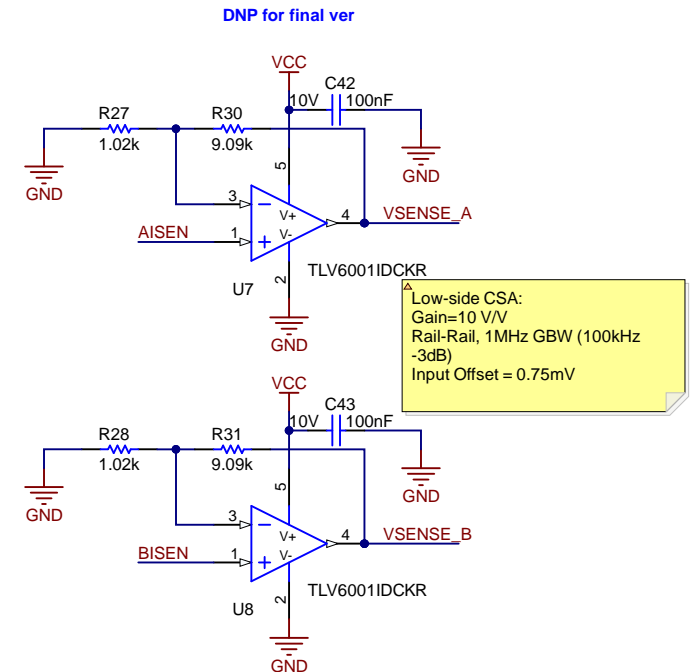
## Power Connector / LDO



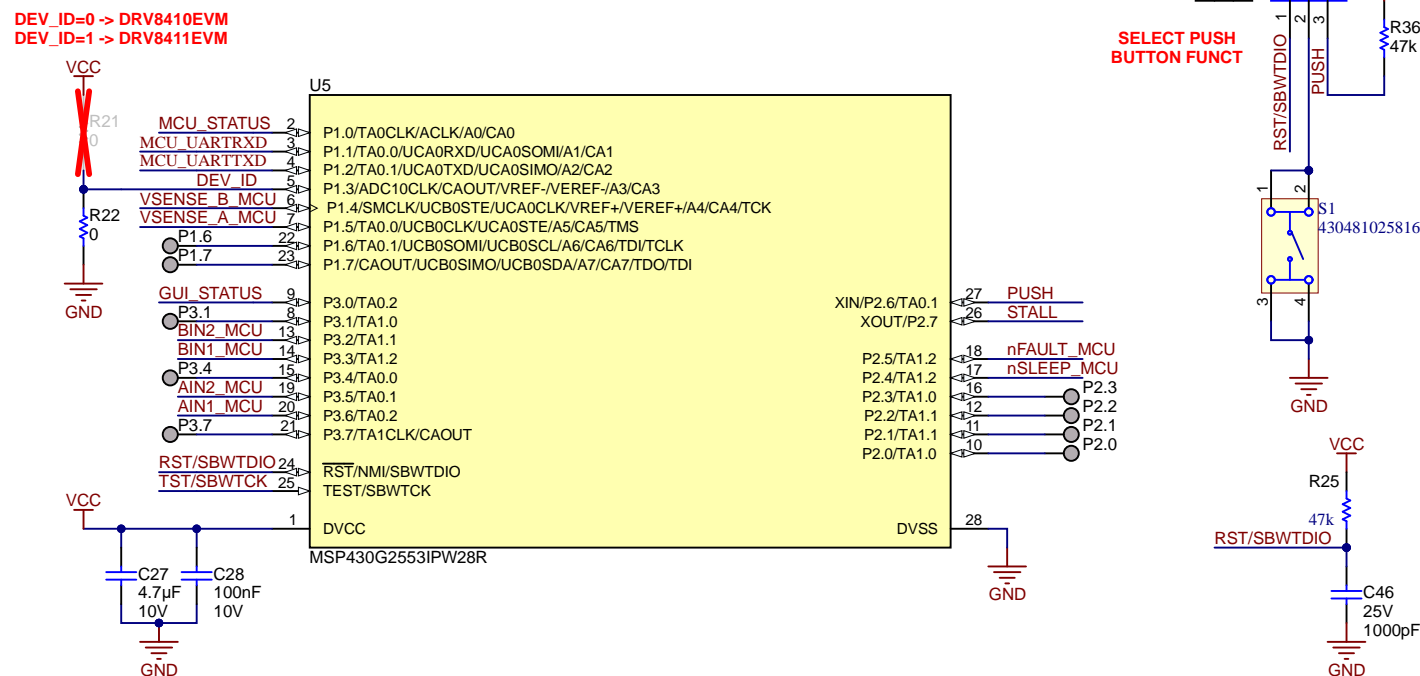
## DRV8410/11



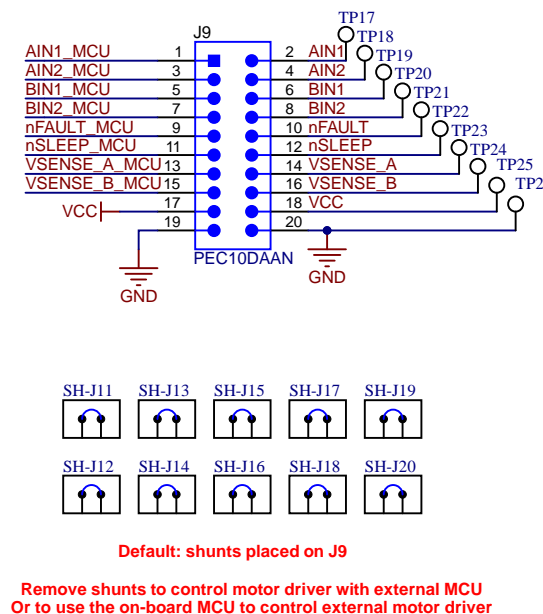
## Low-Side CSA



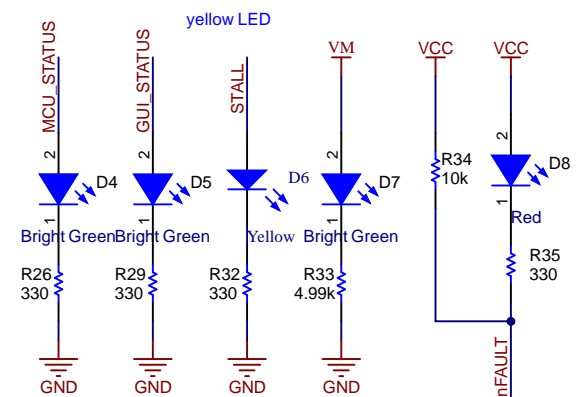
## MSP430G2553



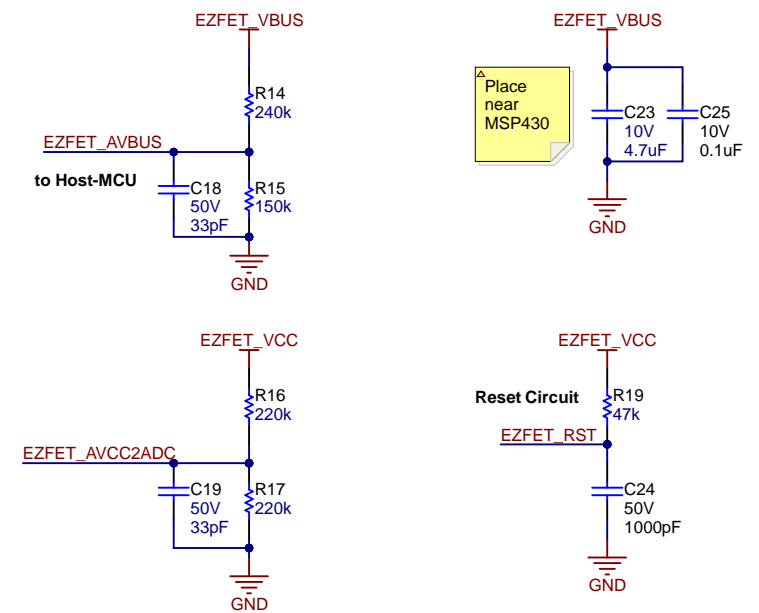
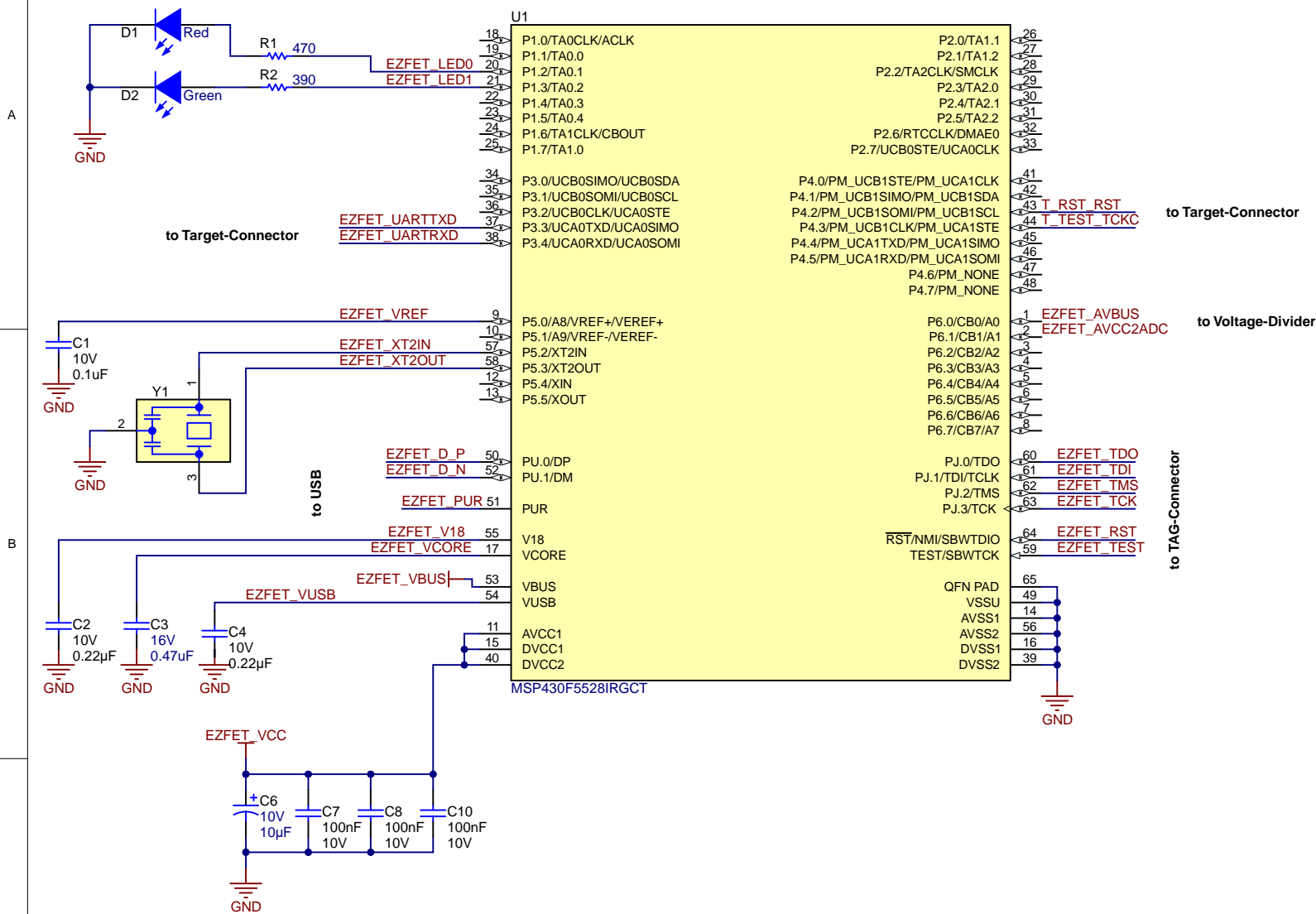
## Signal Header



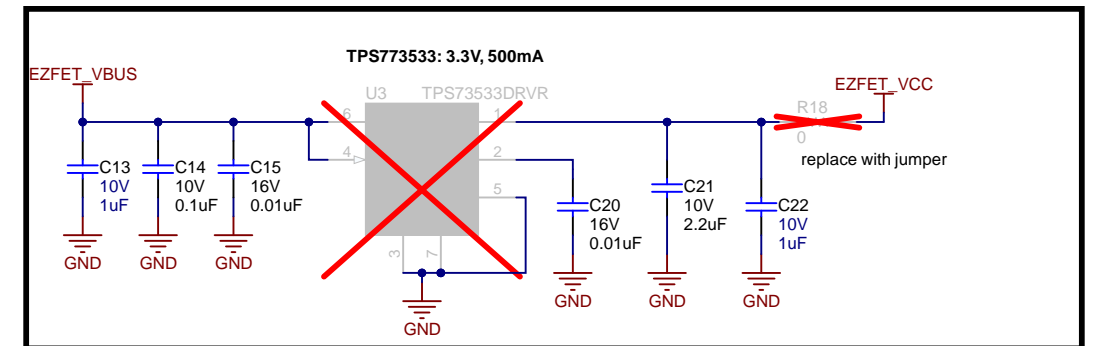
## LED Indicators/Miscellaneous



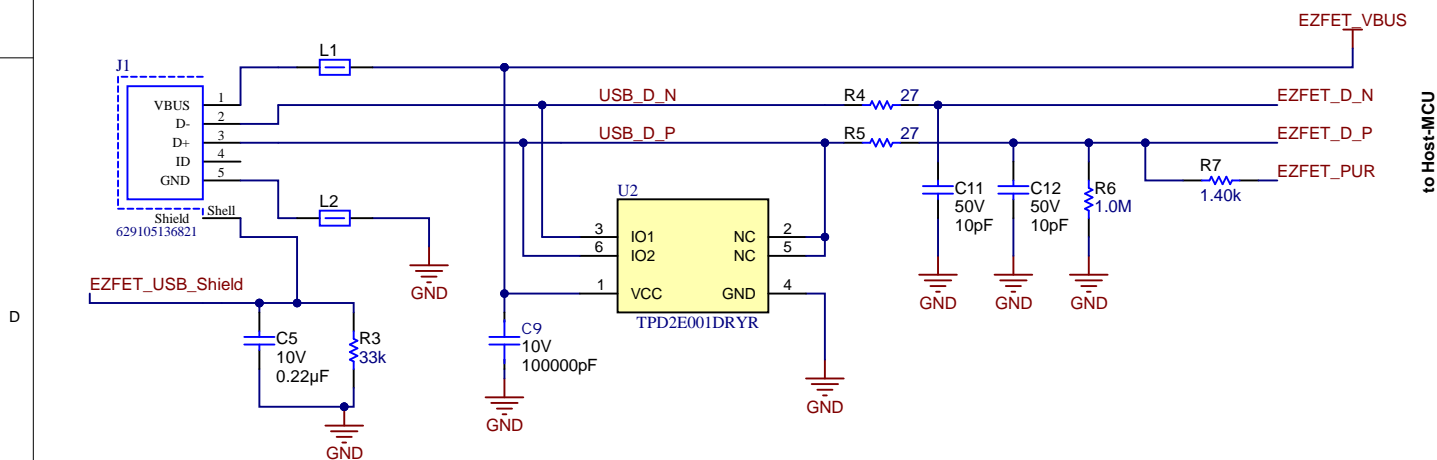
## Host MCU for Emulation



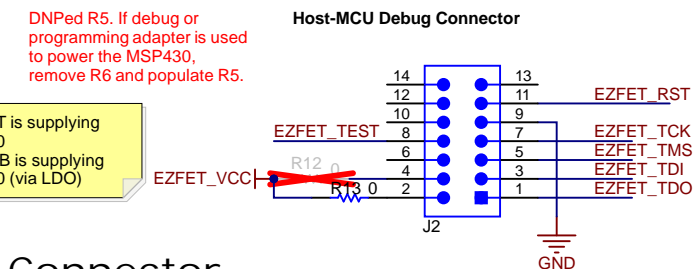
### 3.3V Power (EZFET\_VCC)



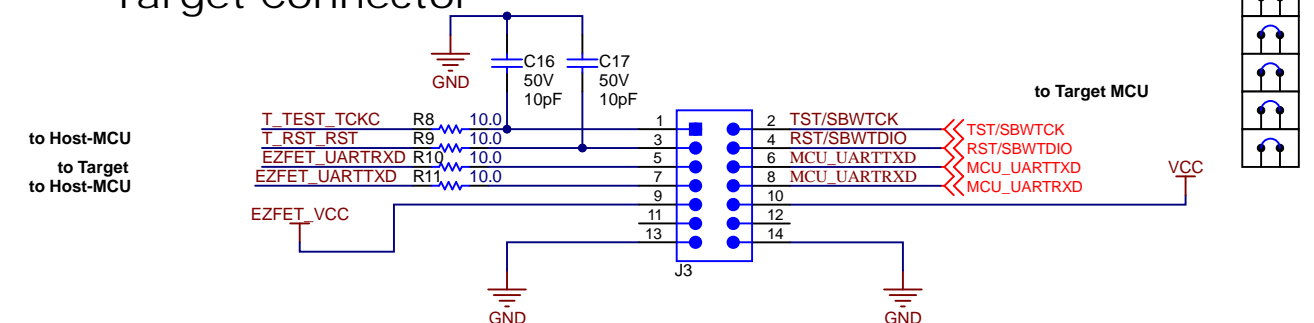
## USB-Interface




## TAG-Connector (Host Debug)



## Target Connector



Orderable: <b>DRV8410EVM</b>	Designed for: <b>Public Release</b>	Mod. Date: 7/11/2022	
TID #: <b>N/A</b>	Project Title: <b>DRV8410_11EVM</b>		
Number: <b>MD062</b>	Rev: <b>E2</b>	Sheet Title:	
SVN Rev: 653ed5ed4c52828bd7363895f5095a2f1f0b02 [Locally Modified]		Sheet: <b>2</b> of <b>3</b>	
Drawn By:	File: <b>ezFET.SchDoc</b>	Size: B	
Engineer: <b>Pablo Armet</b>	Contact: <b>http://www.ti.com/support</b>		<a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2022

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H1  
SJ-5303 (CLEAR)

H2  
SJ-5303 (CLEAR)

H3  
SJ-5303 (CLEAR)

H4  
SJ-5303 (CLEAR)

FID1

FID2

FID3

PCB Number: MD062

PCB Rev: E2

Logo1  
PCB  
LOGO  
Texas Instruments

CE Mark

PCB  
LOGO  
FCC disclaimer

PCB  
LOGO  
WEEE logo

LBL1

PCB Label

THT-14-423-10

Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

Variant/Label Table	
Variant	Label Text
001	DRV8410EVM
002	DRV8411EVM

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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