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Orderable: N/A	Designed for: Public Release	Mod. Date: 11/1/2016
TID #: TIPD192	Project Title: SPI Programmable, High Accuracy, Fast Settling ±	
Number: TIPD192	Rev: E3	Sheet Title: Schematic for TIDU974
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 3
Drawn By:	File: tipd192.SchDoc	Size: B
Engineer: Matt Poole	Contact: http://e2e.ti.com	



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PCB Number: TIPD192
PCB Rev: E3

PCB LOGO
Pb-Free Symbol PCB LOGO
FCC disclaimer

ZZ1
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Number: TIPD192	Rev: E3	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 3 of 3	
Drawn By:	File: TIPD192_HW.SchDoc	Size: B	
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