Revision History	
Revision	Notes

		Designed for: Public Release	Mod. Date: 9/7/2015
		Project Title: TIDA-00555	
Number: TIDA-00555	Rev: E2	Sheet Title: Index Page	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 1 of 9
Drawn By:		File: Pg1 Index Page.SchDoc	Size: B
Engineer: Prahlad Supeda		Contact: http://www.ti.com/support	

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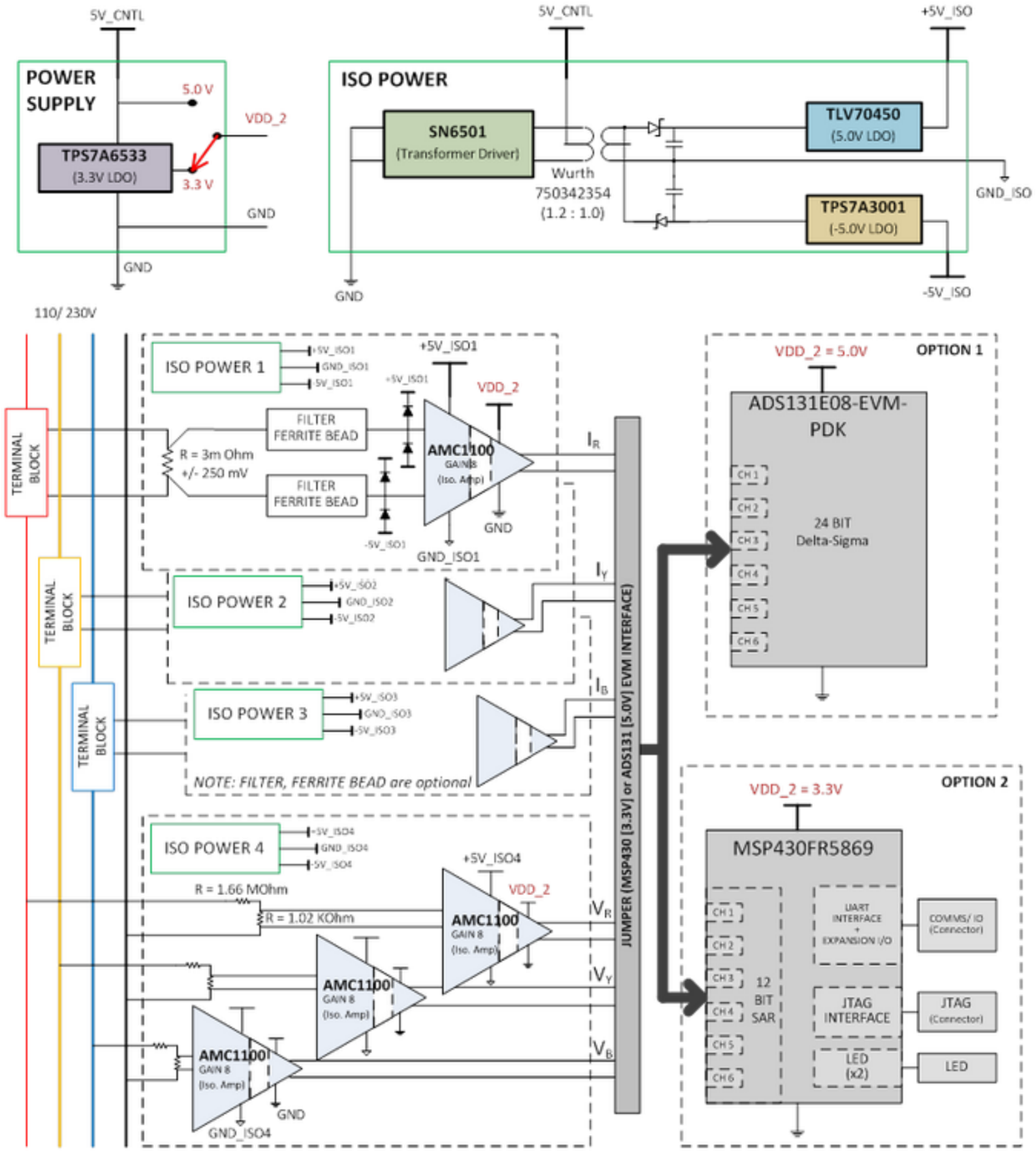

Number: TIDA-00555	Rev: E2	Designed for: Public Release	Mod. Date: 9/2/2015
SVN Rev: Not in version control		Project Title: TIDA-00555	
Drawn By:		Sheet Title: Block Diagram	
Engineer: Prahlad Supeda		Assembly Variant: 001	Sheet: 2 of 9
		File: Pg2 Block Diagram.SchDoc	Size: B
		Contact: http://www.ti.com/support	

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J8 connector can be used for SPI / I2C / UART communication

Configuration for SPI:

Existing net = SPI signal

SDA = UCB0SIMO

SCL = UCB0SOMI

P2.2 = UCB0CLK

P3.7 = /CS

Configuration for I2C:

SDA

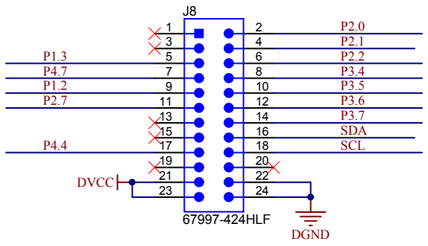
SCL

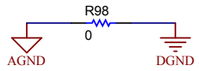
Configuration for UART:

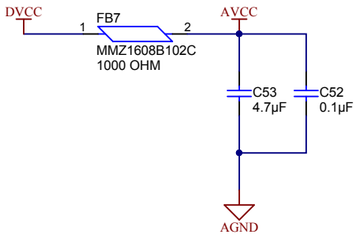
Existing net = UART signal

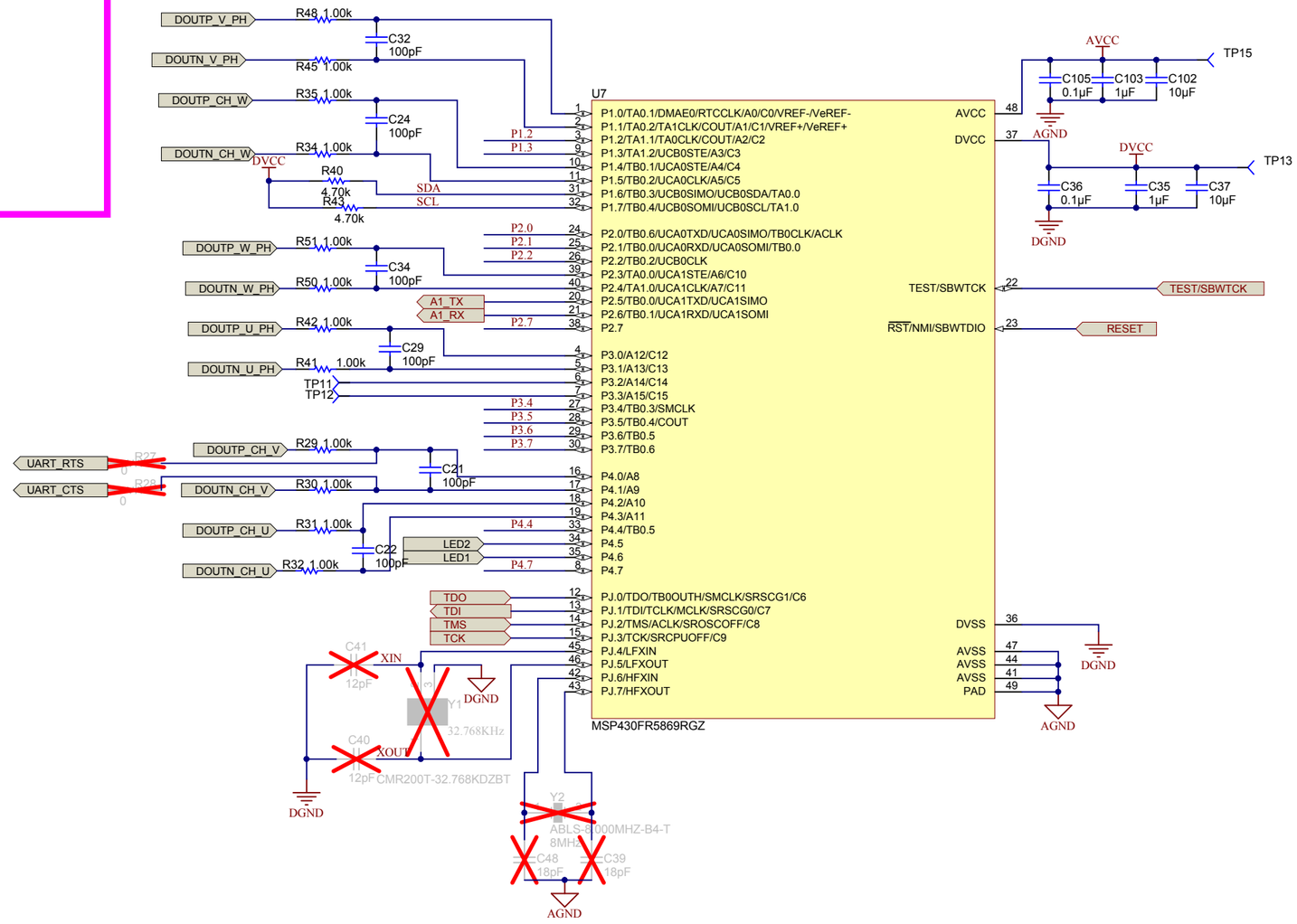
P2.0 = UCA0TXD

P2.1 = UCA0RXD









Designed for: Public Release		Mod. Date: 9/2/2015	
Project Title: TIDA-00555			
Number: TIDA-00555	Rev: E2	Sheet Title: MSP430F6779 + Crystal	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 3 of 9
Drawn By:		File: Pg3-Microcontroller.SchDoc	Size: B
Engineer: Prahlad Supeda		Contact: http://www.ti.com/support	

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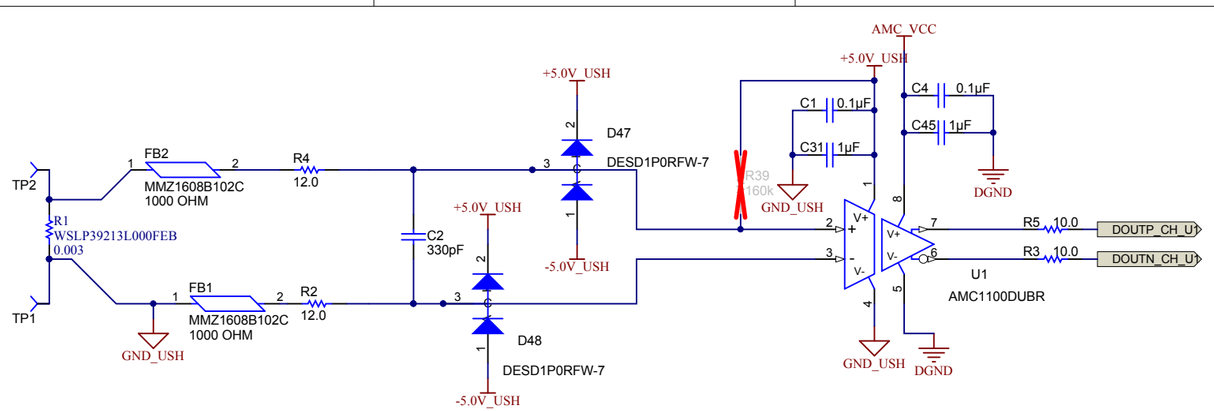
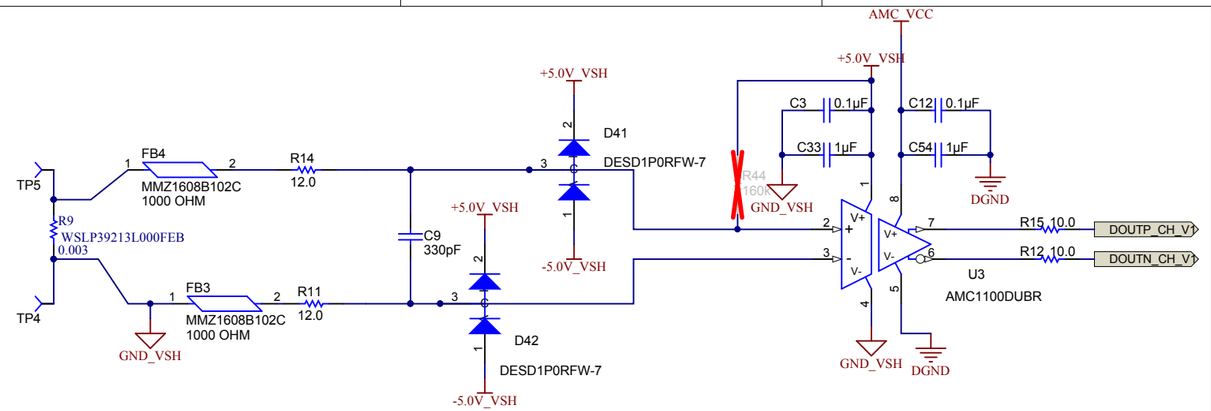
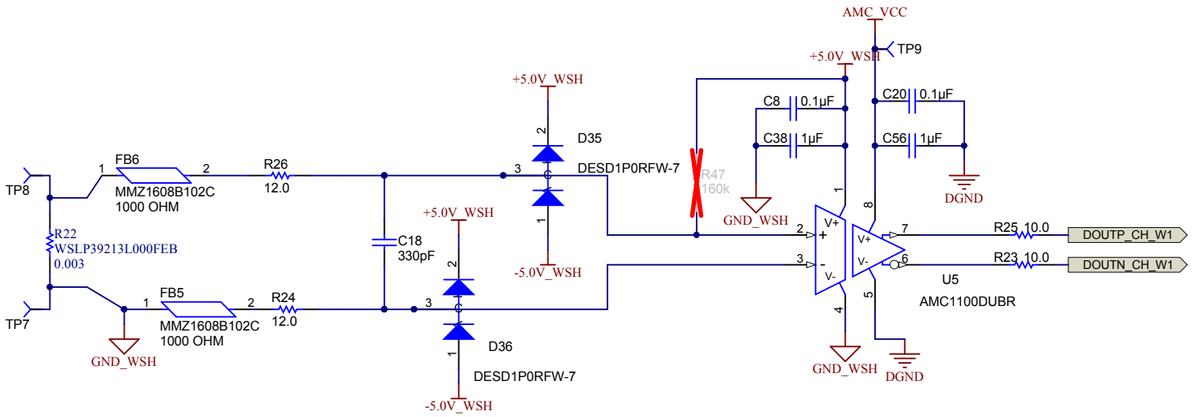
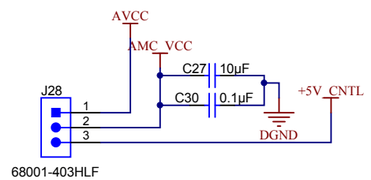
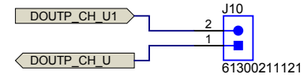
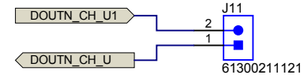


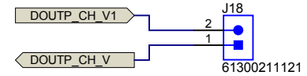


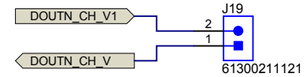


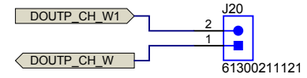


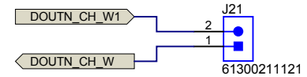




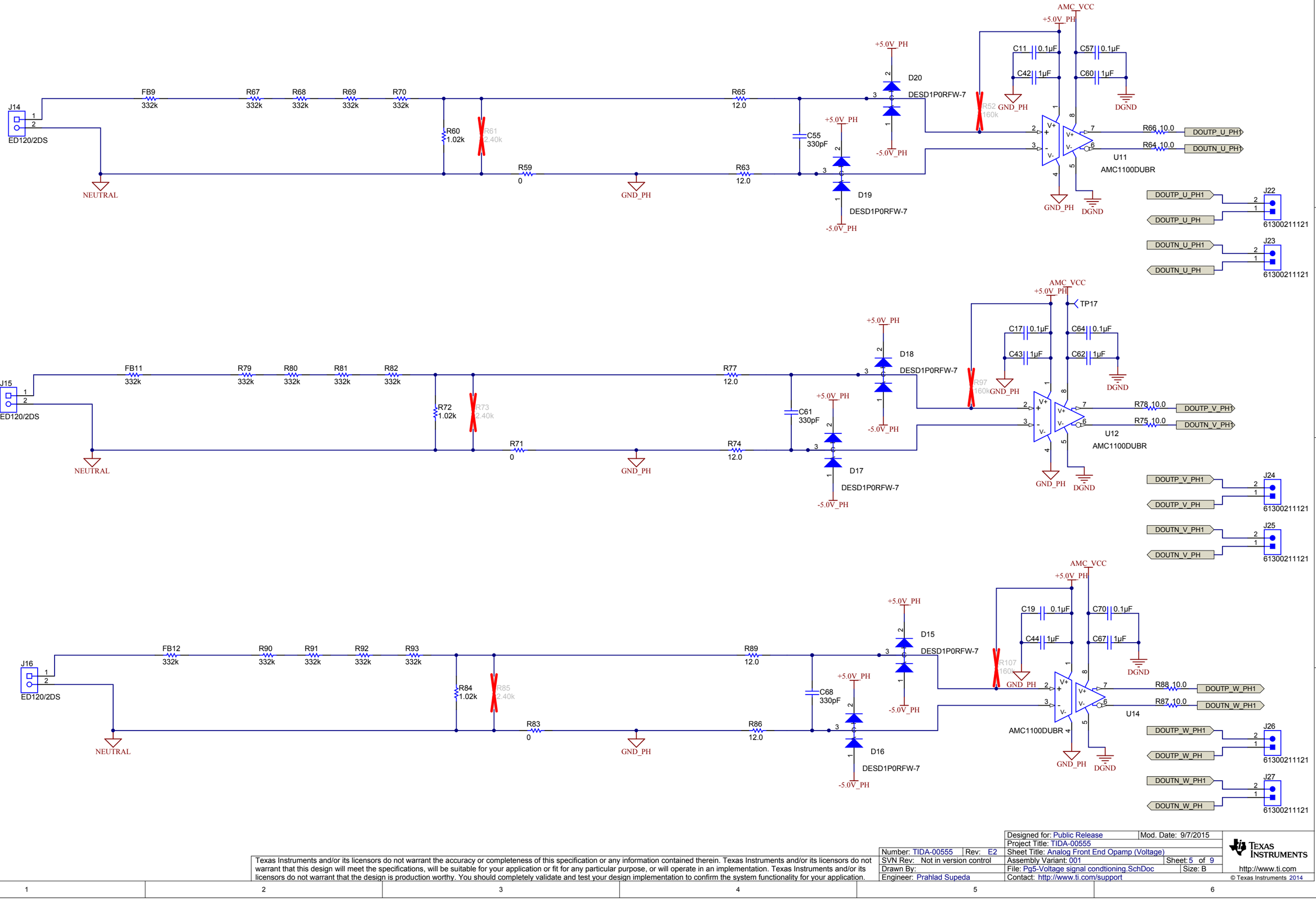




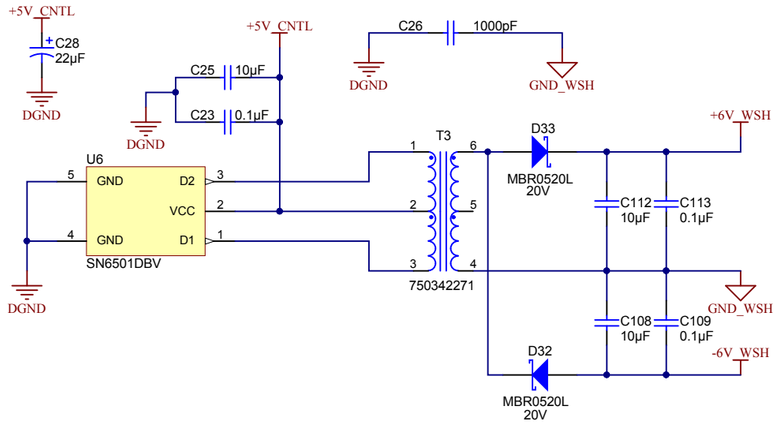


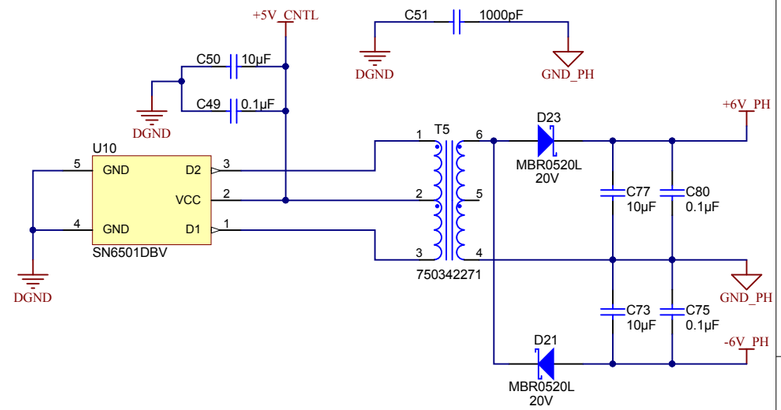


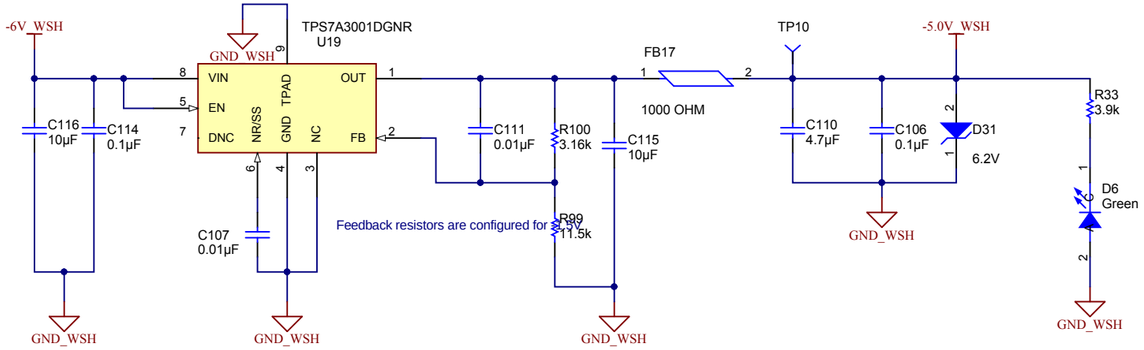
Number: TIDA-00555	Rev: E2	Designed for: Public Release	Mod. Date: 9/7/2015
SVN Rev: Not in version control		Project Title: TIDA-00555	
Drawn By:		Sheet Title: Analog Front End (Current) + Delfino Interface	
Engineer: Prahlad Supeda		Assembly Variant: 001	Sheet: 4 of 9
		File: Pg4-Current signal condtioning.SchDoc	Size: B
		Contact: http://www.ti.com/support	http://www.ti.com

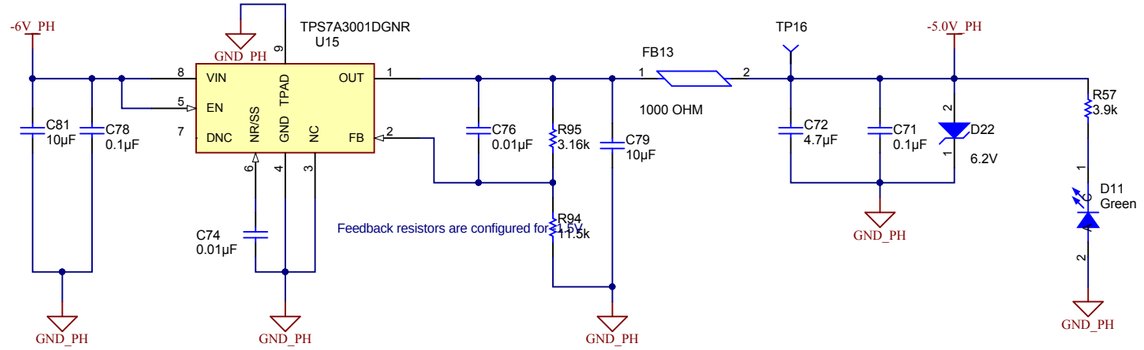


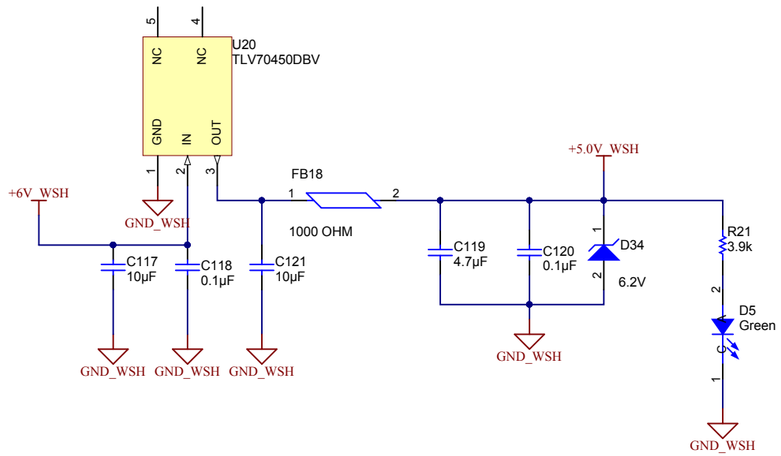
Number: TIDA-00555	Rev: E2	Designed for: Public Release	Mod. Date: 9/7/2015
SVN Rev: Not in version control		Project Title: TIDA-00555	
Drawn By:		Sheet Title: Analog Front End Opamp (Voltage)	
Engineer: Prahlad Supeda		Assembly Variant: 001	Sheet: 5 of 9
		File: Pg5-Voltage signal condtioning.SchDoc	Size: B
		Contact: http://www.ti.com/support	http://www.ti.com

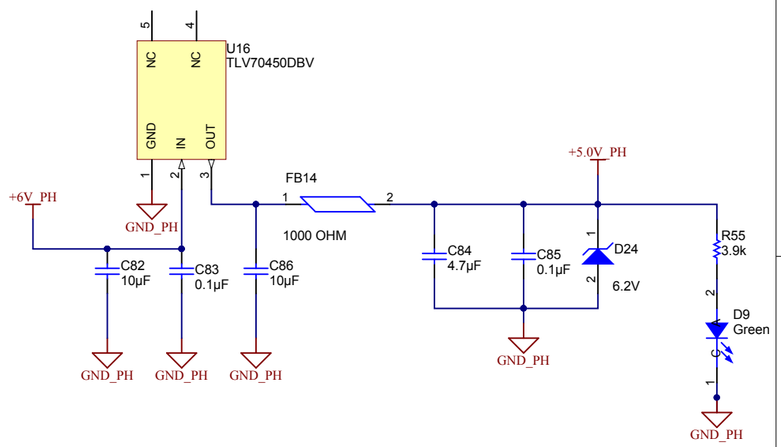




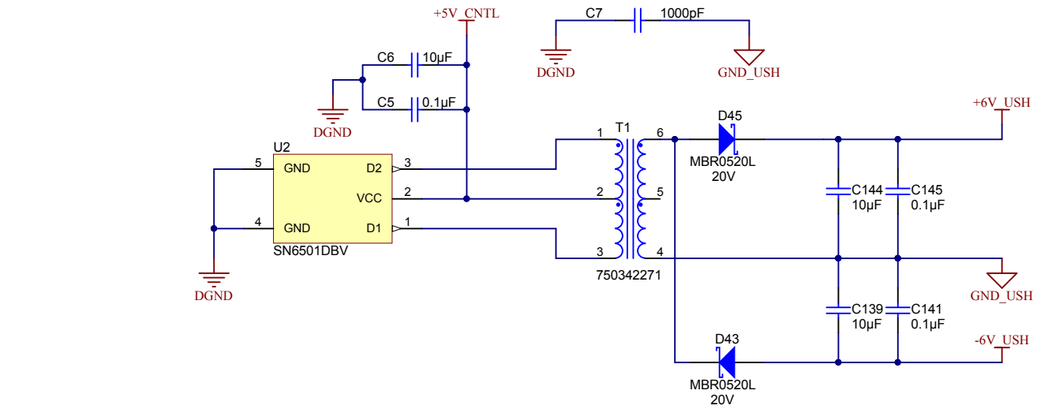


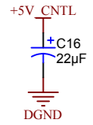


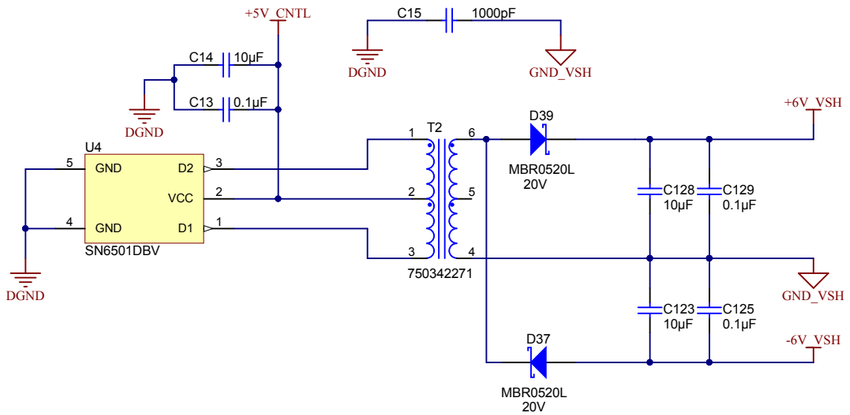


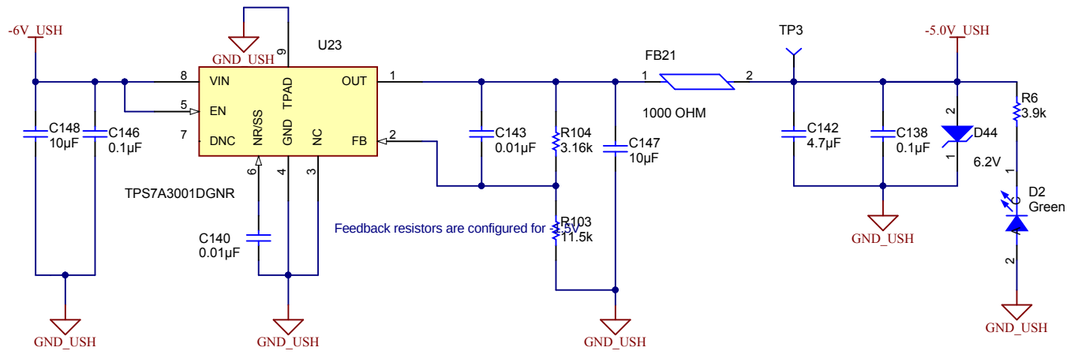


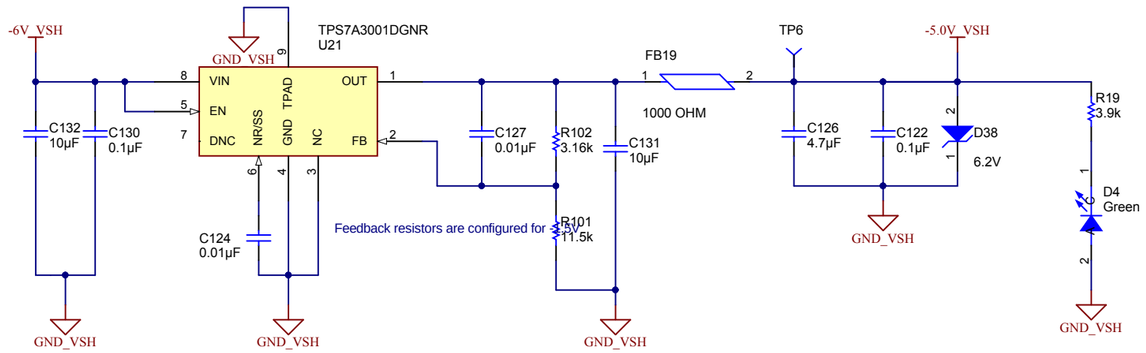
Number: TIDA-00555	Rev: E2	Designed for: Public Release	Mod. Date: 9/7/2015
SVN Rev: Not in version control		Project Title: TIDA-00555	
Drawn By:		Sheet Title: Isolated Poser Supply 2	
Engineer: Prahlad Supeda		Assembly Variant: 001	Sheet: 6 of 9
		File: Pg6-Power supply 1.SchDoc	Size: B
		Contact: http://www.ti.com/support	http://www.ti.com

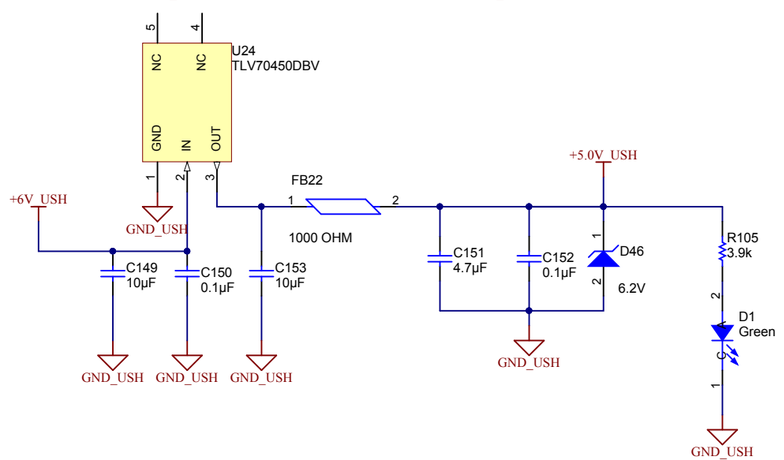


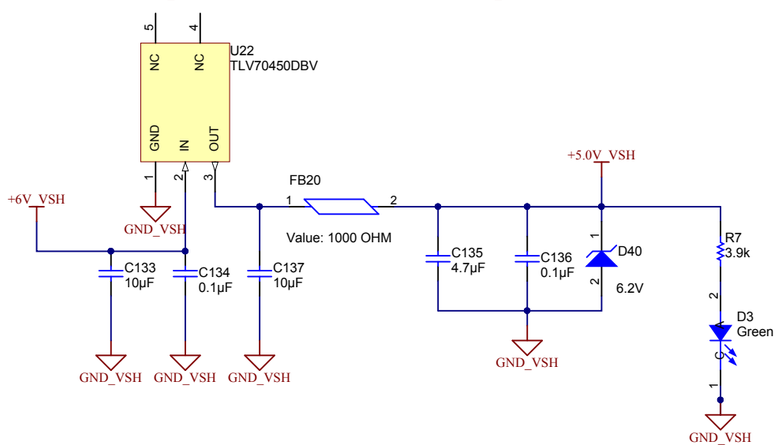












Number: TIDA-00555	Rev: E2	Designed for: Public Release	Mod. Date: 9/7/2015
SVN Rev: Not in version control		Project Title: TIDA-00555	
Drawn By:		Sheet Title: Isolated Poser Supply 1	
Engineer: Prahlad Supeda		Assembly Variant: 001	Sheet: 7 of 9
		File: Pg7-Power supply 2.SchDoc	Size: B
		Contact: http://www.ti.com/support	

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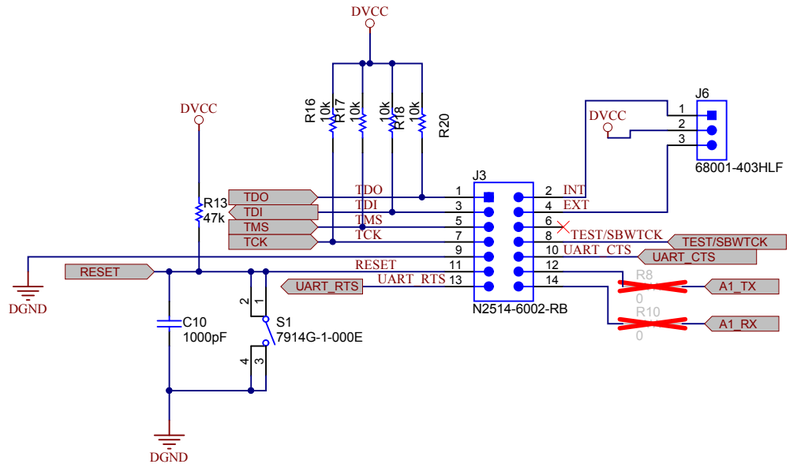
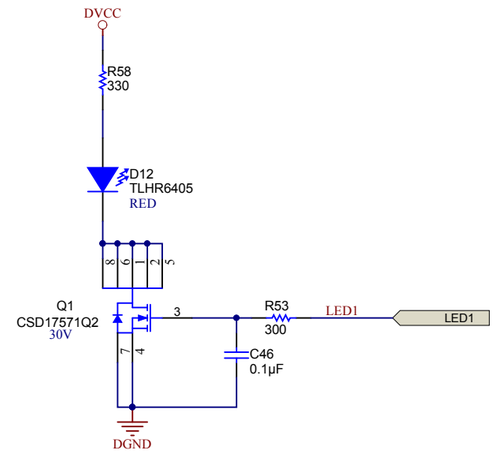
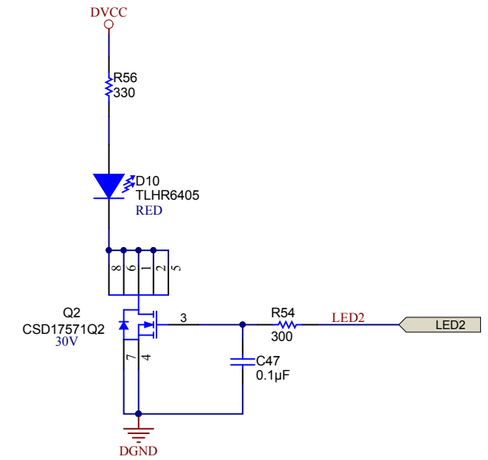
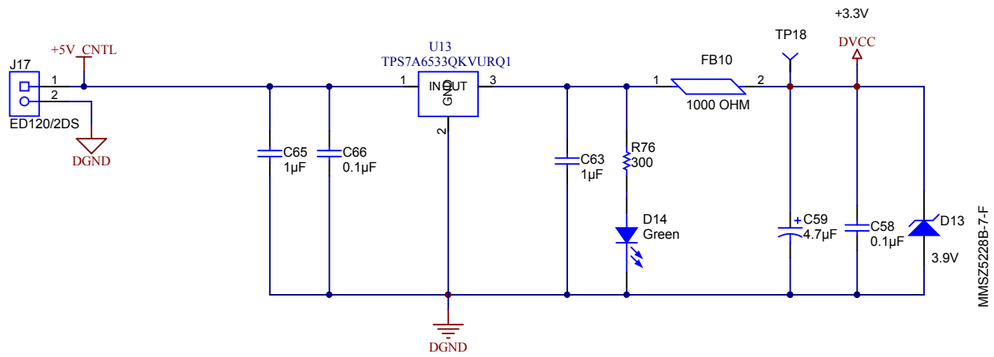
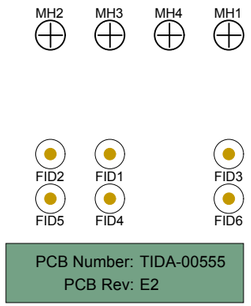








		Designed for: Public Release	Mod. Date: 9/7/2015
		Project Title: TIDA-00555	
Number: TIDA-00555	Rev: E2	Sheet Title: JTAG Interface + LEDs + Decoupling Capacitors	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 8 of 9
Drawn By:		File: Pg8.JTAG Interface + LEDs + Decoupling Capacitors.SchDoc	Size: B
Engineer: Prahlad Supeda		Contact: http://www.ti.com/support	http://www.ti.com



PCB
LOGO

Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

LBL1
PCB Label

ZZ1
Label Assembly Note

ZZ2
Assembly Note

ZZ3
Assembly Note

ZZ4
Assembly Note

		Designed for: Public Release	Mod. Date: 9/2/2015
		Project Title: TIDA-00555	
Number: TIDA-00555	Rev: E2	Sheet Title: Hardware - MIscellaneous	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 9 of 9
Drawn By:		File: Pg9-Hardware.SchDoc	Size: B
Engineer: Prahlad Supeda		Contact: http://www.ti.com/support	

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