

Page 2 Block Diagram

Page 3 10/100 ETHERNET PHY

Page 4 CLOCK GENERATOR

Page 5 RJ45 CONNECTOR

Page 6 MII / RMII INTERFACE

Page 7 PHY CONFIGURATION

Page 8 POWER SUPPLY

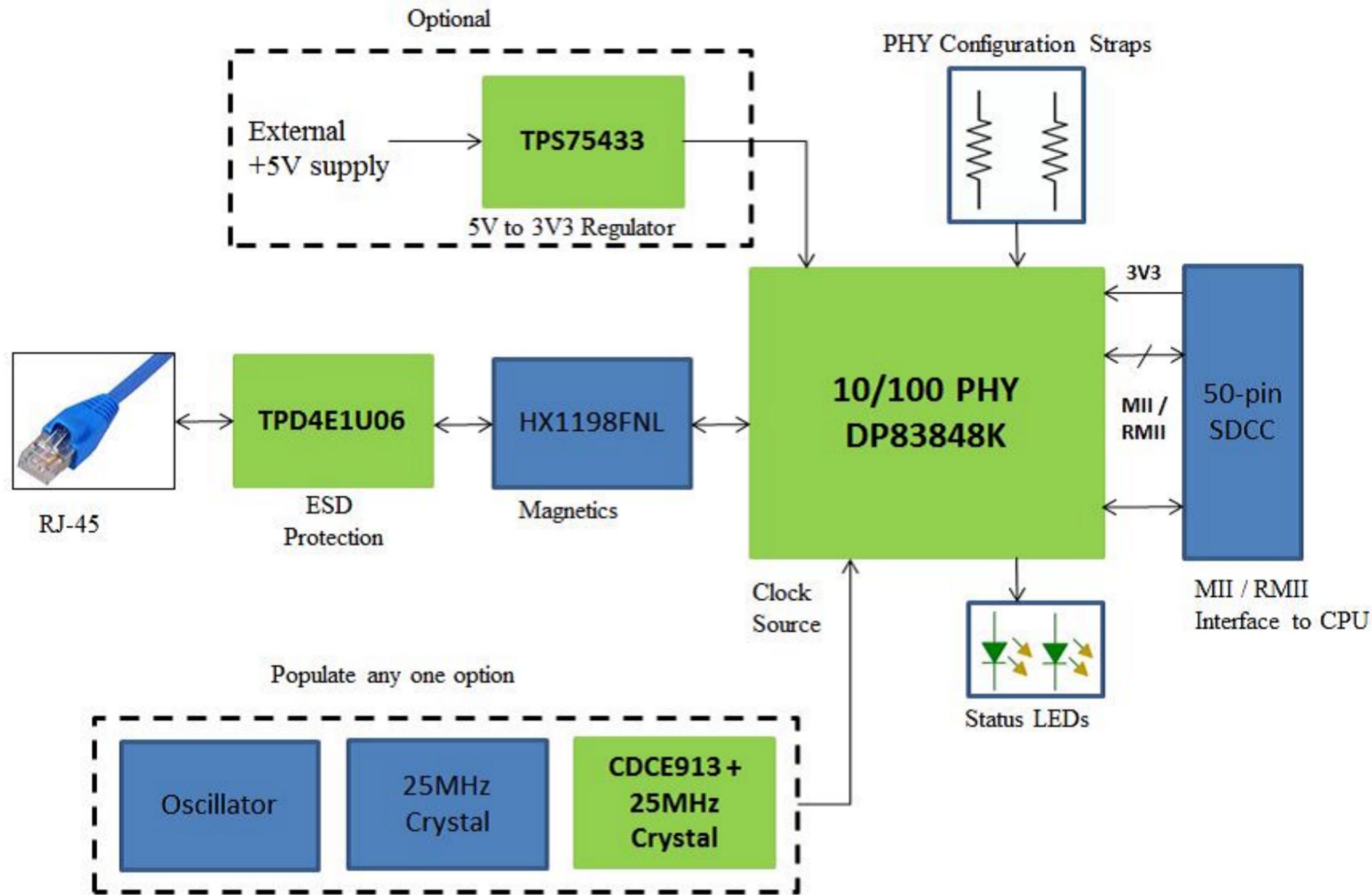
Page 9 Mounting holes, Fiducials Marking

Revision History	
Revision	Notes

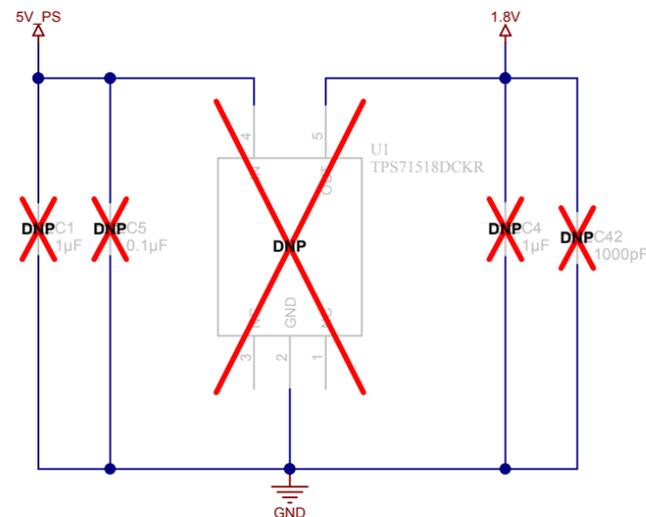
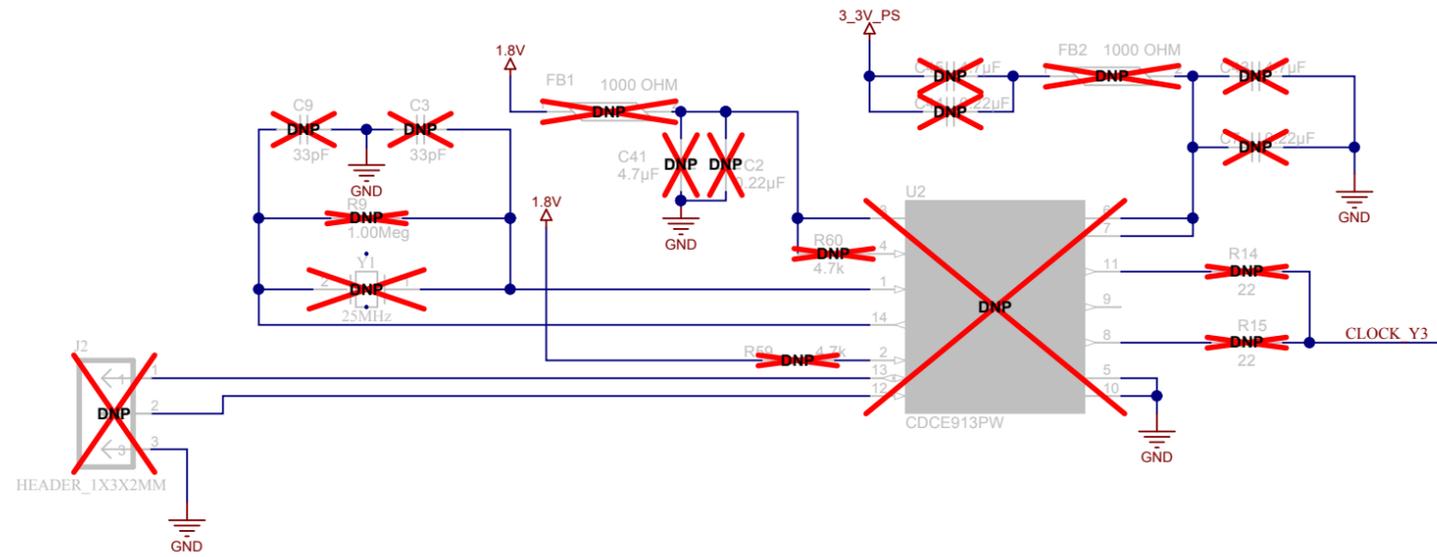
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Number: TIDA-00190	Rev: E1	Designed for: Public Release	Mod. Date: 6/4/2014
SVN Rev: Not in version control	Sheet: 1 of 9	Project Title:	
Drawn By:	File: Pg1 Index Page.SchDoc	Sheet Title:	
Engineer: Enter name of project lead	Contact: http://www.ti.com/support	Size: B	







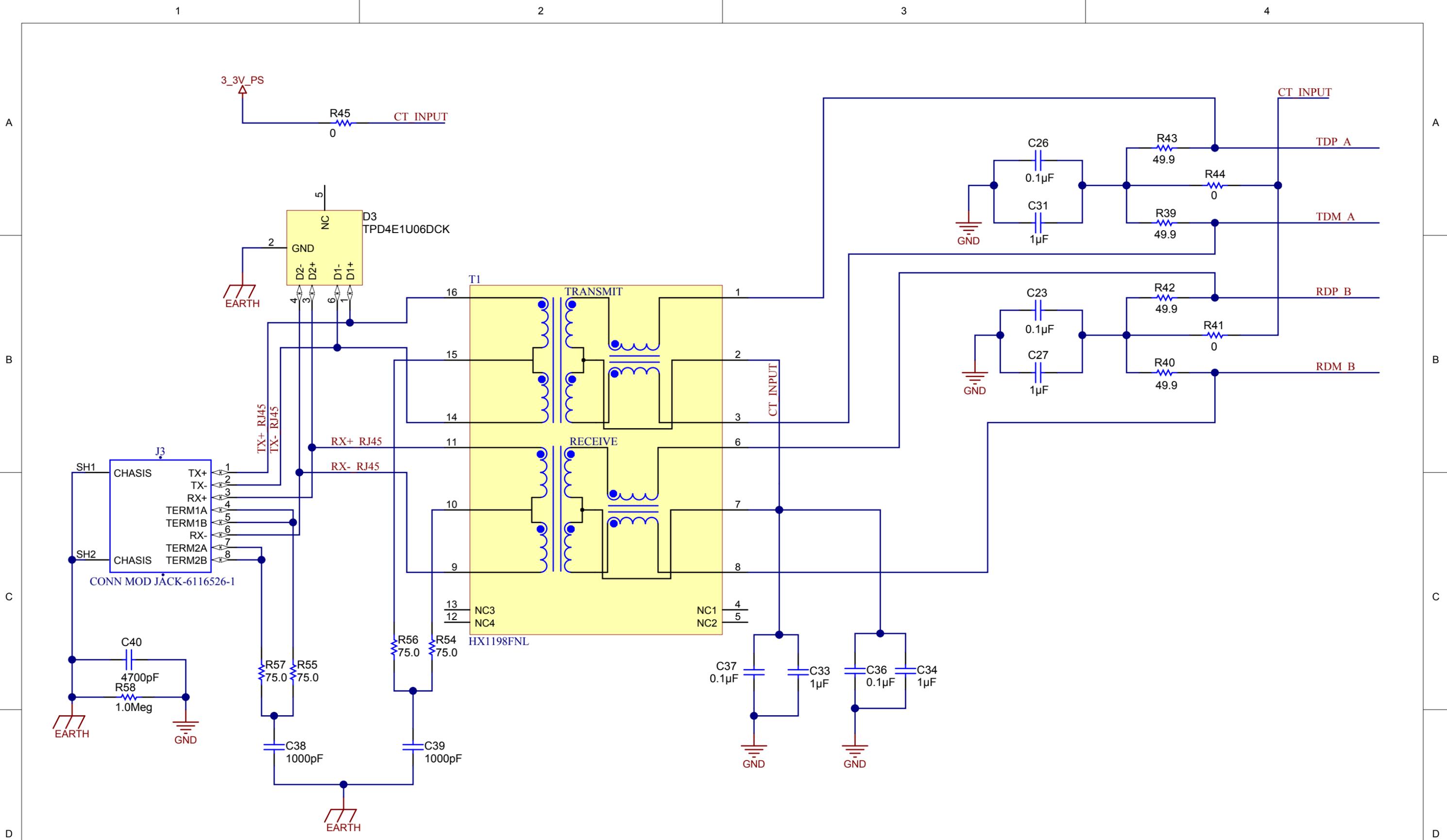


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SVN Rev: Not in version control	Sheet: 4 of 9	Project Title:	
Drawn By:	File: Pg4.SchDoc	Sheet Title:	
Engineer: Enter name of project lead	Contact: http://www.ti.com/support	Assembly Variant: 001	Size: B



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Number: **TIDA-00190** Rev: **E1**  
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Designed for: **Public Release** Mod. Date: 6/4/2014  
 Project Title:  
 Sheet Title:  
 Assembly Variant: **001** Sheet: **5** of **9**  
 File: **Pg5.SchDoc** Size: **A4**  
 Contact: <http://www.ti.com/support>



1

2

3

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A

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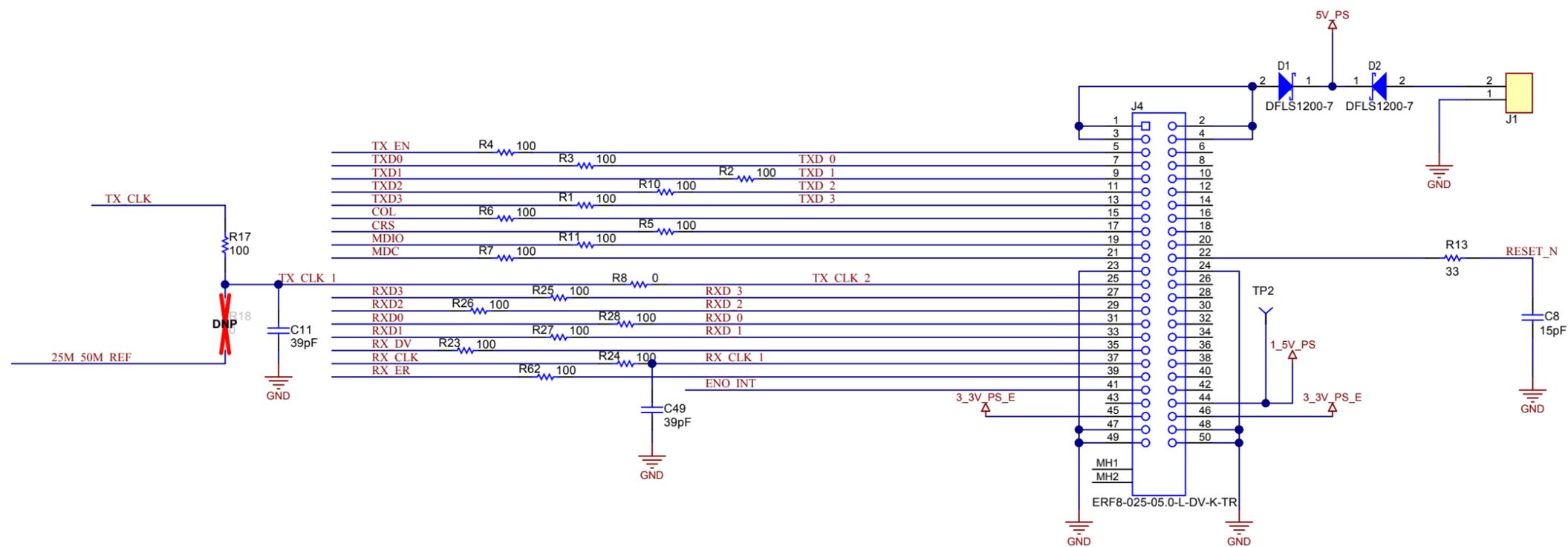
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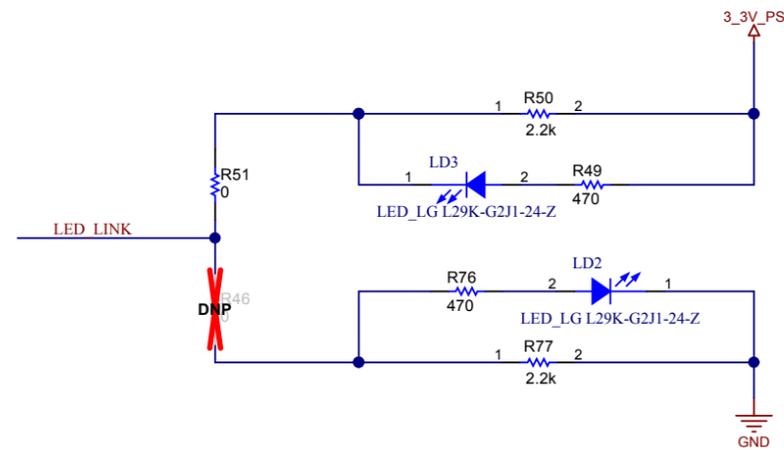
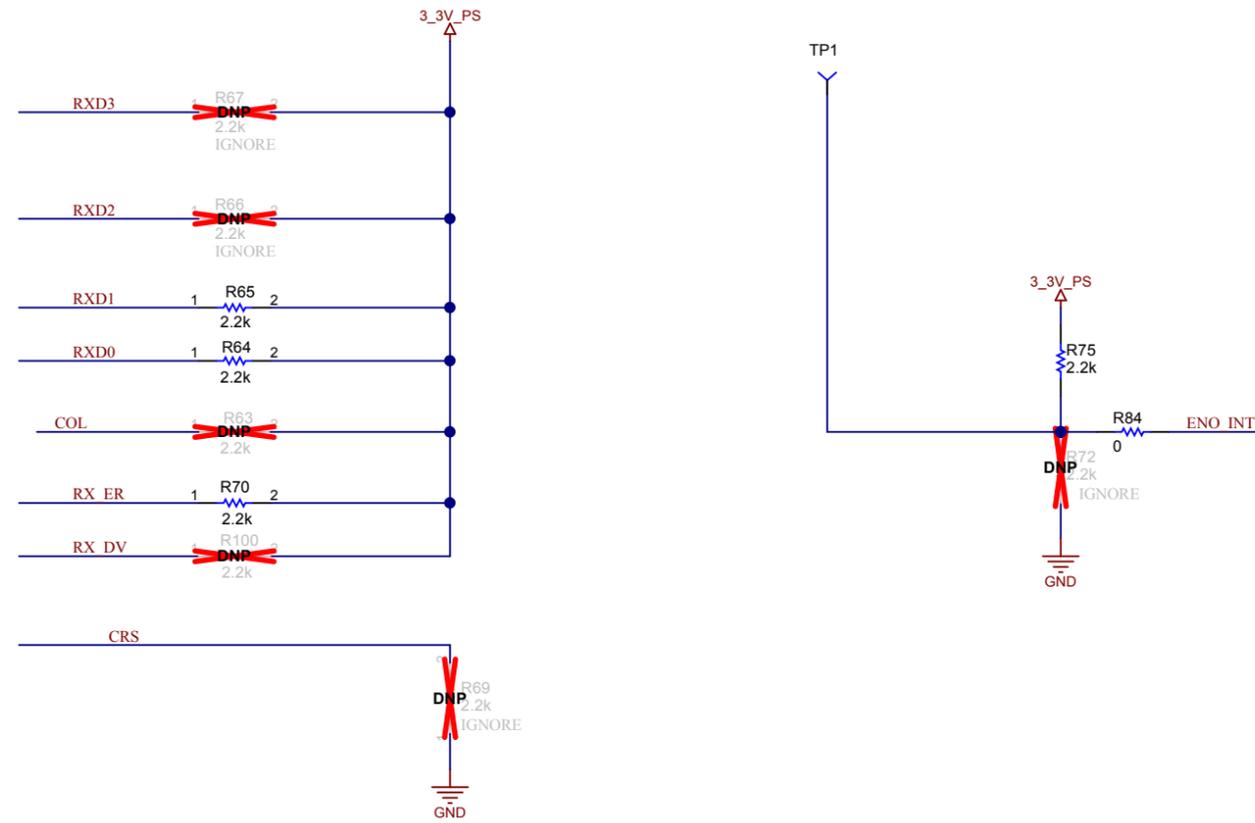
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4



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# CONFIGURATION PINS

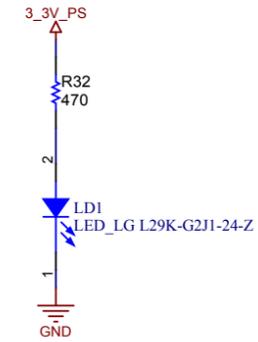
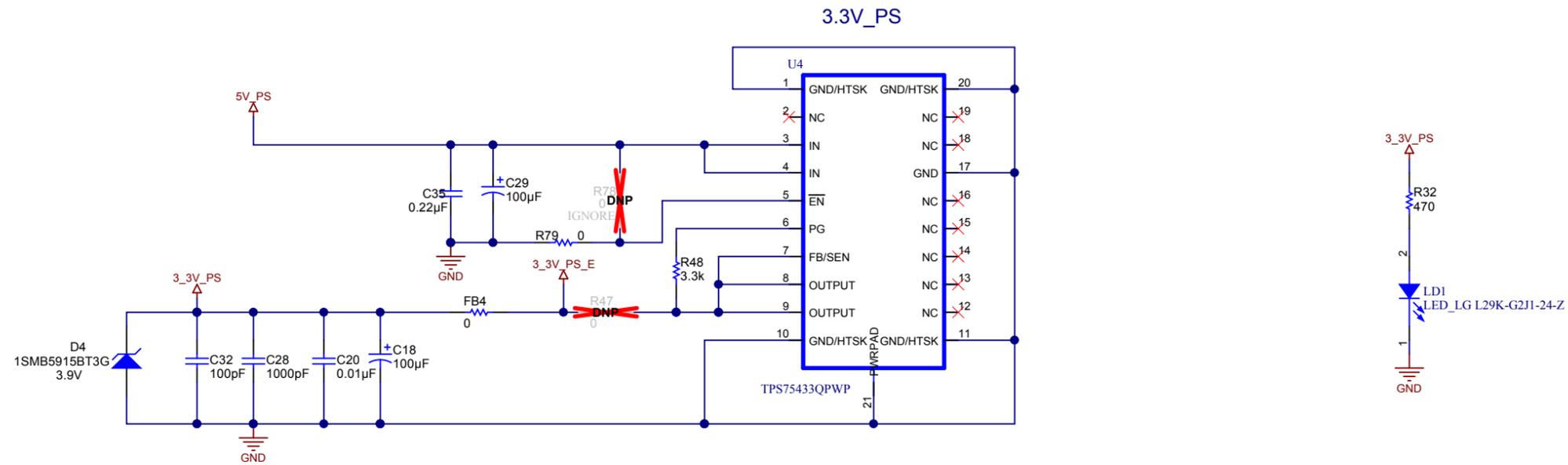


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SVN Rev: Not in version control	Sheet: 7 of 9	Project Title:	Assembly Variant: 001
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Number: TIDA-00190	Rev: E1	Designed for: Public Release	Mod. Date: 6/4/2014
SVN Rev: Not in version control	Sheet: 8 of 9	Project Title:	
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Engineer: Enter name of project lead	Contact: http://www.ti.com/support	Assembly Variant: 001	Size: B

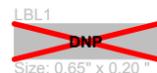


Mounting holes



PCB Number: TIDA-00190  
PCB Rev: E1

PCB  
LOGO  
Texas Instruments



ZZ1  
**Label Assembly Note**  
This Assembly Note is for PCB labels only

ZZ2  
**Assembly Note**  
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3  
**Assembly Note**  
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4  
**Assembly Note**  
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

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