

Flat-Clamp TVS Layout in SMA/SMB Footprints

Alec Forbes



For systems in need of transient IEC 61000-4-5 surge protection, TI's new Flat-Clamp family of TVS devices offers flatter voltage clamping, [improved reliability](#), and lower leakage currents in a much smaller package than industry standard solutions, leading to more efficient and effective input protection design. For more information on the Flat-Clamp family implementation and advantages, please see the [Flat-Clamp surge protection technology for efficient system protection](#) white paper.

While TI's much smaller device package is critical in many space constrained applications, for applications where space is not a factor it can be inconvenient that the Flat-Clamp devices do not share a footprint with industry standard SMA/SMB surge diode package footprints.

that there is no overlap in solder pads. The examples below show layout compatibility with the SMA package, however the SMB package is very similar with slightly taller pads that not should not affect the design.

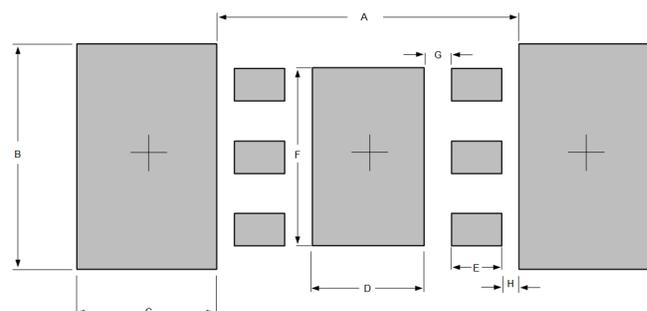


Figure 2. Recommended Footprint

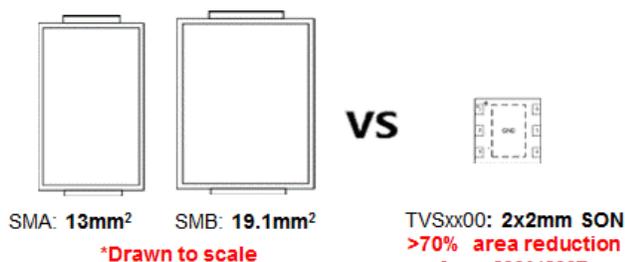


Figure 1. Package Size Comparison

For designers that want to use a common input stage for multiple designs, there is an advantage to being able to share a package footprint. A shared footprint enables a designer to use the same layout when switching between designs that require the precise Flat-Clamp voltage regulation and designs that are able to accept the poor voltage regulation of standard TVS diodes.

With creative use of board layout, it is possible to create a PCB footprint that can be used during production for both the unidirectional Flat-Clamp diodes 2x2 mm standard outline no-leads (SON) package and the conventional SMA/SMB package. This can be done because the large separation between the leads on the SMA/SMB package enables the entire SON package to sit in between the leads, so

Table 1. Stencil Dimensions

Dimension	Millimeters (Mils) Nominal
A	2.7 (106)
B	2.1 (83)
C	1.27 (50)
D	1.01 (40)
E	0.4572 (18)
F	1.6 (63)
G	0.25 (10)
H	0.14 (53)

During layout placement, superimpose both footprints with the SON package fitting between the SMA/SMB pins. Short the IN and GND pins of both packages respectively with a copper pour and then use solder mask openings for the individual pins. Figure 3 shows the solder mask openings in green and the copper planes that electrically short the pins in red. Keeping the solder mask openings separate is important to prevent assembly problems during reflow. The recommended SMA/SMB footprint will vary based on vendor, however the package is standardized and will allow for the layout shown in Figure 3. The landing pads above will allow for reliable manufacturing for SMA/SMB packages regardless of recommended manufacturer footprint.

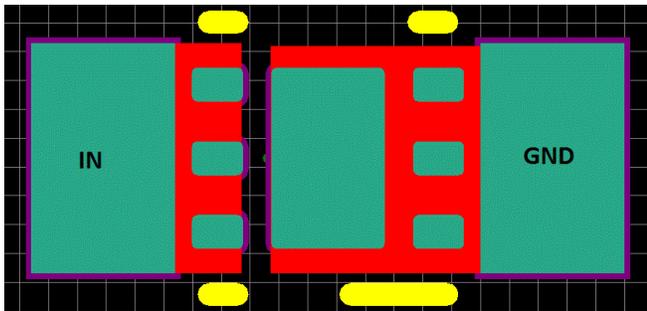


Figure 3. Layout Example

In addition, because the solder mask openings for the SMA/SMB and SON footprints are electrically shorted, if the SMA/SMB pins incidentally create a short there will not be any problems.

Figures 4 and 5 show the Flat-clamp and standard SMA respectively on the combined footprint

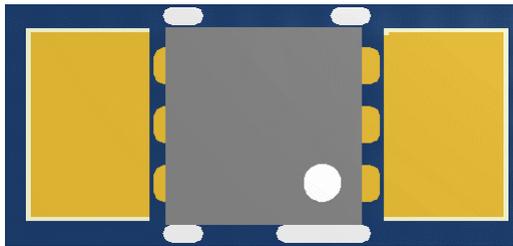


Figure 4. Flat-Clamp SON Package Populated



Figure 5. SMA Device Populated

Figure 6 shows a side view of the SMA device populated, showing that the pins of SMA package do not contact the solder mask of the SON footprint.

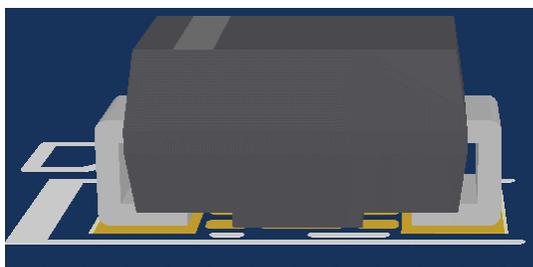


Figure 6. Package Footprint Comparison

This layout allows engineers to improve their design flexibility by having one layout for multiple TVS footprints, removing any challenge of TI's uniquely small footprint. For designers that do not always need the small size of the 2x2 mm SON package, this strategy can save costs and time by enabling a common front end layout with multiple protection options.

To ease the transition towards a new footprint, TI also offers a [Flat-Clamp adapter board kit](#) that easily enables evaluation of the Flat-Clamp devices in an existing SMA/SMB footprint, however the kit is intended for evaluation only and not for production.

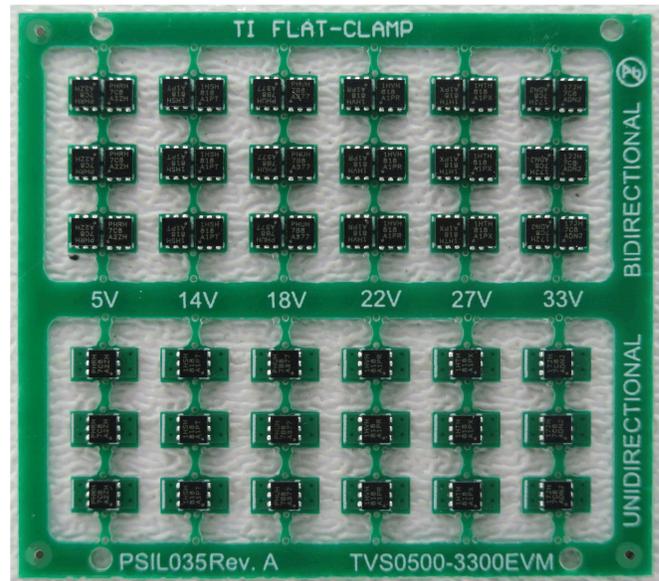


Figure 7. Flat-Clamp Evaluation Kit

Table 2. Device Recommendations

Device	V _{RWM} (V)	V _{CLAMP} (V)	I _{LEAK} (nA)	I _{PP} (A, 8/20 μs)
TVS3300	33	38	19	43
TVS2700	27	32.5	1.7	43
TVS2200	22	27.7	3.2	40
TVS1800	18	22.8	0.5	40
TVS1400	14	18.4	2	40
TVS0500	5	9.2	0.07	35

Related Documentation

[Flat-Clamp surge protection technology for efficient system protection](#)

[TVS Surge Protection in High-Temperature Environments](#)

[TVS0500-3300EVM Adapter Board](#)

IMPORTANT NOTICE FOR TI DESIGN INFORMATION AND RESOURCES

Texas Instruments Incorporated ("TI") technical, application or other design advice, services or information, including, but not limited to, reference designs and materials relating to evaluation modules, (collectively, "TI Resources") are intended to assist designers who are developing applications that incorporate TI products; by downloading, accessing or using any particular TI Resource in any way, you (individually or, if you are acting on behalf of a company, your company) agree to use it solely for this purpose and subject to the terms of this Notice.

TI's provision of TI Resources does not expand or otherwise alter TI's applicable published warranties or warranty disclaimers for TI products, and no additional obligations or liabilities arise from TI providing such TI Resources. TI reserves the right to make corrections, enhancements, improvements and other changes to its TI Resources.

You understand and agree that you remain responsible for using your independent analysis, evaluation and judgment in designing your applications and that you have full and exclusive responsibility to assure the safety of your applications and compliance of your applications (and of all TI products used in or for your applications) with all applicable regulations, laws and other applicable requirements. You represent that, with respect to your applications, you have all the necessary expertise to create and implement safeguards that (1) anticipate dangerous consequences of failures, (2) monitor failures and their consequences, and (3) lessen the likelihood of failures that might cause harm and take appropriate actions. You agree that prior to using or distributing any applications that include TI products, you will thoroughly test such applications and the functionality of such TI products as used in such applications. TI has not conducted any testing other than that specifically described in the published documentation for a particular TI Resource.

You are authorized to use, copy and modify any individual TI Resource only in connection with the development of applications that include the TI product(s) identified in such TI Resource. NO OTHER LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE TO ANY OTHER TI INTELLECTUAL PROPERTY RIGHT, AND NO LICENSE TO ANY TECHNOLOGY OR INTELLECTUAL PROPERTY RIGHT OF TI OR ANY THIRD PARTY IS GRANTED HEREIN, including but not limited to any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information regarding or referencing third-party products or services does not constitute a license to use such products or services, or a warranty or endorsement thereof. Use of TI Resources may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

TI RESOURCES ARE PROVIDED "AS IS" AND WITH ALL FAULTS. TI DISCLAIMS ALL OTHER WARRANTIES OR REPRESENTATIONS, EXPRESS OR IMPLIED, REGARDING TI RESOURCES OR USE THEREOF, INCLUDING BUT NOT LIMITED TO ACCURACY OR COMPLETENESS, TITLE, ANY EPIDEMIC FAILURE WARRANTY AND ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

TI SHALL NOT BE LIABLE FOR AND SHALL NOT DEFEND OR INDEMNIFY YOU AGAINST ANY CLAIM, INCLUDING BUT NOT LIMITED TO ANY INFRINGEMENT CLAIM THAT RELATES TO OR IS BASED ON ANY COMBINATION OF PRODUCTS EVEN IF DESCRIBED IN TI RESOURCES OR OTHERWISE. IN NO EVENT SHALL TI BE LIABLE FOR ANY ACTUAL, DIRECT, SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF TI RESOURCES OR USE THEREOF, AND REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

You agree to fully indemnify TI and its representatives against any damages, costs, losses, and/or liabilities arising out of your non-compliance with the terms and provisions of this Notice.

This Notice applies to TI Resources. Additional terms apply to the use and purchase of certain types of materials, TI products and services. These include; without limitation, TI's standard terms for semiconductor products (<http://www.ti.com/sc/docs/stdterms.htm>), [evaluation modules](#), and [samples](http://www.ti.com/sc/docs/sampterm.htm) (<http://www.ti.com/sc/docs/sampterm.htm>).

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2018, Texas Instruments Incorporated