

bq76PL536/536A/536AQ1 Communications Basics

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ABSTRACT

The bq76PL536 can be stacked vertically to monitor up to 192 cells without additional isolation components between the integrated circuits. A high-speed serial peripheral interface (SPI) bus operates between each bq76PL536 to provide reliable communication through a high-voltage battery cell stack. This application report discusses the communication basics of addressing, reading, and writing for the Texas Instruments bq76PL536, a stackable 3-to-6 series battery pack protector and analog front end.

Although only the bq76PL536 is referred to in this document, all information herein applies equally to the bq76PL536A and the bq76PL536AQ1.

1 bq76PL536 Packet Formats

The following illustrations show the Read and Write packet formats of the bq76PL536.



2 bq76PL536 Addressing, Reading, and Writing

2.1 Addressing

When powering up the integrated circuit (IC), the default address is 0. To change it to address 1, write: 01 3B 01 02. Other addresses require that the last two bytes change.

- Write to address 0 = 01
- Write to address control registers = 3B
- Write address to 1 = 01
- Write the CRC = 02

Now the device answers to address 1.

Note the following two things.

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- 1. The length byte is implied because you can only write one byte at a time. No length byte is sent with Write packets.
- 2. The chip is manufactured with CRC enabled, so you must transmit the CRC. CRCs protect against communications errors.

Subsequent devices must receive unique addresses, usually sequential from 0x01 to 0xnn, where nn < 0x3F.

2.2 Reading Data

The following sequence occurs in reading the cell 1 ADC (the cell_1 registers are 0x03 MSB and 0x04 LSB) from the device at 0x01.

Both can be read in one 2-byte read like this: 02 03 02 00 00 00

- Read (logical) address 1 = 02
- Start read at register 3 = 03
- Read 2 bytes (registers 3 and 4) = 02
- Send 0 to clock in byte 1 (register 3 data) = 00*
- Send 0 to clock in byte 2 (register 4 data) = 00*
- Send 0 to clock in CRC = 00*
 *Padding bytes sent by the host CPU to clock out data bytes from the bq76PL536; the value is unimportant

The LSB in the address field is the R/W bit. The 6-bit logical address field is shifted one position left. So, to address device 1 in this example, a "2" is sent for the read address, and a "3" for the write address.

3 bq76PL536 Sample Data Exchange

The Address is 6 bit + LSB R/W bit:

0 for R 1 for W

Therefore, a read of board 1 Cell voltage is as follows:

SEND	STA	RT and LENGTH /	ADDRESS	+	STUFF BYTES TO CLOCK OUT DATA FROM IC		
SEND	IC Addr	Starting Reg	No. Bytes	+	Clk1 for data	Clk2 for data	Clk3 for CRC
SEND	0x02	0x03	0x02	+	0x0	00x00	0x00

The expected return is as follows: (On a read the device generates/returns the CRC)

RCV (clocked out by)	ADDR -	START ·	+ LEN	+	(new data) Re	eg_0x03_result + Ree	g_0x04_result + I0	C_supplied_CRC
RCV (clocked out by)	0x00	0x00	0x00	+	(new data)	0xMSB	0xLSB	0xCRC

You then can calculate the CRC based on the received message to confirm that the transmission was successful:

Transaction example:

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Data sent to target	02 03 02 00 00 00
Data returned from target	00 00 00 19 9A 0D
Calculate the CRC	function_CRC (02 03 02 19 9A) = 0D



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Note that the foregoing example assumes that this device first has been initialized to be address 1.

4 Simple Stack Addressing Algorithm

Each device in the stack must receive a unique address between 0x01 and 0x3E. Address 0x00 is reserved for device discovery and address 0x3F is reserved for BROADCAST messages to all devices. Typical addressing assigns address 1 to the base device, and incremental addresses go up (north) in the stack; 1-2-3-... n.



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CRC Calculations – SMBUS™ (ATM-8) Algorithm

5 CRC Calculations – SMBUS[™] (ATM-8) Algorithm

namespace Communications
{

class CRC8

<pre>private bytell CroTable = { 0x00, 0x07, 0x27, 0x12, 0x12, 0x12, 0x12, 0x15, 0x38, 0x3F, 0x35, 0x31, 0x24, 0x23, 0x2A, 0x2D, 0x70, 0x77, 0x75, 0x79, 0x6C, 0x6E, 0x62, 0x65, 0x48, 0x4F, 0x46, 0x41, 0x54, 0x53, 0x5A, 0x5D, 0xx00, 0x57, 0x5C, 0x5D, 0x5C, 0x5B, 0x5D, 0xx0, 0x77, 0x75, 0x95, 0x6C, 0x8B, 0x8D, 0xx0, 0x70, 0x75, 0x75, 0x70, 0x8B, 0x8D, 0xx0, 0x07, 0x70, 0x75, 0x75, 0x78, 0x70, 0x8D, 0xx0, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x70, 0x8B, 0x8D, 0x8D, 0x8D, 0x8D, 0x8A, 0x8F, 0x8B, 0x81, 0x86, 0x81, 0x80, 0x82, 0x8A, 0x87, 0x80, 0x80, 0x80, 0x80, 0x80, 0x80, 0x74, 0x80, 0x80, 0x81, 0x86, 0x81, 0x80, 0x72, 0x81, 0x80, 0x80, 0x80, 0x80, 0x80, 0x74, 0x80, 0x80, 0x80, 0x80, 0x80, 0x80, 0x74, 0x80, 0x80, 0x80, 0x80, 0x80, 0x81, 0x80, 0x80, 0x80, 0x80, 0x81, 0x86, 0x87, 0x80, 0x80, 0x81, 0x86, 0x81, 0x86, 0x87, 0x80, 0x80, 0x81, 0x84, 0x89, 0x80, 0x81, 0x86, 0x81, 0x88, 0x81, 0x88, 0x81, 0x84, 0x89, 0x81, 0x88, 0x88, 0x88, 0x88, 0x88, 0x88, 0x88, 0x88, 0x88, 0x88, 0x88,</pre>	
<pre>0x100. 0x17, 0x18, 0x13, 0x24, 0x23, 0x22, 0x22, 0x20, 0x70, 0x77, 0x7E, 0x79, 0x66, 0x61, 0x65, 0x65, 0x65, 0x65, 0x88, 0x4F, 0x46, 0x11, 0x64, 0x53, 0x54, 0x55, 0x57, 0x88, 0xFF, 0x6E, 0x11, 0x64, 0x63, 0x64, 0x65, 0x65, 0x88, 0xFF, 0x66, 0x11, 0x64, 0x83, 0x85, 0x85, 0x88, 0xFF, 0x66, 0x11, 0x64, 0x83, 0x85, 0x85, 0x88, 0xFF, 0x66, 0x11, 0x64, 0x83, 0x85, 0x85, 0x87, 0x80, 0x87, 0x88, 0x81, 0x84, 0x80, 0x80, 0x77, 0x50, 0x59, 0x55, 0x58, 0x84, 0x80, 0x82, 0x87, 0x80, 0x89, 0x85, 0x84, 0x80, 0x82, 0x85, 0x87, 0x80, 0x89, 0x85, 0x84, 0x80, 0x82, 0x85, 0x87, 0x80, 0x89, 0x85, 0x84, 0x80, 0x82, 0x85, 0x87, 0x80, 0x89, 0x85, 0x84, 0x80, 0x82, 0x82, 0x87, 0x80, 0x89, 0x85, 0x83, 0x94, 0x90, 0x90, 0x90, 0x27, 0x30, 0x29, 0x25, 0x30, 0x30, 0x30, 0x04, 0x0, 0x57, 0x50, 0x59, 0x55, 0x48, 0x46, 0x45, 0x42, 0x67, 0x60, 0x59, 0x55, 0x80, 0x94, 0x43, 0x44, 0x45, 0x86, 0x87, 0x80, 0x95, 0x92, 0x98, 0x90, 0x81, 0x66, 0x87, 0x80, 0x95, 0x92, 0x98, 0x90, 0x81, 0x66, 0x87, 0x80, 0x95, 0x92, 0x98, 0x90, 0x61, 0x66, 0x67, 0x60, 0x75, 0x72, 0x78, 0x70, 0x51, 0x60, 0x79, 0x60, 0x75, 0x72, 0x78, 0x70, 0x51, 0x60, 0x79, 0x60, 0x75, 0x72, 0x78, 0x70, 0x51, 0x60, 0x79, 0x60, 0x73, 0x30, 0x33, 0x34, 0x48, 0x49, 0x40, 0x47, 0x60, 0x30, 0x33, 0x34, 0x48, 0x49, 0x40, 0x47, 0x52, 0x55, 0x52, 0x58, 0x76, 0x11, 0x76, 0x77, 0x68, 0x60, 0x64, 0x63, 0x38, 0x39, 0x30, 0x37, 0x22, 0x25, 0x26, 0x78, 0x78, 0x19, 0x00, 0x77, 0x82, 0x85, 0x82, 0x88, 0x80, 0x88, 0x76, 0x11, 0x76, 0x77, 0x68, 0x60, 0x75, 0x72, 0x78, 0x68, 0x11, 0x78, 0x89, 0x40, 0x47, 0x52, 0x55, 0x50, 0x78, 0x78, 0x19, 0x00, 0x77, 0x82, 0x85, 0x67, 0x88, 0x66, 0x11, 0x78, 0x79, 0x64, 0x63, 0x38, 0x89, 0x00, 0x77, 0x82, 0x85, 0x86, 0x83, 0x76, 0x11, 0x78, 0x79, 0x64, 0x43, 0x78, 0x11, 0x78, 0x79, 0x64, 0x63, 0x76, 0x11, 0x78, 0x79, 0x62, 0x65, 0x76, 0x78, 0x76, 0x11, 0x78, 0x80, 0x70, 0x72, 0x65, 0x76, 0x78, 0x76, 0x10, 0x70, 0x80, 0x70, 0x72, 0x65, 0x70, 0x78, 0x76, 0x10, 0</pre>	<pre>private byte[] CrcTable = {</pre>
<pre>DX38, 0439, 0430, 0431, 0444, 0424, 0424, 0424, 0424, 0424, 0424, 0424, 0434, 0454, 0455, 0459, 0455, 0459, 0457, 0458, 0459, 0457, 0453, 0464, 0453, 0464, 0453, 0454, 0453, 0454, 0457, 0457, 0457, 0457, 0453, 0457, 0453, 0454, 0453, 0454, 0457, 0457, 0457, 0453, 0454, 0453, 0454, 0453, 0457, 0457, 0457, 0458, 0457, 0453, 0454, 0453, 0457, 0458, 0457, 0458, 0457, 0453, 0458, 0457, 0453, 0458, 0457, 0453, 0458, 0457, 0453, 0458, 0457, 0453, 0458, 0457, 0458, 0457, 0458, 0457, 0453, 0458, 0457, 0458, 0457, 0458, 0457, 0453, 0458, 0457, 0453, 0458, 0457, 0458, 0457, 0458, 0457, 0453, 0458, 0457, 0</pre>	0×00 , 0×07 , $0 \times 0E$, 0×09 , $0 \times 1C$, $0 \times 1B$, 0×12 , 0×15 ,
<pre>0x40, 0x7, 0x1, 0x1, 0x1, 0x5, 0x5, 0x5, 0x5, 0x5, 0x5, 0x60, 0x7, 0x8E, 0x89, 0x70, 0x78, 0x72, 0x75, 0x80, 0x77, 0x8E, 0x89, 0x70, 0x78, 0x72, 0x75, 0x80, 0x77, 0x85, 0x81, 0x60, 0x81, 0x80, 0x81, 0x81, 0x81, 0x77, 0x70, 0x79, 0x78, 0x78, 0x70, 0x78, 0x72, 0x74, 0x77, 0x70, 0x79, 0x78, 0x74, 0x70, 0x70, 0x74, 0x77, 0x80, 0x89, 0x82, 0x18, 0x70, 0x71, 0x77, 0x80, 0x89, 0x82, 0x18, 0x70, 0x71, 0x77, 0x80, 0x89, 0x82, 0x18, 0x70, 0x71, 0x77, 0x80, 0x79, 0x85, 0x74, 0x70, 0x71, 0x77, 0x80, 0x79, 0x89, 0x70, 0x75, 0x72, 0x71, 0x77, 0x80, 0x79, 0x80, 0x75, 0x72, 0x71, 0x74, 0x87, 0x80, 0x77, 0x80, 0x75, 0x72, 0x71, 0x74, 0x87, 0x80, 0x77, 0x80, 0x75, 0x72, 0x71, 0x74, 0x89, 0x88, 0x87, 0x80, 0x75, 0x72, 0x72, 0x74, 0x89, 0x88, 0x87, 0x80, 0x75, 0x72, 0x74, 0x74, 0x89, 0x88, 0x87, 0x80, 0x75, 0x72, 0x74, 0x74, 0x89, 0x88, 0x87, 0x80, 0x75, 0x72, 0x74, 0x74, 0x81, 0x55, 0x55, 0x58, 0x44, 0x43, 0x44, 0x79, 0x50, 0x57, 0x80, 0x75, 0x72, 0x74, 0x74, 0x51, 0x55, 0x57, 0x80, 0x75, 0x72, 0x74, 0x74, 0x51, 0x55, 0x57, 0x80, 0x75, 0x72, 0x74, 0x74, 0x51, 0x50, 0x57, 0x80, 0x75, 0x72, 0x74, 0x74, 0x51, 0x50, 0x57, 0x80, 0x77, 0x22, 0x64, 0x64, 0x63, 0x75, 0x93, 0x30, 0x47, 0x52, 0x55, 0x50, 0x74, 0x55, 0x50, 0x00, 0x77, 0x22, 0x28, 0x84, 0x83, 0x76, 0x90, 0x00, 0x77, 0x22, 0x25, 0x20, 0x84, 0x83, 0x76, 0x91, 0x90, 0x47, 0x22, 0x25, 0x20, 0x84, 0x83, 0x76, 0x93, 0x40, 0x47, 0x24, 0x44, 0x3, 0x75, 0x93, 0x30, 0x47, 0x22, 0x55, 0x70, 0x73, 0x45, 0x93, 0x30, 0x47, 0x22, 0x25, 0x20, 0x84, 0x83, 0x76, 0x90, 0x00, 0x77, 0x22, 0x25, 0x24, 0x83, 0x76, 0x90, 0x00, 0x77, 0x22, 0x25, 0x74, 0x73, 0x84, 0x94, 0x84, 0x84, 0x84, 0x84, 0x83, 0x75, 0x94, 0x84, 0x84, 0x84, 0x84, 0x84, 0x83, 0x75, 0x94, 0x84, 0x84, 0x84, 0x84, 0x83, 0x75, 0</pre>	0x38, 0x37, 0x36, 0x31, 0x24, 0x23, 0x2A, 0x2D, 0x2D, 0x72,
<pre>Dx80, Dx87, Dx82, Dx89, Dx80, Dx80, Dx82, Dx82, Dx85, Dx80, Dx87, Dx86, Dx81, Dx84, Dx83, Dx84, Dx82, Dx85, Dx88, Dx87, Dx86, Dx88, Dx80, Dx88, Dx82, Dx85, Dx87, Dx80, Dx86, Dx88, Dx88, Dx88, Dx84, Dx87, Dx84, Dx87, Dx80, Dx86, Dx88, Dx88, Dx84, Dx87, Dx84, Dx87, Dx80, Dx80, Dx88, Dx84, Dx81, Dx84, Dx82, Dx84, Dx87, Dx80, Dx80, Dx88, Dx84, Dx81, Dx84, Dx82, Dx87, Dx80, Dx80, Dx84, Dx84, Dx81, Dx84, Dx82, Dx87, Dx80, Dx80, Dx84, Dx84, Dx82, Dx84, Dx82, Dx87, Dx80, Dx80, Dx84, Dx84, Dx82, Dx84, Dx82, Dx87, Dx80, Dx81, Dx86, Dx83, Dx84, Dx81, Dx84, Dx87, Dx80, Dx81, Dx86, Dx85, Dx82, Dx84, Dx82, Dx84, Dx87, Dx80, Dx81, Dx86, Dx87, Dx88, Dx84, Dx81, Dx84, Dx87, Dx87, Dx80, Dx85, Dx82, Dx84, Dx82, Dx84, Dx86, Dx87, Dx80, Dx85, Dx82, Dx84, Dx82, Dx84, Dx82, Dx86, Dx87, Dx80, Dx87, Dx88, Dx84, Dx83, Dx84, Dx86, Dx87, Dx80, Dx87, Dx83, Dx84, Dx83, Dx84, Dx86, Dx86, Dx87, Dx80, Dx87, Dx83, Dx84, Dx84, Dx84, Dx85, Dx86, Dx87, Dx80, Dx87, Dx84, Dx83, Dx84, Dx86, Dx80, Dx87, Dx80, Dx87, Dx84, Dx82, Dx85, Dx82, Dx86, Dx80, Dx87, Dx80, Dx87, Dx84, Dx82, Dx85, Dx82, Dx86, Dx80, Dx07, Dx84, Dx82, Dx85, Dx82, Dx85, Dx82, Dx86, Dx80, Dx07, Dx84, Dx82, Dx85, Dx82, Dx85, Dx82, Dx88, Dx96, Dx81, Dx88, Dx87, Dx84, Dx84, Dx84, Dx83, Dx96, Dx81, Dx88, Dx87, Dx84, Dx84, Dx84, Dx83, Dx96, Dx81, Dx88, Dx87, Dx84, Dx84, Dx84, Dx83, Dx96, Dx81, Dx88, Dx87, Dx84, Dx87, Dx84, Dx83, Dx96, Dx81, Dx88, Dx87, Dx84, Dx84, Dx84, Dx83, Dx86, Dx80, Dx97, Dx80, Dx87, Dx84, Dx84, Dx83, Dx86, Dx80, Dx80, Dx87, Dx84, Dx84, Dx83, Dx84, Dx83, Dx84, Dx84, Dx83, Dx84, D</pre>	$0x^{10}$, $0x^{17}$, $0x^{16}$
<pre></pre>	0xE0, 0xE7, 0xEE, 0xE9, 0xFC, 0xFB, 0xF2, 0xF5,
<pre>bx90, 0x97, 0x94F, 0x99, 0x8C, 0x88, 0x80, 0x80, 0x80, 0x87, 0x80, 0x46, 0xx1, 0x84, 0x81, 0x80, 0x80, 0x87, 0x80, 0x89, 0x88, 0x81, 0x80, 0x80, 0x80, 0x80, 0x87, 0x80, 0x81, 0x86, 0x73, 0x94, 0x90, 0x90, 0x87, 0x80, 0x11, 0x16, 0x93, 0x94, 0x90, 0x90, 0x17, 0x18, 0x11, 0x16, 0x73, 0x94, 0x00, 0x00, 0x57, 0x50, 0x59, 0x52, 0x18, 0x40, 0x00, 0x00, 0x57, 0x50, 0x59, 0x52, 0x48, 0x40, 0x45, 0x42, 0x69, 0x68, 0x51, 0x66, 0x73, 0x74, 0x70, 0x7A, 0x89, 0x68, 0x51, 0x66, 0x73, 0x74, 0x70, 0x7A, 0x89, 0x68, 0x57, 0x50, 0x40, 0x85, 0x22, 0x45, 0x20, 0x61, 0x66, 0x57, 0x50, 0x40, 0x40, 0x43, 0x44, 0x79, 0x70, 0x70, 0x70, 0x70, 0x70, 0x61, 0x66, 0x57, 0x50, 0x20, 0x85, 0x22, 0x70, 0x61, 0x66, 0x57, 0x50, 0x50, 0x20, 0x80, 0x70, 0x61, 0x66, 0x57, 0x50, 0x50, 0x20, 0x80, 0x00, 0x61, 0x66, 0x77, 0x70, 0x75, 0x72, 0x70, 0x51, 0x56, 0x57, 0x50, 0x40, 0x40, 0x43, 0x44, 0x19, 0x18, 0x77, 0x10, 0x50, 0x20, 0x80, 0x00, 0x21, 0x26, 0x27, 0x20, 0x50, 0x22, 0x25, 0x50, 0x54, 0x76, 0x71, 0x70, 0x77, 0x52, 0x55, 0x52, 0x55, 0x55, 0x38, 0x39, 0x30, 0x37, 0x82, 0x52, 0x50, 0x64, 0x63, 0x38, 0x99, 0x30, 0x37, 0x22, 0x25, 0x20, 0x28, 0x66, 0x81, 0x88, 0x97, 0x82, 0x82, 0x80, 0x84, 0x83, 0x96, 0x91, 0x98, 0x97, 0x82, 0x82, 0x80, 0x84, 0x83, 0x96, 0x91, 0x98, 0x97, 0x82, 0x85, 0x62, 0x28, 0x66, 0x81, 0x88, 0x97, 0x82, 0x85, 0x62, 0x28, 0x66, 0x81, 0x88, 0x97, 0x80, 0x87, 0x82, 0x84, 0x83, 0x96, 0x91, 0x98, 0x97, 0x80, 0x87, 0x82, 0x85, 0x84, 0x83, 0x96, 0x91, 0x98, 0x97, 0x80, 0x87, 0x84, 0x83, 0x96, 0x81, 0x88, 0x87, 0x82, 0x85, 0x80, 0x84, 0x83, 0x96, 0x81, 0x88, 0x87, 0x82, 0x85, 0x84, 0x83, 0x96, 0x81, 0x88, 0x87, 0x81, 0x82, 0x85, 0x86, 0x88, 0x96, 0x81, 0x88, 0x87, 0x81, 0x82, 0x85, 0x80, 0x87, 0x86, 0x81, 0x88, 0x87, 0x80, 0x87, 0x80, 0x87, 0x80, 0x87, 0x86, 0x81, 0x88, 0x87, 0x80, 0x87, 0x80, 0x87, 0x80, 0x83, 0x96, 0x81, 0x88, 0x87, 0x80, 0x87, 0x80, 0x87, 0x80, 0x83, 0x96, 0x81, 0x88, 0x88, 0x82, 0x85, 0x80, 0x88, 0x96, 0x81, 0x88, 0x88, 0x80, 0x80, 0x80, 0x80, 0x83, 0x98, 0x81, 0x88, 0x80, 0x80, 0x80, 0x80, 0x80, 0x83, 0x98,</pre>	0xD8, 0xDF, 0xD6, 0xD1, 0xC4, 0xC3, 0xCA, 0xCD,
<pre>0xA8, 0xAF, 0xA6, 0xA1, 0xB4, 0xB3, 0xBA, 0xBD, 0xC7, 0xC0, 0xCC, 0xCC, 0xDB, 0xDC, 0xD5, 0xD2, 0xFF, 0xB0, 0xB1, 0xB6, 0xB3, 0xA4, 0xAD, 0xA2, 0xB7, 0xB0, 0xB1, 0xB6, 0xB3, 0xA4, 0xAD, 0xA3, 0x27, 0xD0, 0x29, 0x28, 0xB3, 0xA4, 0xAD, 0xA3, 0x1F, 0x18, 0x11, 0x16, 0x03, 0x04, 0xDD, 0xA4, 0x57, 0x56, 0x51, 0x56, 0x73, 0x74, 0x7D, 0x7A, 0x67, 0x56, 0x51, 0x56, 0x73, 0x74, 0x7D, 0x7A, 0x89, 0x88, 0x87, 0x80, 0x55, 0x92, 0x88, 0x2C, 0x81, 0x66, 0x77, 0x50, 0x55, 0x48, 0xA2, 0xA4, 0x67, 0x56, 0x57, 0x50, 0x50, 0xA0, 0xA4, 0xA3, 0xA4, 0xF9, 0xFE, 0xF7, 0xF0, 0xF5, 0xF2, 0xFB, 0xC2, 0xC1, 0xC6, 0xC7, 0x60, 0x75, 0x72, 0x7B, 0x72, 0x51, 0x56, 0x57, 0x50, 0x73, 0x74, 0x73, 0x74, 0x69, 0x55, 0x57, 0x50, 0x50, 0xA0, 0xA3, 0xA4, 0xF9, 0xFF, 0xF7, 0xF0, 0x55, 0x52, 0x55, 0x55, 0x54, 0x60, 0x55, 0x57, 0x50, 0x73, 0x74, 0x74, 0x61, 0x16, 0x77, 0x10, 0x05, 0x02, 0x08, 0x02, 0x21, 0x26, 0x74, 0x47, 0x52, 0x28, 0x20, 0x04, 0x42, 0x49, 0x40, 0x47, 0x52, 0x25, 0x55, 0x56, 0x54, 0x76, 0x11, 0x70, 0x77, 0x12, 0x25, 0x26, 0x26, 0x38, 0x39, 0x30, 0x37, 0x22, 0x25, 0x2C, 0x28, 0x96, 0x10, 0x00, 0x07, 0x14, 0x14, 0x14, 0x48, 0x49, 0x40, 0x47, 0x82, 0x84, 0x84, 0x83, 0x96, 0x10, 0x00, 0x07, 0x22, 0x25, 0x2C, 0x28, 0x26, 0x19, 0x00, 0x07, 0x22, 0x25, 0x2C, 0x28, 0x26, 0x10, 0x00, 0x07, 0x22, 0x28, 0x83, 0x26, 0x10, 0x00, 0x07, 0x28, 0x84, 0x83, 0x26, 0x10, 0x00, 0x07, 0x28, 0x84, 0x83, 0x26, 0x10, 0</pre>	0x90, 0x97, 0x9E, 0x99, 0x8C, 0x8B, 0x82, 0x85,
<pre>bxC7, 0xC0, 0xC9, 0xCF, 0xDE, 0xDE, 0xDD, 0xEA, 0xEF, 0xF8, 0xF1, 0xF6, 0xEB, 0xEA, 0xED, 0xEA, 0xEF, 0xF8, 0xE1, 0xF8, 0xAE, 0xAE, 0xED, 0xEA, 0xEF, 0xF8, 0xE1, 0xF8, 0xAE, 0xAE, 0xDD, 0xOA, 0x27, 0x20, 0x29, 0x2E, 0xH8, 0x4C, 0x45, 0x42, 0xF7, 0xF8, 0xF1, 0xF6, 0x73, 0x74, 0xDD, 0xOA, 0xF7, 0xF5, 0xF7, 0xF0, 0xF5, 0xE2, 0xFB, 0xCC, 0xEB, 0xFF, 0xF7, 0xF0, 0xF5, 0xE2, 0xFB, 0xCC, 0xEB, 0xFF, 0xF7, 0xF0, 0xF5, 0xE2, 0xFB, 0xCC, 0xC1, 0xC6, 0xCF, 0xC8, 0xDD, 0xAA, 0xA3, 0xA4, 0xF7, 0xF5, 0xF7, 0xF0, 0xF5, 0xE2, 0xFB, 0xCC, 0xC1, 0xC6, 0xCF, 0xF8, 0xF5, 0xE2, 0xFB, 0xCC, 0xC1, 0xC6, 0xF7, 0xF1, 0xF5, 0xE2, 0xFB, 0xCC, 0xC1, 0xC6, 0xF7, 0xF1, 0xF5, 0xE2, 0xFB, 0xCC, 0xC1, 0xC6, 0xF7, 0xF1, 0xF5, 0xE2, 0xFB, 0xCC, 0xC1, 0xF8, 0xFF, 0xF1, 0xF3, 0xF3, 0xA4, 0xA3, 0xA4, 0x49, 0xA4, 0x47, 0xF2, 0xF5, 0xCC, 0xFB, 0xF6, 0xF1, 0xF8, 0xFF, 0xFA, 0xFD, 0xF4, 0xF3, 0xF5, 0xF1, 0xF8, 0xFF, 0xFA, 0xFD, 0xF4, 0xF3, 0xA5, 0xA9, 0xA0, 0xA7, 0xE2, 0xC5, 0xCC, 0xCE, 0xFB, 0xA9, 0xA0, 0xA7, 0xE2, 0xFB, 0xF4, 0xF3, 0xFB, 0xF1, 0xF8, 0xFF, 0xFA, 0xFD, 0xF4, 0xF3, 0xFB, 0xF1, 0xF8, 0xFF, 0xFA, 0xFD, 0xF4, 0xF3, 0xFE, 0xF1, 0xF8, 0xFF, 0xFA, 0xFF, 0xFA, 0xF3, 0xFF, 0xF1, 0xF8, 0xFF, 0xFA, 0xFF, 0xFA, 0xF3, 0xFF, 0xF1, 0xF8, 0xFF, 0xFA, 0xFF, 0xFA, 0xF3, 0xFF, 0xF1, 0xF8, 0xFF, 0xFA, 0xFF, 0xFA, 0xFF, 0xFA, 0xFF, 0xFF, 0xF1, 0xF8, 0xFF, 0xFA, 0xFF, 0xFA, 0xFF, 0xFA, 0xFF, 0xFF, 0xF1, 0xFF, 0xFA, 0xFF, 0xFF, 0xFA, 0xFF, 0xFA, 0xFF, 0xFF, 0xFF, 0xFF, 0xFF, 0</pre>	0xA8, 0xAF, 0xA6, 0xA1, 0xB4, 0xB3, 0xBA, 0xBD,
<pre>0xFF, 0xFB, 0xF1, 0xF6, 0xF3, 0xF4, 0xF0, 0xF4, 0xF5, 0xF4, 0xF7, 0xF0, 0xF3, 0xF6, 0xF3, 0xF4, 0xF0, 0xF5, 0xF4, 0xF7, 0xF0, 0xF3, 0xF1, 0xF6, 0xF3, 0xF4, 0xF0, 0xF5, 0xF3, 0xF7, 0xF0, 0xF5, 0xF5, 0xF5, 0xF4, 0xF4, 0xF0, 0xF4, 0xF7, 0xF0, 0xF7, 0xF0, 0xF5, 0xF2, 0xF4, 0xF0, 0xF4, 0xF7, 0xF6, 0xF7, 0xF0, 0xF5, 0xF2, 0xF1, 0xF4, 0xF9, 0xF5, 0xF7, 0xF0, 0xF5, 0xF2, 0xF1, 0x72, 0xF1, 0xF6, 0xF7, 0xF0, 0xF5, 0xF2, 0xF1, 0x74, 0xF9, 0xF5, 0xF7, 0xF0, 0xF5, 0xF2, 0xF1, 0x74, 0xF3, 0xF6, 0xF7, 0xF0, 0xF5, 0xF2, 0xF1, 0x74, 0xF3, 0xF6, 0xF7, 0xF0, 0xF5, 0xF2, 0xF1, 0x74, 0xF3, 0xF4, 0xF1, 0xF1, 0xF1, 0xF3, 0xF4, 0xF3, 0xF5, 0xF1, 0x77, 0xF1, 0xF0, 0xF5, 0xF5, 0xF5, 0x75, 0xF1, 0x71, 0x77, 0xF2, 0xF8, 0xF0, 0xF4, 0xF3, 0xF9, 0xF0, 0xF1, 0xF1, 0xF1, 0xF3, 0xF5, 0xF1, 0xF3, 0xF7, 0xF2, 0xF5, 0xF5, 0xF5, 0xF5, 0xF1, 0xF3, 0xF7, 0xF2, 0xF5, 0xF5, 0xF5, 0xF5, 0xF1, 0xF3, 0xF7, 0xF2, 0xF5, 0xF5, 0xF5, 0xF5, 0xF1, 0xF3, 0xF7, 0xF3, 0xF3, 0xF4, 0xF3, 0xF5, 0xF1, 0xF4, 0xF3, 0xF7, 0xF4, 0xF3, 0xF5, 0xF1, 0xF4, 0xF3, 0xF7, 0xF4, 0xF3, 0xF5, 0xF1, 0xF4, 0xF7, 0xF7, 0xF7, 0xF7, 0xF4, 0xF3, 0xF5, 0xF1, 0xF8, 0xFF, 0xFA, 0xF0, 0xF4, 0xF3, 0xF5, 0xF1, 0xF8, 0xFF, 0xFA, 0xF0, 0xF4, 0xF3, 0xF5, 0xF1, 0xF8, 0xFF, 0xFA, 0xF7, 0xF4, 0xF3, 0xF5, 0xF1, 0xF8, 0xFF, 0xF4, 0xF3, 0xF5, 0xF1, 0xF8, 0xFF, 0xF4, 0xF3, 0xF5, 0xF1, 0xF8, 0xFF, 0xF4, 0xF3, 0xF5, 0xF1, 0xF5, 0xF4, 0xF3, 0xF5, 0xF4, 0xF3, 0xF5, 0xF1, 0xF5, 0xF5, 0xF4, 0xF5, 0xF4, 0xF3, 0xF5, 0xF1, 0xF5, 0xF5, 0xF4, 0xF5, 0xF4, 0xF3, 0xF5, 0xF1, 0xF5, 0xF5, 0xF4, 0xF5, 0xF4, 0xF3, 0xF5, 0xF1, 0xF5, 0xF4, 0xF5, 0</pre>	0xC7, 0xC0, 0xC9, 0xCE, 0xDB, 0xDC, 0xD5, 0xD2,
<pre>0xB7, 0xB8, 0xB8, 0xB8, 0xAB, 0xAB, 0xAB, 0xAB, 0xAB, 0xB7, 0xB8, 0xB8, 0xB1, 0xB2, 0xB3, 0x3C, 0xB3, 0xCAA, 0xC7, 0x20, 0x29, 0x2E, 0xBB, 0xAC, 0xAD, 0xAD, 0xAA, 0xS7, 0xS0, 0xS5, 0xSE, 0xAB, 0xAC, 0xAD, 0xAA, 0xAD, 0xAA, 0xS7, 0xS0, 0xSE, 0xB7, 0xB0, 0xB5, 0xE2, 0xBB, 0xPC, 0xB1, 0xBE, 0xBF, 0xB8, 0xAD, 0xAA, 0xA3, 0xA4, 0xF9, 0xFE, 0xF7, 0xF0, 0xE5, 0xE2, 0xBB, 0xPC, 0xC1, 0xC6, 0xCF, 0xC3, 0xAD, 0xAA, 0xA3, 0xA4, 0xF9, 0xFE, 0xF7, 0xF0, 0xE5, 0xF2, 0xFB, 0xPC, 0xC1, 0xC6, 0xCF, 0xC3, 0xAD, 0xAA, 0xA3, 0xA4, 0xF9, 0xFE, 0xF7, 0xF0, 0xE5, 0xF2, 0xFB, 0xPC, 0xC1, 0xC6, 0xCF, 0xC9, 0xDD, 0xAA, 0xA3, 0xA4, 0xF9, 0xFE, 0xF7, 0xF0, 0xF5, 0x72, 0xFB, 0xPC, 0xC1, 0xC6, 0xCF, 0xC9, 0xAD, 0xAA, 0xA3, 0xA4, 0xF1, 0xF5, 0xF7, 0xF0, 0xF5, 0xF2, 0xFB, 0xPC, 0xC1, 0xC6, 0xCF, 0x73, 0x77, 0xF2, 0xFB, 0xPC, 0xC1, 0xC6, 0xCF, 0x73, 0x77, 0xF2, 0xF5, 0xCC, 0xCB, 0xF6, 0x71, 0x78, 0xFF, 0xFA, 0xFD, 0xF4, 0xF3, 0xA6, 0xA9, 0xA0, 0xA7, 0xF2, 0xF5, 0xCC, 0xCB, 0xF6, 0x11, 0x08, 0xFF, 0xFA, 0xFD, 0xF4, 0xF3, 0xF6, 0x11, 0x08, 0xFF, 0xFA, 0xFD, 0xF4, 0xF3}; /// <summarys /// calculates an 8 bit CRC // <summarys public byte Pec(byte[] buffer) { temp = orc ^ buffer.Length; I++) temp = crc ^ buffer[i]; crc = 0; int temp = 0; for (int I = 0; < buffer.Length; I++) { temp = crc ^ buffer[i]; crc = 0; int temp = 0; for (int I = 0; < buffer.Length; I++) { temp = crc ^ buffer[i]; crc = 0; int temp = 0; for (int I = 0; < buffer.Length; I++) { temp = crc ^ buffer[i]; crc = 0; int temp = 0; for (int I = 0; < buffer.Length; I++) {</summarys </summarys </pre>	0xFF, 0xF8, 0xF1, 0xF6, 0xE3, 0xE4, 0xED, 0xEA,
<pre>D288*, 0x88, 0x81, 0x86, 0x95, 0x94, 0x94, 0x94, 0x32, 0x1F, 0x18, 0x11, 0x16, 0x03, 0x04, 0x00, 0x04, 0x57, 0x50, 0x55, 0x55, 0x48, 0x44, 0x42, 0x67, 0x68, 0x61, 0x66, 0x73, 0x74, 0x7D, 0x7A, 0x89, 0x8E, 0x87, 0x80, 0x85, 0x22, 0x98, 0x92C, 0x81, 0x86, 0x8F, 0x80, 0x80, 0x40, 0xA3, 0xA4, 0x79, 0x7E, 0x77, 0x70, 0x75, 0x72, 0x7B, 0x7C, 0x61, 0x66, 0x67, 0x60, 0x75, 0x72, 0x7B, 0x7C, 0x51, 0x56, 0x57, 0x50, 0x75, 0x72, 0x7B, 0x7C, 0x51, 0x56, 0x57, 0x50, 0x75, 0x72, 0x7B, 0x7C, 0x51, 0x56, 0x78, 0x7F, 0x80, 0xA3, 0x34, 0x34, 0x19, 0x1E, 0x17, 0x10, 0x05, 0x22, 0x58, 0x2C, 0x21, 0x26, 0x78, 0x7F, 0x80, 0x40, 0x44, 0x43, 0x44, 0x19, 0x1E, 0x17, 0x10, 0x05, 0x22, 0x55, 0x52, 0x58, 0x76, 0x71, 0x78, 0x7F, 0x7A, 0x50, 0x44, 0x63, 0x38, 0x39, 0x30, 0x37, 0x22, 0x25, 0x2C, 0x28, 0x06, 0x10, 0x06, 0x7F, 0x8A, 0x60, 0x44, 0x63, 0x38, 0x39, 0x30, 0x37, 0x22, 0x25, 0x2C, 0x28, 0x66, 0x91, 0x98, 0xFF, 0x8A, 0x6D, 0x44, 0x83, 0x26, 0x91, 0x98, 0xFF, 0x8A, 0x6D, 0xF4, 0x73}; /// <summarys /// <summarys public byte Pec(byte[] buffer) { for (int I = 0! < buffer.Length; I++) { temp = crc ^ buffer[i]; crc = CrcTable[temp]; } return s>A one byte CRC</summarys </summarys </pre>	0xB7, 0xB0, 0xB9, 0xBE, 0xAB, 0xAC, 0xA5, 0xA2,
<pre>0122, 0120, 0122, 01245, 0125, 0125, 0124, 0200, 0200, 0127, 0126, 0126, 011, 0126, 0203, 0026, 0207, 0200, 0127, 0126, 0126, 0126, 0126, 0127, 0127, 0128, 0128, 0128, 0128, 0128, 0120, 0127, 0128, 0128, 0128, 0128, 0128, 0120, 0127, 0128, 0128, 0128, 0128, 0128, 0120, 0128, 0127, 0128, 0128, 0128, 0128, 0128, 0128, 0127, 0128, 0</pre>	0x8F, 0x88, 0x81, 0x86, 0x93, 0x94, 0x9D, 0x9A,
<pre>Doff, 0x10, 0</pre>	
<pre>0.057, 0x68, 0x61, 0x66, 0x73, 0x74, 0x70, 0x7A, 0x89, 0x88, 0x87, 0x80, 0x95, 0x92, 0x9B, 0x9C, 0x81, 0x86, 0x87, 0x80, 0x95, 0x22, 0x8B, 0x84, 0x79, 0xFE, 0xF7, 0xF0, 0xE5, 0xE2, 0xEE, 0xEC, 0xC1, 0xC6, 0xCF, 0xC8, 0xD0, 0xD3, 0xD3, 0xD4, 0x69, 0x68, 0x67, 0x60, 0x75, 0x72, 0x7B, 0x7C, 0x51, 0x56, 0x5F, 0x58, 0x4D, 0xA4, 0x43, 0x44, 0x19, 0x18, 0x17, 0x10, 0x05, 0x12, 0x08, 0x02, 0x21, 0x26, 0x2F, 0x28, 0x3D, 0x3A, 0x33, 0x34, 0x64, 0x40, 0x47, 0x52, 0x55, 0x5C, 0x5B, 0x76, 0x71, 0x78, 0x7F, 0x6A, 0x6D, 0x64, 0x63, 0x32, 0x30, 0x37, 0x22, 0x25, 0x2C, 0x2B, 0x66, 0x01, 0x08, 0x0F, 0x1A, 0x1D, 0x14, 0x13, 0xAE, 0x49, 0xA0, 0xA7, 0xE2, 0x25, 0xCC, 0x2B, 0x66, 0x91, 0x98, 0x9F, 0x8A, 0x4D, 0x84, 0x83, 0xDE, 0xD9, 0xA0, 0xA7, 0xE2, 0xC5, 0xCC, 0xCB, 0xE6, 0xE1, 0x88, 0xEF, 0xFA, 0xFD, 0xF4, 0xF3}; /// calculates an 8 bit CRC /// <summarys /// calculates an 8 bit CRC /// <summarys /// calculates an 8 bit CRC /// <summarys /// cruturnavA one byte CRC public byte Pec(byte[] buffer) { termp = crc ^buffer!i]; crc = CrcTable[temp]; } terurn crc; } } // "namespace", "class", and associated braces may be removed for C code implementations // Copyright @ Texas Instruments. All rights reserved. // THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY /// CP ANY KIND, EITHER EXPRESSED OR IMPLED, INCLUDING BUT NOT // LIMITED TO THE IMPLED WARRANTIES CO MERCANTABLILITY AND</summarys </summarys </summarys </pre>	0x57 0x50 0x59 0x56 0x58 0x46 0x46 0x42
<pre>0x89, 0x8E, 0x87, 0x80, 0x95, 0x92, 0x9E, 0x9C, 0x81, 0xBE, 0x8F, 0x88, 0xAD, 0xAA, 0xA3, 0xA4, 0xF9, 0xFE, 0xF7, 0xF0, 0xE5, 0xE2, 0xEE, 0xEC, 0xC1, 0xCC, 0xCF, 0xC8, 0xED, 0xDA, 0xD3, 0xD4, 0x59, 0x6E, 0x67, 0x60, 0xF5, 0x72, 0x7E, 0xC2, 0x51, 0x56, 0xFF, 0x58, 0x4D, 0xAA, 0x43, 0x44, 0x19, 0x1E, 0x17, 0x10, 0x05, 0x02, 0x0E, 0xCC, 0x21, 0x26, 0x2F, 0x28, 0x3D, 0x33, 0x34, 0x4E, 0x49, 0x40, 0x47, 0x52, 0x55, 0x5C, 0x5B, 0x76, 0x71, 0x78, 0x7F, 0x7F, 0xAA, 0x43, 0x44, 0x13, 0x4E, 0x49, 0x40, 0x47, 0x82, 0x55, 0x5C, 0x2B, 0x06, 0x01, 0x08, 0x0F, 0x1A, 0x1D, 0x14, 0x13, 0xAE, 0xA9, 0xA0, 0xA7, 0x82, 0x85, 0x8C, 0x8B, 0x66, 0x21, 0x98, 0x9F, 0x8A, 0x8D, 0x44, 0x83, 0x06, 0x01, 0x08, 0x9F, 0xFA, 0xFD, 0xF4, 0xF3; 0x66, 0xe1, 0x98, 0x0F, 0xFA, 0xFD, 0xF4, 0xF3; /// <summarys /// <pre>crc % buffer.Length; I++) { temp = crc % buffer.Length; I++) temp = cr; byte crc = 0; int temp = 0; for (int I = 0; < buffer.Length; I++) { temp = crc % buffer[i]; crc = CrcTable[temp]; } // 'namespace", "class", and associated braces may be removed for C code implementations // Copyright @ Texas Instruments. All rights reserved. // THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY // OF ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT // LIMITED TO THE IMPLIED WARRANTIES COF MERCHANTABLLITY AND</pre></summarys </pre>	0x57, 0x58, 0x51, 0x56, 0x73, 0x74, 0x70, 0x73.
<pre>0xBl, 0xBe, 0xBe, 0xBe, 0xAD, 0xAA, 0xA3, 0xA4, 0xF9, 0xFE, 0xF7, 0xF0, 0xE5, 0xE2, 0xEB, 0xEC, 0xCl, 0xCe, 0xCF, 0xCB, 0xDD, 0xAA, 0xA3, 0xA4, 0x59, 0x6E, 0x67, 0x68, 0xDD, 0xAA, 0xA3, 0xA4, 0x19, 0x1E, 0x17, 0x10, 0x05, 0x02, 0x0B, 0x0C, 0x21, 0x26, 0x2F, 0x28, 0x3D, 0xA3, 0x33, 0x34, 0x4E, 0x49, 0x40, 0x47, 0x52, 0x55, 0x5C, 0x5B, 0x76, 0x71, 0x78, 0x7F, 0x6A, 0x45, 0x45, 0x63, 0x3E, 0x39, 0x30, 0x37, 0x22, 0x25, 0x2C, 0x2B, 0x06, 0x01, 0x08, 0x07, 0x1A, 0x1D, 0x14, 0x13, 0xAE, 0xA9, 0xA0, 0xA7, 0x82, 0x55, 0xEC, 0xEB, 0x66, 0x81, 0x88, 0x9F, 0x8A, 0x8D, 0x84, 0x83, 0xDE, 0xD9, 0xD0, 0xD7, 0xC2, 0xC5, 0xCC, 0xCB, 0x86, 0xE1, 0x88, 0xFF, 0xFA, 0xFD, 0xF4, 0xF3}; /// <summary> /// calculates an 8 bit CRC // <summary> /// calculates an 8 bit CRC // <summary> /// verturns>A one byte CRC/returns> public byte Pec(byte[] buffer) { temp = crc ^ buffer.Length; I++) { temp = crc ^ buffer[i]; crc = CrcTable[temp]; } // "namespace", "class", and associated braces may be removed for C code implementations // Copyright @ Texas Instruments. All rights reserved. // THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRAMTY /// Gr ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT // LIMITED TO THE INPLIED WARRAMTIES OF MERCHANTABLLITY AND</summary></summary></summary></pre>	0x89, 0x8E, 0x87, 0x80, 0x95, 0x92, 0x9B, 0x9C,
<pre>0xF9, 0xFE, 0xF7, 0xF0, 0xE5, 0xE2, 0xEB, 0xEC, 0xC1, 0xC6, 0xCF, 0xC8, 0xD0, 0xD3, 0xD3, 0xD4, 0xt9, 0xE6, 0x67, 0x60, 0x75, 0x72, 0x7B, 0x77, 0x51, 0x56, 0x5F, 0x58, 0x40, 0x4A, 0x43, 0x44, 0x19, 0x1E, 0x17, 0x10, 0x05, 0x02, 0x0B, 0x0C, 0x21, 0x26, 0x2F, 0x28, 0x3D, 0x3A, 0x33, 0x34, 0x4E, 0x49, 0x40, 0x47, 0x52, 0x55, 0x5C, 0x5E, 0x76, 0x71, 0x78, 0x7F, 0x6A, 0x6D, 0x64, 0x63, 0x1E, 0x39, 0x30, 0x37, 0x22, 0x25, 0x2C, 0x2B, 0x06, 0x01, 0x08, 0x0F, 0x1A, 0x1D, 0x14, 0x13, 0xAE, 0xA9, 0xA0, 0xA7, 0xE2, 0x55, 0xCC, 0xEB, 0x96, 0x91, 0x98, 0x9F, 0x8A, 0x8D, 0x64, 0x83, 0x0E, 0x19, 0xD0, 0xD7, 0xC2, 0x25, 0xCC, 0xCB, 0x56, 0xE1, 0xE8, 0xEF, 0xFA, 0xFD, 0xF4, 0xF3; /// <summary> /// <summar< td=""><td>0xB1, 0xB6, 0xBF, 0xB8, 0xAD, 0xAA, 0xA3, 0xA4,</td></summar<></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></pre>	0xB1, 0xB6, 0xBF, 0xB8, 0xAD, 0xAA, 0xA3, 0xA4,
<pre>0xCl, 0xC6, 0xCF, 0xC8, 0xDD, 0xDA, 0xD3, 0xD4, 0x59, 0x6E, 0x67, 0x60, 0x75, 0x72, 0x7B, 0x7C, 0x51, 0x56, 0x5F, 0x58, 0x4D, 0x4A, 0x43, 0x44, 0x19, 0x1E, 0x17, 0x10, 0x05, 0x02, 0x0B, 0x0C, 0x21, 0x26, 0x2F, 0x28, 0x3D, 0x33, 0x34, 0x4E, 0x49, 0x40, 0x47, 0x52, 0x55, 0x5C, 0x5B, 0x76, 0x71, 0x78, 0x7F, 0x6A, 0x6D, 0x64, 0x63, 0x3E, 0x39, 0x30, 0x37, 0x22, 0x25, 0x2C, 0x2B, 0x06, 0x01, 0x08, 0x0F, 0x1A, 0x1D, 0x14, 0x13, 0xAE, 0xA9, 0xA0, 0xA7, 0x85, 0xBC, 0xBB, 0x96, 0x91, 0x98, 0x9F, 0x8A, 0x8D, 0x84, 0x83, 0xDE, 0xD9, 0xD0, 0xD7, 0xC2, 0xC5, 0xCC, 0xCB, 0x86, 0x81, 0xE8, 0xEF, 0xFA, 0xFD, 0xF4, 0xF3}; /// calculates an 8 bit CRC /// summary> /// calculates an 8 bit CRC /// summary> /// creturns>A one byte CRC public byte Pec(byte[] buffer) { byte orc = 0; int temp = 0; for (int I = 0; < buffer.Length; I++) { temp = crc ^ buffer[i]; crc = CrcTable[temp]; } return crc; } } // 'namespace", "class", and associated braces may be removed for C code implementations // Copyright © Texas Instruments. All rights reserved. // THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRAMTY // OF ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT // LIMTED TO THE IMPLIED WARAMTIES OF MERCHANTABLIJITY AND</pre>	$0 \times F9$, $0 \times FE$, $0 \times F7$, $0 \times F0$, $0 \times E5$, $0 \times E2$, $0 \times EB$, $0 \times EC$,
<pre>0x69, 0x6E, 0x67, 0x60, 0x75, 0x72, 0x7B, 0x7C, 0x51, 0x56, 0x5F, 0x48, 0x43, 0x44, 0x19, 0x1E, 0x17, 0x10, 0x05, 0x02, 0x0B, 0x0C, 0x21, 0x26, 0x2F, 0x28, 0x3D, 0x3A, 0x33, 0x34, 0x4E, 0x49, 0x40, 0x47, 0x52, 0x55, 0x5C, 0x5B, 0x76, 0x71, 0x78, 0x7F, 0x6A, 0x6D, 0x64, 0x63, 0x38, 0x39, 0x30, 0x37, 0x22, 0x25, 0x2C, 0x2B, 0x06, 0x01, 0x08, 0x0F, 0x1A, 0x1D, 0x14, 0x13, 0xAE, 0x89, 0xA0, 0xA7, 0x82, 0x85, 0x8C, 0x8B, 0x056, 0x99, 0x00, 0x77, 0x22, 0x25, 0x2C, 0x2B, 0x26, 0x99, 0x00, 0x77, 0x22, 0x25, 0xCC, 0x2B, 0xE6, 0xe1, 0x88, 0x9F, 0x8A, 0x8D, 0x84, 0x83, 0xDE, 0x19, 0x00, 0x77, 0x22, 0x25, 0xCC, 0x2B, 0xE6, 0xe1, 0x88, 0xFF, 0xFA, 0xFD, 0xF4, 0xF3}; /// <summary> /// <summary> /// <summary> /// <summary> /// <summary> public byte Pec(byte[] buffer) { temp = crc ^ buffer.Length; I++) { temp = crc ^ buffer.Length; I++) { temp = crc ^ buffer[i]; crc = CrcTable[temp]; } return crc; } } } // "namespace", "class", and associated braces may be removed for C code implementations // Copyright © Texas Instruments. All rights reserved. // THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY // OF ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT // LIMTED TO THE IMPLIED WARRANTIES OF MERCHANTABLILITY AND</summary></summary></summary></summary></summary></pre>	0xC1, 0xC6, 0xCF, 0xC8, 0xDD, 0xDA, 0xD3, 0xD4,
<pre>0x51, 0x56, 0x5F, 0x58, 0x4D, 0x4A, 0x43, 0x44, 0x19, 0x1E, 0x17, 0x10, 0x05, 0x02, 0x0B, 0x0C, 0x21, 0x26, 0x2F, 0x28, 0x3D, 0x3A, 0x33, 0x34, 0x4E, 0x49, 0x40, 0x47, 0x52, 0x55, 0x5C, 0x5B, 0x76, 0x71, 0x78, 0x7F, 0x5A, 0x5D, 0x64, 0x63, 0x3B, 0x39, 0x30, 0x37, 0x22, 0x25, 0x2C, 0x2B, 0x06, 0x01, 0x08, 0x0F, 0x1A, 0x1D, 0x14, 0x13, 0xAE, 0x49, 0x40, 0xA7, 0x82, 0x85, 0x8C, 0x8B, 0x96, 0x91, 0x98, 0x9F, 0x8A, 0x8D, 0x84, 0x83, 0x06, 0x01, 0x08, 0x0F, 0x1A, 0x1D, 0x74, 0x73; 0x66, 0xe1, 0x88, 0x9F, 0x8A, 0x8D, 0x84, 0x83, 0x06, 0xe1, 0x88, 0x9F, 0x8A, 0x8D, 0x84, 0x83, 0x66, 0xe1, 0x88, 0xFF, 0xFA, 0xFD, 0xF4, 0xF3; /// <summary> /// <cutumary> /// <cutumary> // <cutum< td=""><td>0x69, 0x6E, 0x67, 0x60, 0x75, 0x72, 0x7B, 0x7C,</td></cutum<></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></cutumary></summary></pre>	0x69, 0x6E, 0x67, 0x60, 0x75, 0x72, 0x7B, 0x7C,
<pre>0x19, 0x11, 0x11, 0x11, 0x05, 0x02, 0x0B, 0x0C, 0x21, 0x26, 0x2F, 0x28, 0x30, 0x33, 0x34, 0x64, 0x4E, 0x49, 0x40, 0x47, 0x52, 0x55, 0x5C, 0x5B, 0x76, 0x71, 0x78, 0x7F, 0x6A, 0x6D, 0x64, 0x63, 0x35, 0x39, 0x30, 0x37, 0x22, 0x22, 0x22, 0x2B, 0x06, 0x01, 0x08, 0x0F, 0x1A, 0x1D, 0x14, 0x13, 0xAE, 0xA9, 0xA0, 0xA7, 0xB2, 0xB5, 0xEC, 0xBB, 0x96, 0x91, 0x98, 0x9F, 0x8A, 0x8D, 0x84, 0x83, 0xDE, 0xD9, 0xD0, 0xD7, 0xC2, 0xC5, 0xCC, 0xCB, 0xE6, 0xE1, 0xE8, 0xEF, 0xFA, 0xFD, 0xF4, 0xF3}; /// <summarys /// <summarys /// <summarys /// <summarys /// <cuculates 8="" an="" bit="" crc<br="">/// <summarys public byte Pec(byte[] buffer) { byte crc = 0; int temp = 0; for (int I = 0; < buffer.Length; I++) { temp = crc ^ buffer[i]; crc = CrcTable[temp]; } return crc; } } // ' mamespace", "class", and associated braces may be removed for C code implementations // Copyright © Texas Instruments. All rights reserved. // THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARANTY // OF ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT // LIMITED TO THE IMPLIED WARRANTIES OF MERCHANTABLILITY AND</summarys </cuculates></summarys </summarys </summarys </summarys </pre>	0x51, 0x56, 0x5F, 0x58, 0x4D, 0x4A, 0x43, 0x44,
<pre>0x21, 0x20, 0x27, 0x28, 0x30, 0x34, 0x33, 0x34, 0x48, 0x49, 0x40, 0x47, 0x52, 0x55, 0x56, 0x58, 0x76, 0x71, 0x78, 0x77, 0x27, 0x22, 0x22, 0x22, 0x38, 0x39, 0x30, 0x37, 0x22, 0x25, 0x2C, 0x28, 0x38, 0x39, 0x30, 0x37, 0x22, 0x85, 0x80, 0x84, 0x33, 0x48, 0x49, 0x40, 0x47, 0x82, 0x85, 0x80, 0x84, 0x83, 0x96, 0x91, 0x98, 0x97, 0x82, 0x85, 0x80, 0x74, 0x73, 0x06, 0x01, 0x98, 0x97, 0x82, 0x85, 0x80, 0x74, 0x73, 0x86, 0xe1, 0xe8, 0xeF, 0xFA, 0xFD, 0xF4, 0xF3}; /// <summary> /// <sumary> /// <summary> ///</summary></sumary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></summary></pre>	0x19, $0x1E$, $0x17$, $0x10$, $0x05$, $0x02$, $0x0B$, $0x0C$,
<pre>Overage (0xeg), 0xeg, 0xe</pre>	UX21, UX26, UX27, UX28, UX3D, UX3A, UX33, UX34,
<pre>0x3E, 0x39, 0x30, 0x37, 0x22, 0x22, 0x22, 0x2E, 0x06, 0x01, 0x08, 0x0F, 0x1A, 0x1D, 0x14, 0x13, 0xAE, 0xA9, 0xA0, 0xA7, 0xE2, 0xE5, 0xEC, 0xBB, 0x96, 0x91, 0x98, 0x9F, 0x8A, 0x8D, 0x84, 0x83, 0xDE, 0xD9, 0xD0, 0xD7, 0xC2, 0xC5, 0xCC, 0xCB, 0xE6, 0xE1, 0xE8, 0xEF, 0xFA, 0xFD, 0xF4, 0xF3}; /// <summary> /// <alculates 8="" an="" bit="" crc<br="">/// <summary> /// <clculates 8="" an="" bit="" crc<br="">// <clculate< td=""><td>0475, 0479, 0470, 0477, 0492, 0495, 0455, 0</td></clculate<></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></clculates></summary></clculates></summary></clculates></summary></clculates></summary></clculates></summary></clculates></summary></clculates></summary></alculates></summary></pre>	0475, 0479, 0470, 0477, 0492, 0495, 0455, 0
<pre>Dx06, 0x11, 0x08, 0x0F, 0x1A, 0x1D, 0x14, 0x13, 0xAE, 0xA9, 0xA0, 0xA7, 0xE2, 0xB5, 0xBC, 0xBB, 0x96, 0x91, 0x98, 0x9F, 0x8A, 0x8D, 0x84, 0x83, 0xDE, 0xD9, 0xD0, 0xD7, 0xC2, 0xC5, 0xCC, 0xCB, 0xE6, 0xE1, 0xE8, 0xEF, 0xFA, 0xFD, 0xF4, 0xF3}; /// <summary> /// calculates an 8 bit CRC /// summary> /// calculates an 8 bit CRC /// summary> public byte Pec(byte[] buffer) { byte crc = 0; int temp = 0; for (int I = 0; < buffer.Length; I++) { temp = crc ^ buffer[i]; crc = CrcTable[temp]; } return crc; } } // "namespace", "class", and associated braces may be removed for C code implementations // Copyright © Texas Instruments. All rights reserved. // THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY // OF ANY KIND, EITHER EXPRESSED OR IMPLIED, INCUDING BUT NOT // LIMITED TO THE IMPLIED WARRANTIES OF MERCHANTABLILTY AND</summary></pre>	x_{35} , x_{39} , x_{30} , x_{37} , x_{22} , x_{22} , x_{22} , x_{22} , x_{23}
<pre>DXAE, 0xA9, 0xA0, 0xA7, 0xB2, 0xB5, 0xBC, 0xBB, 0x96, 0x91, 0x98, 0x9F, 0xBA, 0x8A, 0x84, 0x83, 0xDE, 0xD9, 0xD0, 0xD7, 0xC2, 0xC5, 0xCC, 0xCB, 0xE6, 0xE1, 0xE8, 0xEF, 0xFA, 0xFD, 0xF4, 0xF3}; /// <summary> /// Calculates an 8 bit CRC /// <summary> /// <pre>creturns>A one byte CRC</pre> public byte Pec(byte[] buffer) { byte crc = 0; int temp = 0; for (int I = 0; < buffer.Length; I++) { temp = crc ^ buffer[i]; crc = CrcTable[temp]; } return crc; } } // "namespace", "class", and associated braces may be removed for C code implementations // Copyright © Texas Instruments. All rights reserved. // THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY // OF ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLIDING BUT NOT // LIMITED TO THE IMPLIED WARRANTIES OF MERCHANTABILITY AND</summary></summary></pre>	0x06, $0x01$, $0x08$, $0x0F$, $0x1A$, $0x1D$, $0x14$, $0x13$.
<pre>0x96, 0x91, 0x98, 0x9F, 0x8A, 0x8D, 0x84, 0x83, 0xDE, 0xD9, 0xD0, 0xD7, 0xC2, 0xCC, 0xCB, 0xE6, 0xE1, 0xE8, 0xEF, 0xFA, 0xFD, 0xF4, 0xF3}; /// <summary> /// <summary> /// <summary> /// <summary> /// <returns>A one byte CRC</returns> public byte Pec(byte[] buffer) { byte crc = 0; int temp = 0; for (int I = 0; < buffer.Length; I++) { temp = crc ^ buffer[i]; crc = CrcTable[temp]; } return crc; } } // "namespace", "class", and associated braces may be removed for C code implementations // Copyright © Texas Instruments. All rights reserved. // THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY // OF ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT // LIMITED TO THE IMPLIED WARRANTIES OF MERCHANTABILITY AND</summary></summary></summary></summary></pre>	0xAE, 0xA9, 0xA0, 0xA7, 0xB2, 0xB5, 0xBC, 0xBB,
<pre>0xDE, 0xD9, 0xD0, 0xD7, 0xC2, 0xC5, 0xCC, 0xCB, 0xE6, 0xE1, 0xE8, 0xEF, 0xFA, 0xFD, 0xF4, 0xF3}; /// <summary> /// Calculates an 8 bit CRC /// <summary> /// cparam name="buffer">An array of bytes /// <returns>A one byte CRC</returns> public byte Pec(byte[] buffer) { byte crc = 0; int temp = 0; for (int I = 0; < buffer.Length; I++) { temp = crc ^ buffer[i]; crc = CrcTable[temp]; } return crc; } } // "namespace", "class", and associated braces may be removed for C code implementations // Copyright © Texas Instruments. All rights reserved. // THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY // OF ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT // LIMITED TO THE IMPLIED WARRANTIES OF MERCHANTABILITY AND</summary></summary></pre>	0x96, 0x91, 0x98, 0x9F, 0x8A, 0x8D, 0x84, 0x83,
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<pre>/// <summary> /// Calculates an 8 bit CRC /// <summary> // <pre>// <graam name="buffer">An array of bytes</graam></pre>// <pre>// <pre>//</pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></pre></summary></summary></pre>	0xE6, 0xE1, 0xE8, 0xEF, 0xFA, 0xFD, 0xF4, 0xF3};
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CRC:

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The CRC calculation implements the method used in the Smart Battery (SBData) over SMBUS – NBPC battery. It is also known as ATM-8. See the SMBUS Specification Sec 5.4.1, 5.4.1.3, at www.smbus.org/specs. For more specific implementation information, see http://smbus.org/faq/faq.htm

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