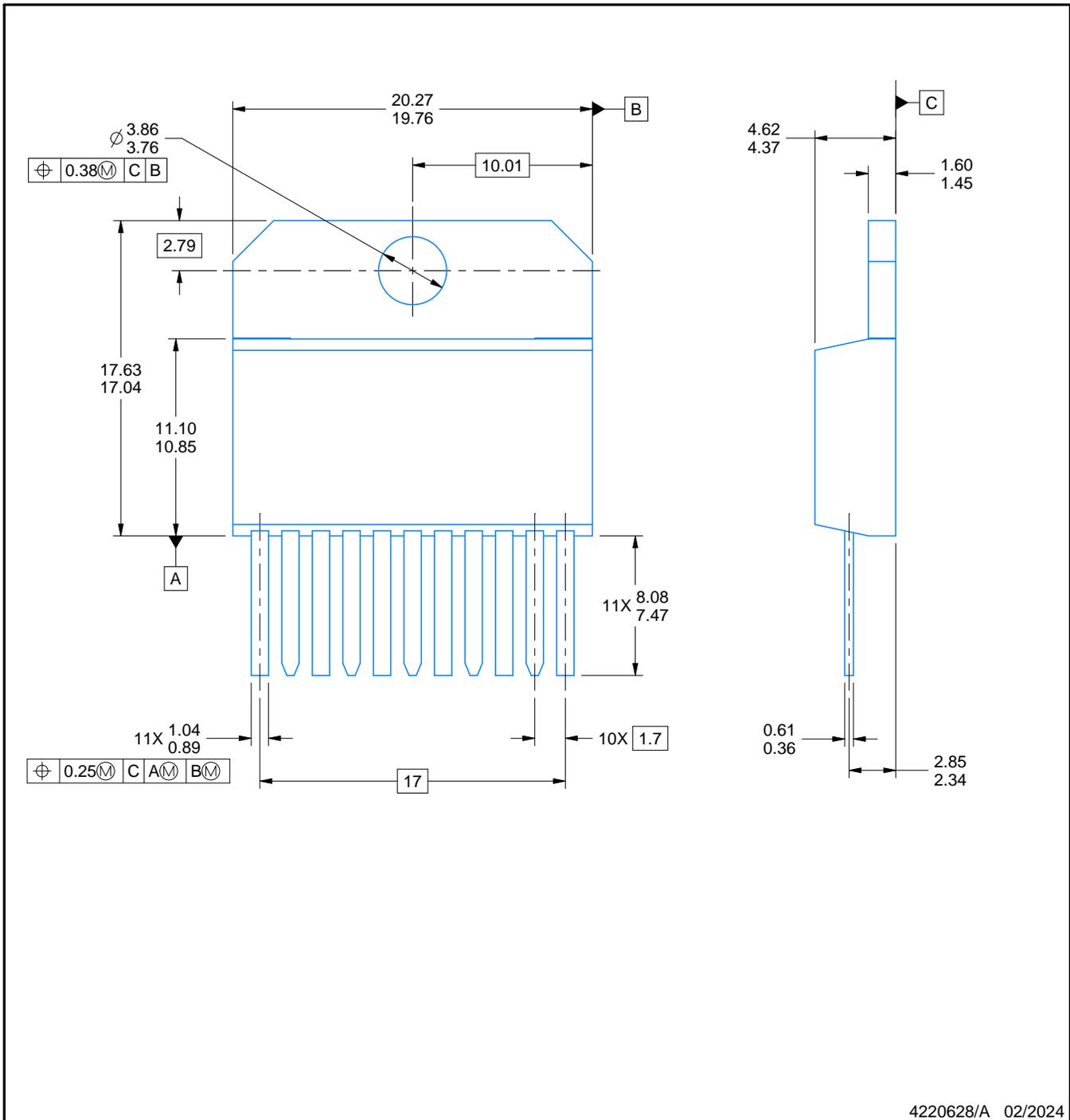


PACKAGE OUTLINE

KVC0011A

TO - 4.62 mm max height

TRANSISTOR OUTLINE



NOTES:

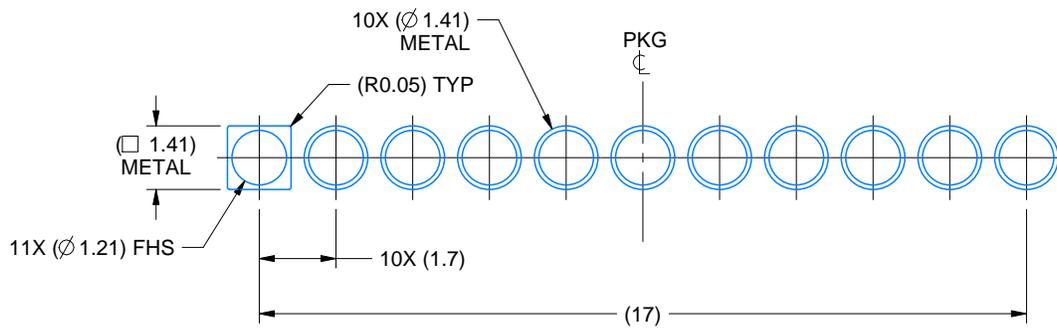
1. Dimensions are in millimeters. Any dimension in brackets or parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Falls within JEDEC MO-48-AA. Reference for body dimensions only (excluding lead forming dimensions).

EXAMPLE BOARD LAYOUT

KVC0011A

TO - 4.62 mm max height

TRANSISTOR OUTLINE



LAND PATTERN EXAMPLE
SCALE: 6X

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NOTES: (continued)

4. Refer to IPC-7251 which may have alternate design recommendations.

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