

SLVS392A - JULY 2001 - REVISED JUNE 2007

## VOLTAGE DETECTOR

#### FEATURES

- Single Voltage Detector (TPS3803): Adjustable/1.5 V
- Dual Voltage Detector (TPS3805): Adjustable/3.3 V
- High ±1.5% Threshold Voltage Accuracy
- Supply Current: 3 μA Typical at V<sub>DD</sub> = 3.3 V
- Push/Pull Reset Output (TPS3805) Open-Drain Reset Output (TPS3803)
- Temperature Range: –40°C to +85°C
- 5-Pin SC-70 Package

#### DESCRIPTION

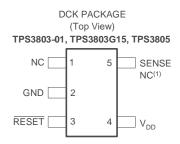
The TPS3803 and TPS3805 families of supervisory circuits provide circuit initialization and timing supervision, primarily for DSPs and processor-based systems.

The TPS3803G15 device has a fixed-sense threshold voltage V<sub>IT</sub> set by an internal voltage divider, whereas the TPS3803–01 has an adjustable SENSE input that can be configured by two external resistors. In addition to the fixed sense threshold monitored at V<sub>DD</sub>, the TPS3805 devices provide a second adjustable SENSE input. RESET is asserted in case any of the two voltages drops below V<sub>IT</sub>.

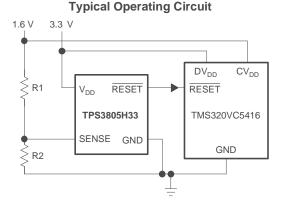
During power on,  $\overline{\text{RESET}}$  is asserted when supply voltage  $V_{DD}$  becomes higher than 0.8 V. Thereafter, the supervisory circuit monitors  $V_{DD}$  (and/or SENSE) and keeps  $\overline{\text{RESET}}$  active as long as  $V_{DD}$  or SENSE remains below the threshold voltage  $V_{IT}$ . As soon as  $V_{DD}$  (SENSE) rises above the threshold voltage  $V_{IT}$ ,  $\overline{\text{RESET}}$  is deasserted again. The product spectrum is designed for 1.5 V, 3.3 V, and adjustable supply voltages. The devices are available in a 5-pin SC-70 package. The TPS3803 and TPS3805 devices are characterized for operation over a temperature range of  $-40^{\circ}$ C to  $+85^{\circ}$ C.

#### APPLICATIONS

- Applications Using DSPs, Microcontrollers, or Microprocessors
- Wireless Communication Systems
- Portable/Battery-Powered Equipment
- Programmable Controls
- Intelligent Instruments
- Industrial Equipment
- Notebook/Desktop Computers
- Automotive Systems







**5**3

Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

All trademarks are the property of their respective owners.



All a



SLVS392A – JULY 2001 – REVISED JUNE 2007

This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

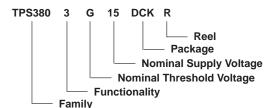
ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

#### PACKAGE INFORMATION

т <sub>А</sub>		THRESHOL		
	DEVICE NAME	V <sub>DD</sub>	SENSE	MARKING
–40°C to +85°C	TPS3803-01DCKR <sup>(1)</sup>	NA	1.226 V	AWG
	TPS3803G15DCKR <sup>(1)</sup>	1.40 V	NA	AWI
	TPS3805H33DCKR <sup>(1)</sup>	3.05 V	1.226 V	AWK

(1) The DCKR passive indicates tape and reel containing 3000 parts.

#### **ORDERING INFORMATION**



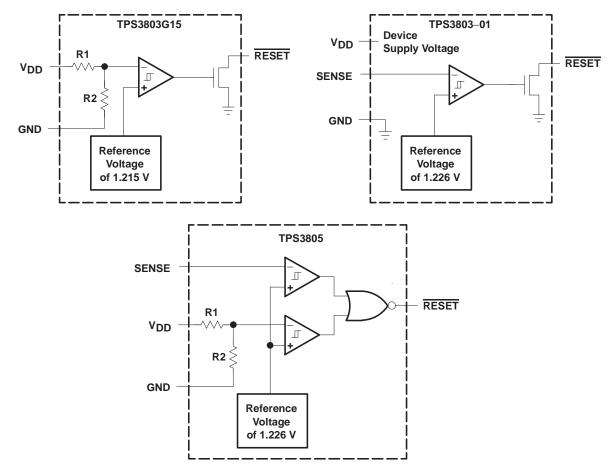
#### Function/Truth Tables

TPS3803-	01	TPS3803G15				
SENSE > V <sub>IT</sub>	RESET	V <sub>DD</sub> > V <sub>IT</sub> RESE				
0	L	0	L			
1	Н	1	Н			

TPS3805H33									
$V_{DD} > V_{IT}$	$V_{DD} > V_{IT}$ SENSE > $V_{IT}$								
0	0	L							
0	1	L							
1	0	L							
1	1	Н							



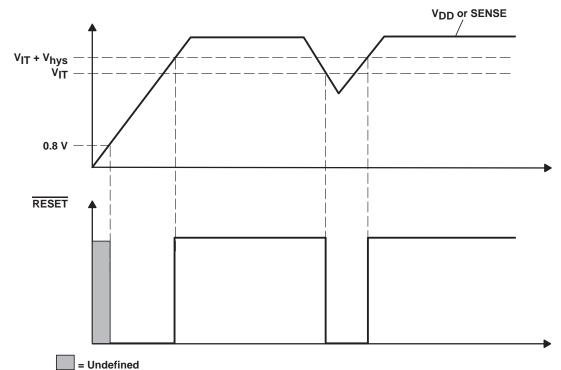
#### FUNCTIONAL BLOCK DIAGRAM



SLVS392A – JULY 2001 – REVISED JUNE 2007

#### TIMING REQUIREMENTS





#### **Terminal Functions**

TERMINAL			DECODIDEION						
NAME	NO.	1/0	DESCRIPTION						
GND	2	Ι	Ground						
RESET	3	0	Active-low reset output (TPS3803—open–drain, TPS3805—push/pull)						
SENSE	5	I	Adjustable sense input						
NC	1		No internal connection						
NC (TPS3803G15)	5		No internal connection						
V <sub>DD</sub>	4	I	Input supply voltage, fixed sense input for TPS3803G15 and TPS3805						

#### ABSOLUTE MAXIMUM RATINGS(1)

Over operating free-air temperature range, unless otherwise noted.

Supply voltage, V <sub>DD</sub> <sup>(2)</sup>	
All other pins <sup>(2)</sup>	–0.3 V to +7 V
Maximum low-output current, I <sub>OL</sub>	+5 mA
Maximum high-output current, IOH	–5 mA
Input clamp current, $I_{IK}$ (V <sub>I</sub> < 0 or V <sub>I</sub> > V <sub>DD</sub> )	±10 mA
Output clamp current, $I_{OK}$ ( $V_O < 0$ or $V_O > V_{DD}$ )	±10 mA
Continuous total power dissipation	See Dissipation Rating Table
Operating free-air temperature range, T <sub>A</sub>	–40°C to +85°C
Storage temperature range, T <sub>stg</sub>	–65°C to +150°C
Soldering temperature	

(1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) All voltage values are with respect to GND. For reliable operation the device should not be continuously operated at 7 V for more than t = 1000 h.

# DISSIPATION RATING TABLEPACKAGE $T_A < +25^{\circ}C$ <br/>POWER RATINGDERATING FACTOR<br/>ABOVE $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$ <br/>POWER RATING $T_A = +85^{\circ}C$ <br/>POWER RATINGDCK321 mW2.6 mW/°C206 mW167 mW

#### **RECOMMENDED OPERATING CONDITIONS**

	MIN	MAX	UNIT
Supply voltage, V <sub>DD</sub>	1.3	6	V
Input voltage, VI	0	V <sub>DD</sub> + 0.3	V
Operating free-air temperature range, TA	-40	85	°C



SLVS392A - JULY 2001 - REVISED JUNE 2007

#### **ELECTRICAL CHARACTERISTICS**

Over recommended operating free-air temperature range, unless otherwise noted.

	PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT	
VOH	High-level output voltage (TPS3805 or		0.8 x V <sub>DD</sub>			V		
VOL	Low-level output voltage		-		0.3	V		
	Dower up react voltage(1)	25°C	0.8			V		
	Power-up reset voltage <sup>(1)</sup>	VIT $\leq$ 1.5 V, TA =	25°C	1.0			V	
		SENSE		1.208	1.226	1.244		
VIT	Negative-going input threshold voltage <sup>(2)</sup>	TPS3803G15	$T_A = -40^{\circ}C$ to $+85^{\circ}C$	1.379	1.4	1.421	V	
	Vollago	TPS3805H33		3.004	3.05	3.096		
			$1.2 \text{ V} < \text{V}_{IT} < 2.5 \text{ V}$		15			
V <sub>hys</sub>	Hysteresis		$2.5 \text{ V} < \text{V}_{IT} < 3.5 \text{ V}$		30		mV	
Ιį	Input current	SENSE		-25		25	nA	
IOH	High-level output current at RESET	Open drain only	$V_{DD} = V_{IT} + 0.2V, V_{OH} = V_{DD}$			300	nA	
		TPS3803-01			2	4		
		TPS3805, TPS3803G15	$V_{DD} = 3.3 V$ , output unconnected		3	5		
IDD	Supply current	TPS3803-01			2	4	μA	
		TPS3805, TPS3803G15	V <sub>DD</sub> = 6 V, output unconnected		4	6		
Cl	Input capacitance		$V_{I} = 0 V \text{ to } V_{DD}$		1		pF	

(1) The lowest supply voltage at which  $\overline{\text{RESET}}$  (V<sub>OL</sub>(max) = 0.2 V, I<sub>OL</sub> = 50 µA) becomes active. t<sub>r</sub>(V<sub>DD</sub>)  $\geq$  15 µs/V. (2) To ensure the best stability of the threshold voltage, place a bypass capacitor (ceramic, 0.1 µF) near the supply terminals.

#### TIMING REQUIREMENTS

AT  $R_L = 1 M\Omega$ ,  $C_L = 50 PF$ ,  $T_A = -40^{\circ}C TO +85^{\circ}C$ .

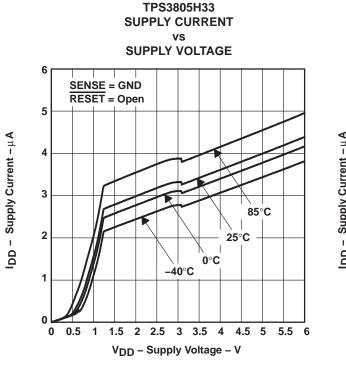
PARAMETER			TEST CONDITIONS	MIN	TYP	MAX	UNIT
	Dula a utidah	At V <sub>DD</sub>					
۲W	Pulse width	At SENSE	$V_{IH} = 1.05 \times V_{IT}, V_{IL} = 0.95 \times V_{IT}$	5.5			μs

#### SWITCHING CHARACTERISTICS

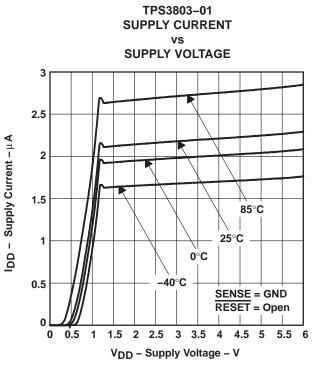
AT  $R_L = 1 M\Omega$ ,  $C_L = 50 PF$ ,  $T_A = -40^{\circ}C TO +85^{\circ}C$ .

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
<sup>t</sup> PHL	Propagation (delay) time, high-to-low-level output	V <sub>DD</sub> to RESET delay SENSE to RESET delay	V <sub>IH</sub> = 1.05 × V <sub>IT</sub> ,		5	100	_
<sup>t</sup> PLH	Propagation (delay) time, low-to-high-level output	V <sub>DD</sub> to RESET delay SENSE to RESET delay	VIL = 0.95 x VIT		5	100	μs

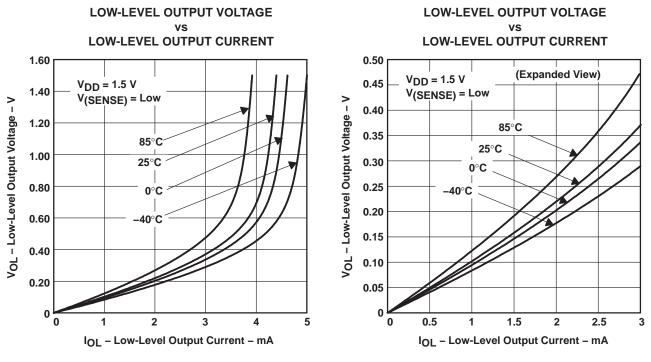
#### **TYPICAL CHARACTERISTICS**











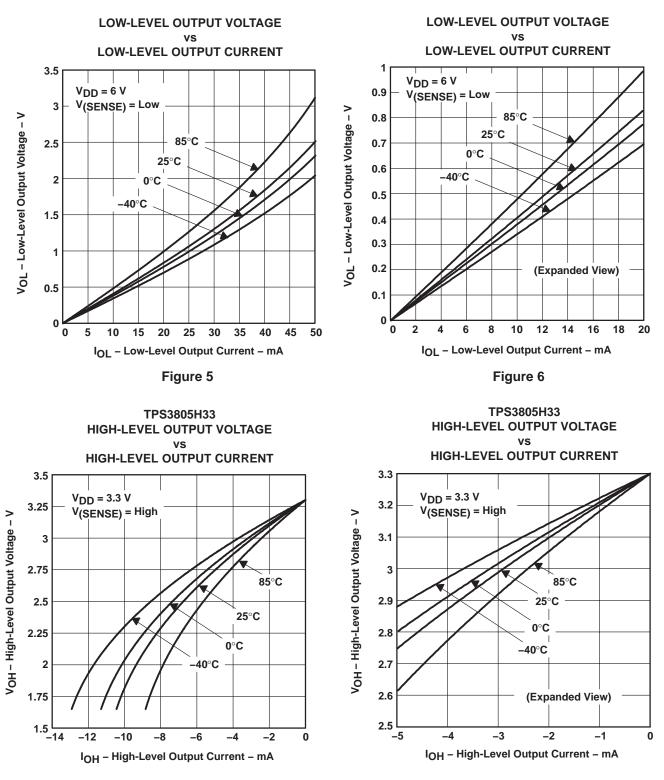




SLVS392A - JULY 2001 - REVISED JUNE 2007



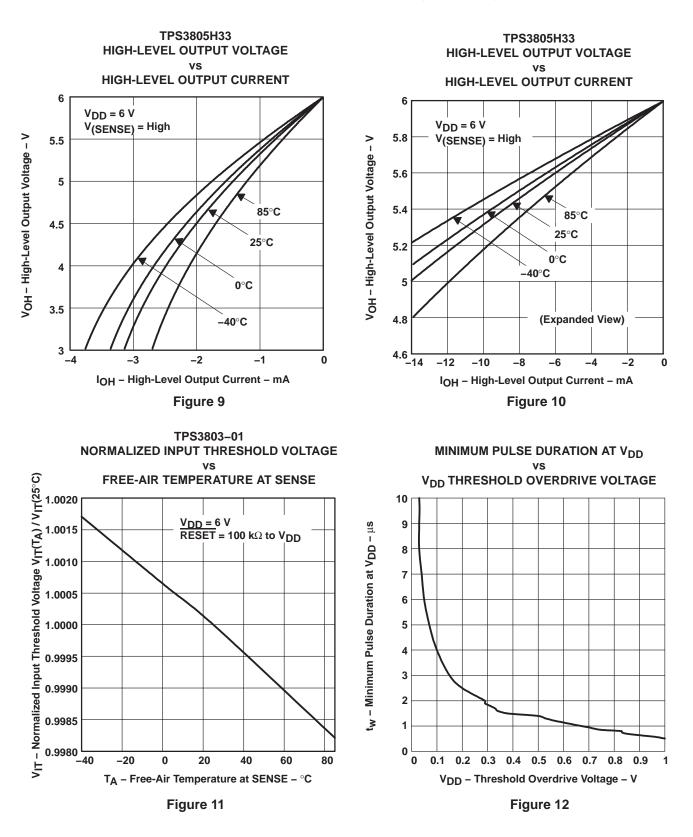
#### **TYPICAL CHARACTERISTICS (continued)**







#### **TYPICAL CHARACTERISTICS (continued)**



SLVS392A - JULY 2001 - REVISED JUNE 2007



#### **TYPICAL CHARACTERISTICS (continued)**

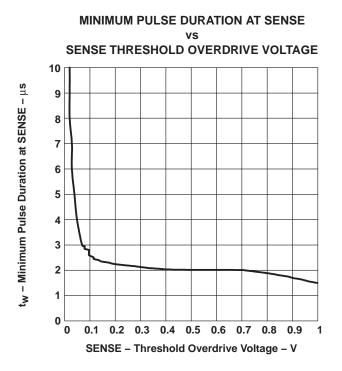


Figure 13

#### **Revision History**

DATE	REV	PAGE	SECTION	DESCRIPTION
6/07	Δ	Front Page	—	Updated front page.
0/07	~	3	—	Functional block diagram change.

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.



#### **PACKAGING INFORMATION**

Orderable part number	Status	Material type	Package   Pins	Package qty   Carrier	RoHS	Lead finish/	MSL rating/	Op temp (°C)	Part marking
	(1)	(2)			(3)	Ball material	Peak reflow		(6)
						(4)	(5)		
TPS3803-01DCKR	Active	Production	SC70 (DCK)   5	3000   LARGE T&R	Yes	NIPDAU   NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	AWG
TPS3803-01DCKR.B	Active	Production	SC70 (DCK)   5	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	AWG
TPS3803G15DCKR	Active	Production	SC70 (DCK)   5	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-	AWI
TPS3803G15DCKR.B	Active	Production	SC70 (DCK)   5	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	AWI
TPS3805H33DCKR	Active	Production	SC70 (DCK)   5	3000   LARGE T&R	Yes	NIPDAU   NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	AWK
TPS3805H33DCKR.B	Active	Production	SC70 (DCK)   5	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 85	AWK

<sup>(1)</sup> **Status:** For more details on status, see our product life cycle.

<sup>(2)</sup> Material type: When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

<sup>(3)</sup> RoHS values: Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.

<sup>(4)</sup> Lead finish/Ball material: Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

<sup>(5)</sup> MSL rating/Peak reflow: The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

<sup>(6)</sup> Part marking: There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.



OTHER QUALIFIED VERSIONS OF TPS3803, TPS3805H33 :

• Automotive : TPS3803-Q1, TPS3805H33-Q1

• Enhanced Product : TPS3803-EP, TPS3805H33-EP

NOTE: Qualified Version Definitions:

- Automotive Q100 devices qualified for high-reliability automotive applications targeting zero defects
- Enhanced Product Supports Defense, Aerospace and Medical Applications



Texas

STRUMENTS

#### TAPE AND REEL INFORMATION





#### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal												
Device	-	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS3803-01DCKR	SC70	DCK	5	3000	178.0	9.0	2.4	2.5	1.2	4.0	8.0	Q3
TPS3803G15DCKR	SC70	DCK	5	3000	180.0	8.4	2.41	2.41	1.2	4.0	8.0	Q3
TPS3805H33DCKR	SC70	DCK	5	3000	178.0	9.0	2.4	2.5	1.2	4.0	8.0	Q3



www.ti.com

## PACKAGE MATERIALS INFORMATION

4-Jan-2025



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS3803-01DCKR	SC70	DCK	5	3000	180.0	180.0	18.0
TPS3803G15DCKR	SC70	DCK	5	3000	183.0	183.0	20.0
TPS3805H33DCKR	SC70	DCK	5	3000	180.0	180.0	18.0

## **DCK0005A**



## **PACKAGE OUTLINE**

#### SOT - 1.1 max height

SMALL OUTLINE TRANSISTOR



NOTES:

- 1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
  This drawing is subject to change without notice.
  Reference JEDEC MO-203.

- 4. Support pin may differ or may not be present.5. Lead width does not comply with JEDEC.
- 6. Body dimensions do not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.25mm per side



## **DCK0005A**

## **EXAMPLE BOARD LAYOUT**

#### SOT - 1.1 max height

SMALL OUTLINE TRANSISTOR



NOTES: (continued)

Publication IPC-7351 may have alternate designs.
Solder mask tolerances between and around signal pads can vary based on board fabrication site.



## DCK0005A

## **EXAMPLE STENCIL DESIGN**

### SOT - 1.1 max height

SMALL OUTLINE TRANSISTOR



NOTES: (continued)

9. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

10. Board assembly site may have different recommendations for stencil design.



#### IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2025, Texas Instruments Incorporated