



## Low-Noise, Low-Distortion, $G = 2000$ INSTRUMENTATION AMPLIFIER

### FEATURES

- LOW NOISE:  $1.3\text{nV}/\sqrt{\text{Hz}}$  at 1kHz
- LOW THD+N: 0.09% at 1kHz
- WIDE BANDWIDTH: 450kHz
- WIDE SUPPLY RANGE:  $\pm 4.5\text{V}$  to  $\pm 18\text{V}$
- HIGH CMR:  $> 100\text{dB}$
- GAIN SET WITH EXTERNAL RESISTOR
- SO-14 SURFACE-MOUNT PACKAGE

### APPLICATIONS

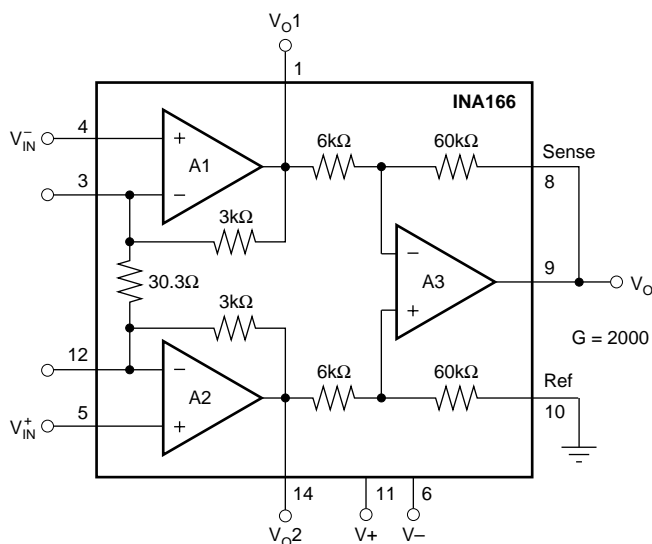
- MOVING-COIL TRANSDUCER AMPLIFIERS
- DIFFERENTIAL RECEIVERS
- BRIDGE TRANSDUCER AMPLIFIERS
- MICROPHONE AND HYDROPHONE PREAMPS

### DESCRIPTION

The INA166 is a very low-noise, low-distortion, monolithic instrumentation amplifier. Its current-feedback circuitry achieves very wide bandwidth and excellent dynamic response over a wide range of gain. It is ideal for low-level signals such as microphones or hydrophones. Many industrial, instrumentation, and medical applications also benefit from its low noise and wide bandwidth.

Unique distortion cancellation circuitry reduces distortion to extremely low levels, even in high gain. The INA166 provides near-theoretical noise performance for  $200\Omega$  source impedance. Its differential input, low noise, and low distortion provide superior performance as a low-level signal amplifier.

The INA166 is available in a space-saving SO-14 surface-mount package, specified for operation over the  $-40^\circ\text{C}$  to  $+85^\circ\text{C}$  temperature range.

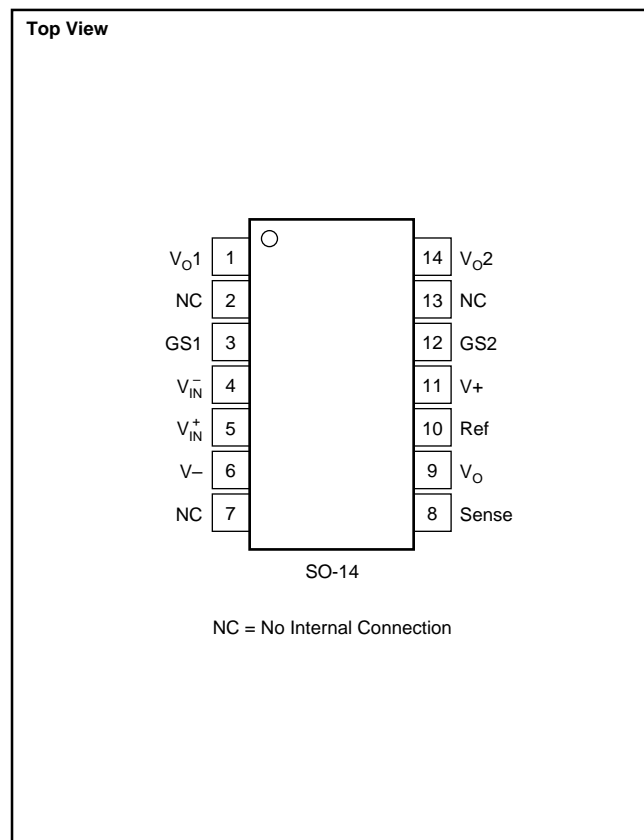


# SPECIFICATIONS: $V_S = \pm 5V$

$T_A = +25^\circ\text{C}$  and at rated supplies,  $V_S = \pm 5V$ ,  $R_L = 2k\Omega$  connected to ground,  $G = 2000$ , unless otherwise noted.

PARAMETER	CONDITIONS	INA166UA			UNITS
		MIN	TYP	MAX	
<b>GAIN</b> Gain Error Gain Temp Drift Coefficient Nonlinearity			$\pm 0.3$ $\pm 10$ $\pm 0.005$	$\pm 1$	% ppm/ $^\circ\text{C}$ % of FS
<b>INPUT REFERRED NOISE</b> Voltage Noise $f_O = 1\text{kHz}$ $f_O = 100\text{Hz}$ $f_O = 10\text{Hz}$ Current Noise $f_O = 1\text{kHz}$	$R_{\text{SOURCE}} = 0\Omega$		1.3 1.6 2 0.8		nV/ $\sqrt{\text{Hz}}$ nV/ $\sqrt{\text{Hz}}$ nV/ $\sqrt{\text{Hz}}$ pA/ $\sqrt{\text{Hz}}$
<b>INPUT OFFSET VOLTAGE</b> Input Offset Voltage vs Temperature vs Power Supply	$V_{\text{CM}} = V_{\text{OUT}} = 0V$ $T_A = T_{\text{MIN}}$ to $T_{\text{MAX}}$ $V_S = \pm 4.5V$ to $\pm 18V$		$\pm 50$ $\pm 2.5$ $\pm 1$	$\pm 250$  $\pm 3$	$\mu V$ $\mu V/^\circ\text{C}$ $\mu V/V$
<b>INPUT VOLTAGE RANGE</b> Common-Mode Voltage Range  Common-Mode Rejection	$V_{\text{IN}}^+ - V_{\text{IN}}^- = 0V$ $V_{\text{IN}}^+ - V_{\text{IN}}^- = 0V$ $V_{\text{CM}} = \pm 1V$ , $R_{\text{SRC}} = 0\Omega$	$(V^+) - 4$ $(V^-) + 4$ 100	$(V^+) - 3$ $(V^-) + 3$ 120		V V dB
<b>INPUT BIAS CURRENT</b> Initial Bias Current vs Temperature Initial Offset Current vs Temperature			2.5 15 0.1 0.5	12  1	$\mu A$ nA/ $^\circ\text{C}$ $\mu A$ nA/ $^\circ\text{C}$
<b>INPUT IMPEDANCE</b>	Differential Common-Mode		60    2 60    2		M $\Omega$    pF M $\Omega$    pF
<b>DYNAMIC RESPONSE</b> Bandwidth, Small Signal, $-3\text{dB}$ Slew Rate THD+Noise, $f = 1\text{kHz}$ Settling Time, 0.1% 0.01% Overload Recovery	5V Step 5V Step 50% Overdrive		450 15 0.09 2.5 3.5 1		kHz V/ $\mu s$ % $\mu s$ $\mu s$ $\mu s$
<b>OUTPUT</b> Voltage  Load Capacitance Stability Short-Circuit Current	$R_L = 2k\Omega$ to Ground  Continuous-to-Common	$(V^+) - 2$ $(V^-) + 2$	$(V^+) - 1.8$ $(V^-) + 1.8$ 1000 $\pm 60$		V V pF mA
<b>POWER SUPPLY</b> Rated Voltage Voltage Range Current, Quiescent	$I_O = 0\text{mA}$	$\pm 4.5$	$\pm 5$ $\pm 10$	$\pm 18$ $\pm 12$	V V mA
<b>TEMPERATURE RANGE</b> Specification Operating Thermal Resistance, $\theta_{JA}$		$-40$ $-40$	100	$+85$ $+125$	$^\circ\text{C}$ $^\circ\text{C}$ $^\circ\text{C/W}$

## PIN CONFIGURATION



## ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Burr-Brown recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### ABSOLUTE MAXIMUM RATINGS<sup>(1)</sup>

Power Supply Voltage .....	±18V
Signal Input Terminals, Voltage <sup>(2)</sup> .....	(V-) - 0.5V to (V+) + 0.5V
Current <sup>(2)</sup> .....	10mA
Output Short-Circuit to Ground .....	Continuous
Operating Temperature .....	-55°C to +125°C
Storage Temperature .....	-55°C to +125°C
Junction Temperature .....	+150°C
Lead Temperature (soldering, 10s) .....	+300°C

NOTES: (1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied. (2) Input terminals are diode-clamped to the power-supply rails. Input signals that can swing more than 0.5V beyond the supply rails should be current limited to 10mA or less.

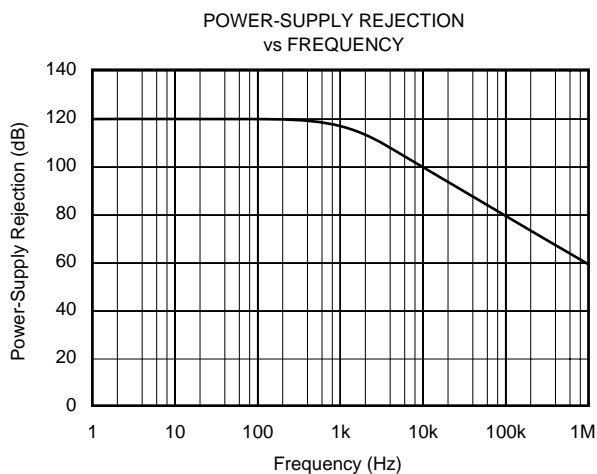
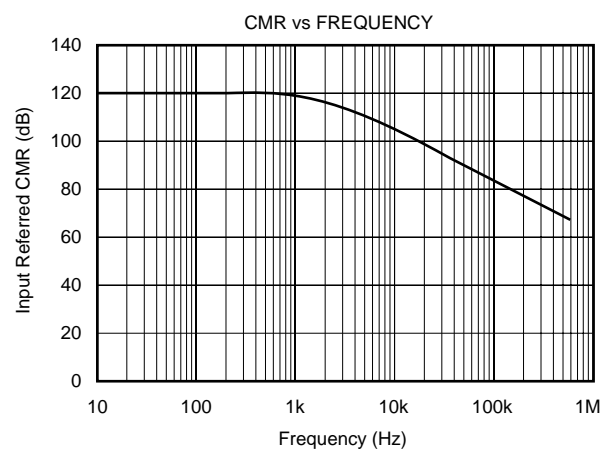
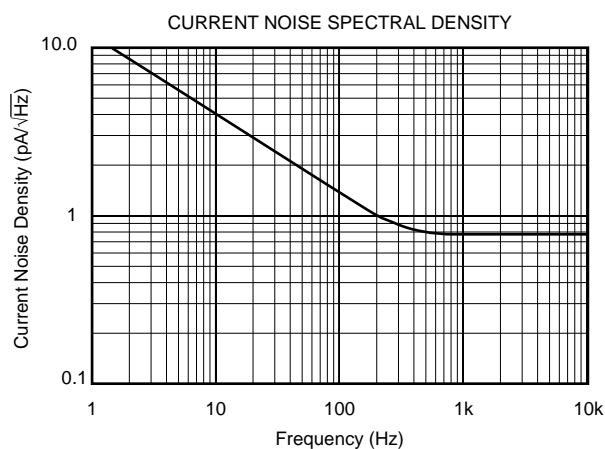
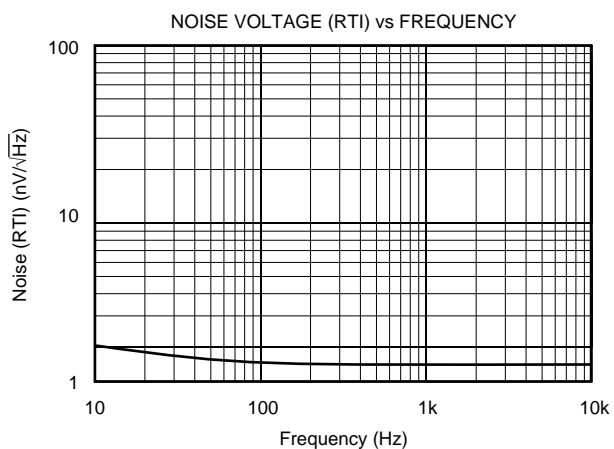
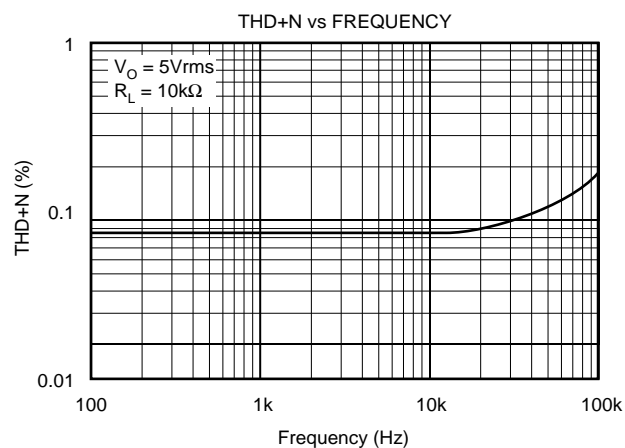
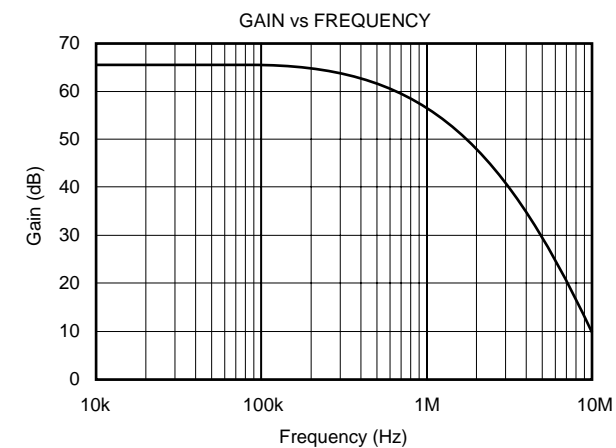
## PACKAGE/ORDERING INFORMATION

PRODUCT	PACKAGE	PACKAGE DRAWING NUMBER	PACKAGE MARKING	ORDERING NUMBER <sup>(1)</sup>	TRANSPORT MEDIA
INA166UA "	SO-14 Surface Mount "	235 "	INA166UA "	INA166UA INA166UA/2K5	Rails Tape and Reel

NOTE: (1) Models with a slash (/) are available only in Tape and Reel in the quantities indicated (e.g., /2K5 indicates 2500 devices per reel). Ordering 2500 pieces of "INA166UA/2K5" will get a single 2500-piece Tape and Reel.

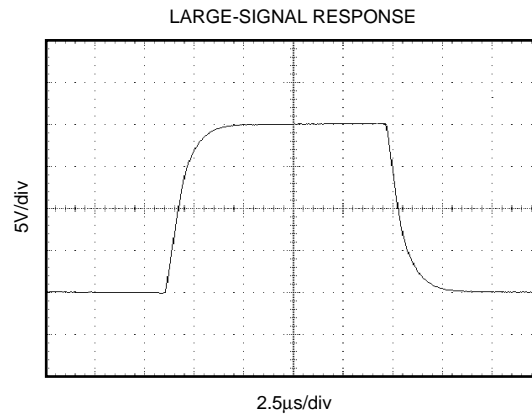
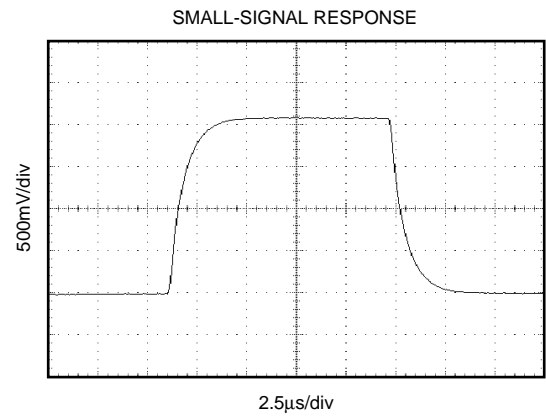
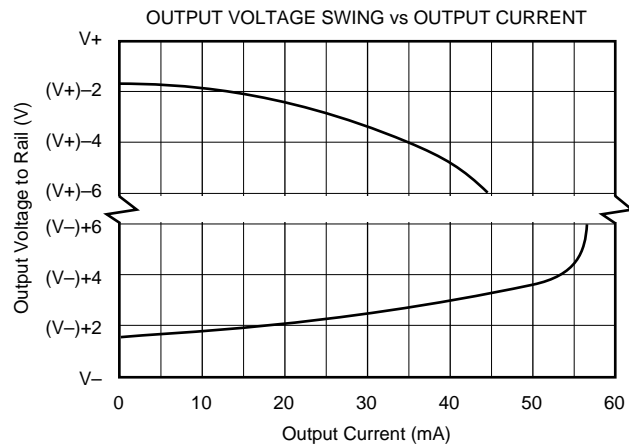
# TYPICAL PERFORMANCE CURVES

At  $T_A = +25^\circ\text{C}$ ,  $V_S = \pm 5\text{V}$ ,  $R_L = 2\text{k}\Omega$ ,  $C_L = 50\text{pF}$ ,  $G = 2000$ , unless otherwise noted.



# TYPICAL PERFORMANCE CURVES (Cont.)

At  $T_A = +25^\circ\text{C}$ ,  $V_S = \pm 5\text{V}$ ,  $R_L = 2\text{k}\Omega$ ,  $C_L = 50\text{pF}$ ,  $G = 2000$ , unless otherwise noted.



# APPLICATIONS INFORMATION

Figure 1 shows the basic connections required for operation. Power supplies should be bypassed with 0.1µF tantalum capacitors near the device pins. The output Sense (pin 8) and output Reference (pin 10) should be low-impedance connections. Resistance of greater than 5Ω in series with these connections will degrade the common-mode rejection of the INA166.

## GAIN

Gain of the INA166 is internally set for  $G = 2000$ . Input stage (A1, A2) gain is 200 and the output stage gain (A3) is 10. Internal resistor values are laser trimmed for accurate ratios to achieve excellent gain accuracy and common-mode rejection, but absolute resistor values are approximately  $\pm 20\%$ . Nominal resistor values are shown.

Although the INA166 is primarily intended for fixed-gain applications, the gain can be increased by connecting a gain-set resistor,  $R_G$ , between pin 3 and pin 12. The nominal gain will be:

$$G = 2000 + \frac{60000}{R_G}$$

Accuracy of the 60000 term in this equation is approximately  $\pm 20\%$ . The stability and temperature drift of  $R_G$  contributes to the overall gain accuracy and these effects can be inferred from this gain equation.

## NOISE PERFORMANCE

The INA166 provides very low-noise with low-source impedance. Its  $1.3\text{nV}/\sqrt{\text{Hz}}$  voltage noise delivers near-theoretical noise performance with a source impedance of 200Ω.

The input stage design used to achieve this low noise, results in relatively high input bias current and input bias current noise. As a result, the INA166 may not provide the best noise performance with a source impedance greater than 10kΩ. For source impedance greater than 10kΩ, other instrumentation amplifiers may provide improved noise performance.

## INPUT CONSIDERATIONS

Very low source impedance (less than 10Ω) can cause the INA166 to oscillate. This depends on circuit layout, signal source, and input cable characteristics. An input network consisting of a small inductor and resistor, as shown in Figure 2, can greatly reduce any tendency to oscillate. This is especially useful if a variety of input sources are to be connected to the INA166. Although not shown in other figures, this network can be used as needed with all applications shown.

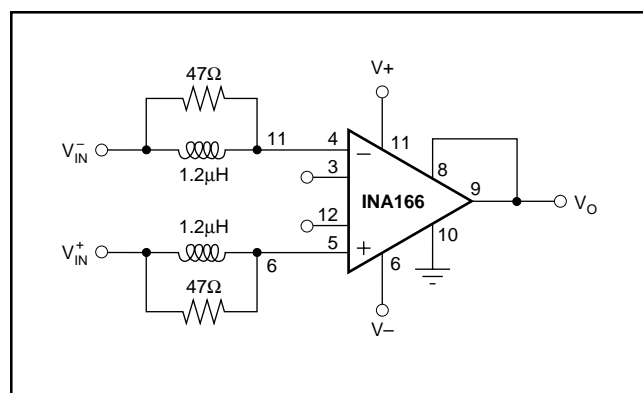


FIGURE 2. Input Stabilization Network.

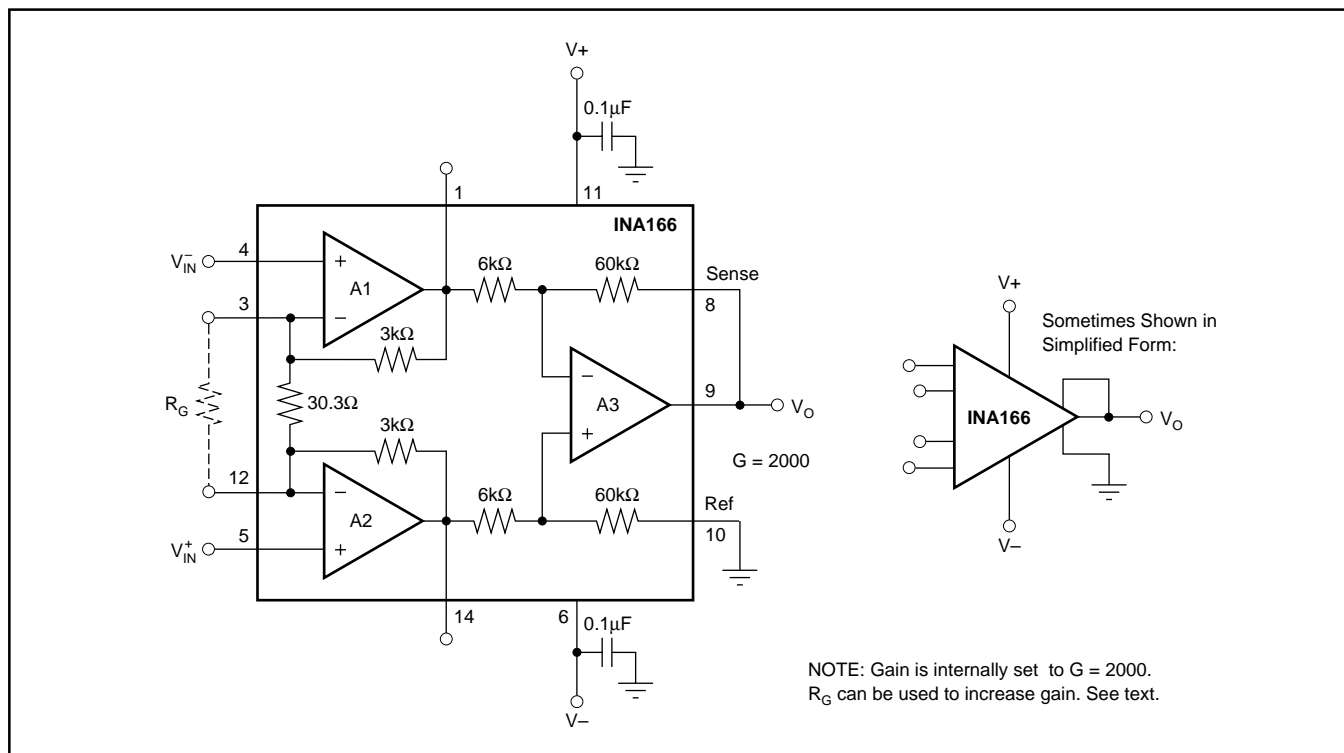


FIGURE 1. Basic Circuit Connections.

## OFFSET VOLTAGE TRIM

A variable voltage applied to pin 10, as shown in Figure 3, can be used to adjust the output offset voltage. A voltage applied to pin 10 is summed with the output signal. An op amp connected as a buffer is used to provide a low impedance at pin 10 to assure good common-mode rejection.

## OUTPUT SENSE

An output sense terminal allows greater gain accuracy in driving the load. By connecting the sense connection at the load,  $I \cdot R$  voltage loss to the load is included inside the feedback loop. Current drive can be increased by connecting a buffer amp inside the feedback loop, as shown in Figure 4.

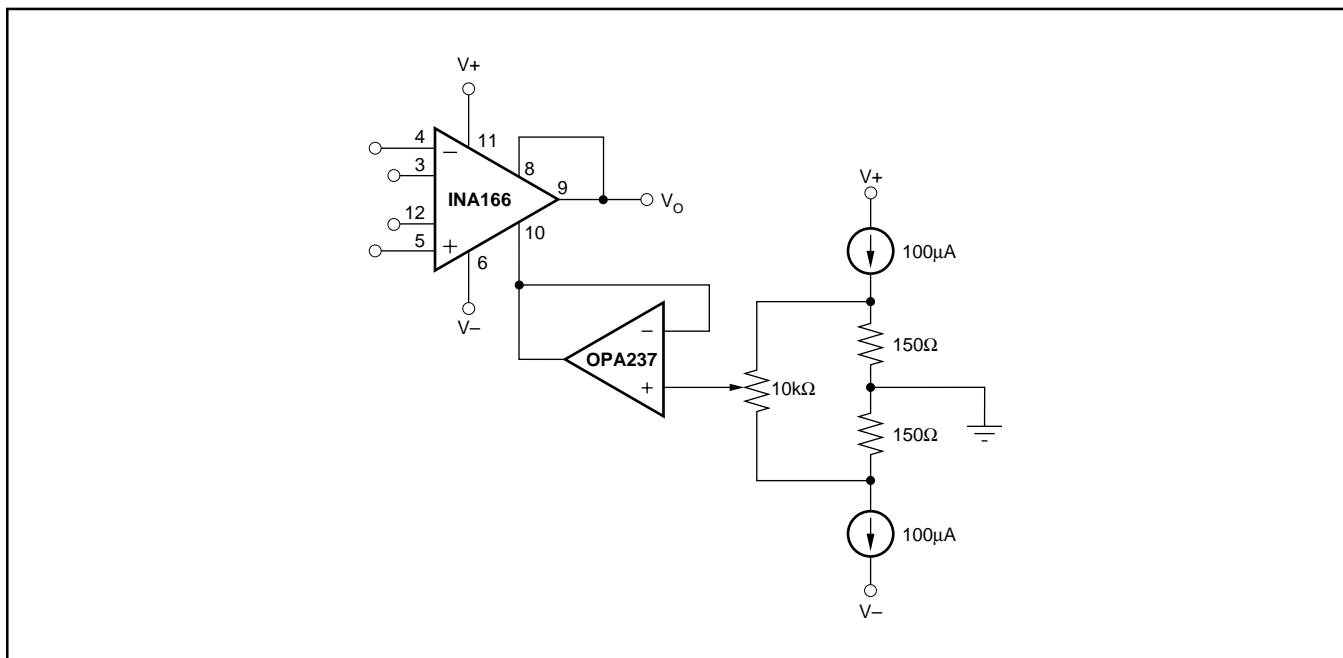


FIGURE 3. Offset Voltage Adjustment Circuit.

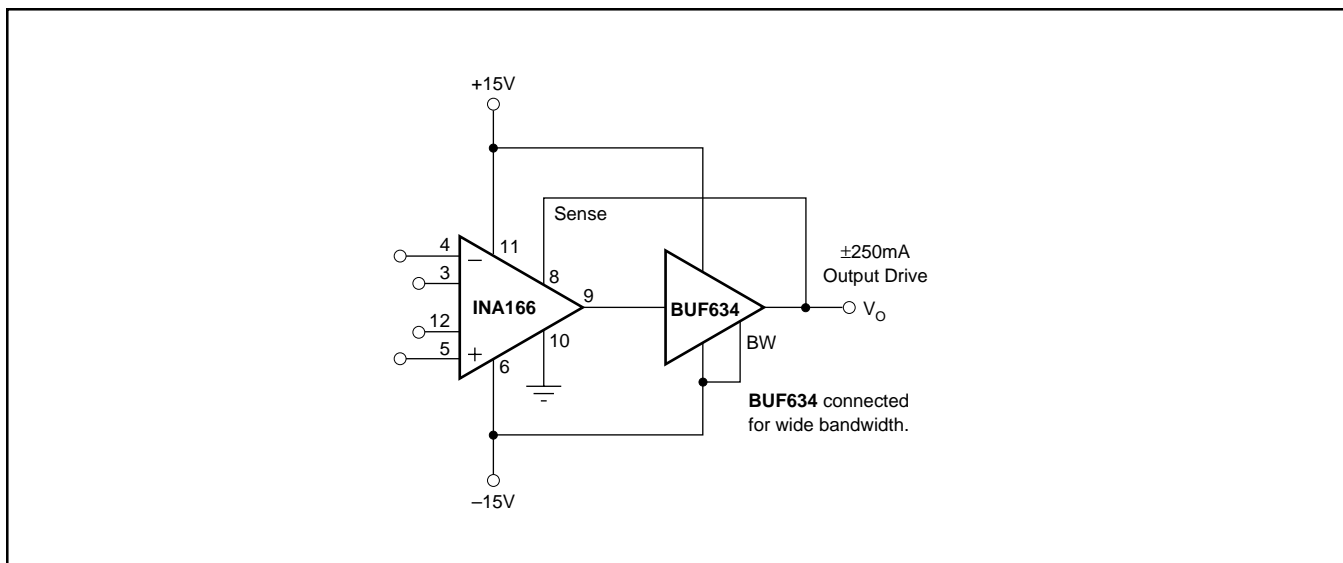


FIGURE 4. Buffer for Increase Output Current.

## PACKAGING INFORMATION

Orderable part number	Status (1)	Material type (2)	Package   Pins	Package qty   Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
INA166UA	Obsolete	Production	SOIC (D)   14	-	-	Call TI	Call TI	-55 to 125	INA166UA
INA166UA/2K5	Active	Production	SOIC (D)   14	2500   LARGE T&R	Yes	NIPDAU	Level-3-260C-168 HR	-55 to 125	INA166UA
INA166UA/2K5.A	Active	Production	SOIC (D)   14	2500   LARGE T&R	Yes	NIPDAU	Level-3-260C-168 HR	-55 to 125	INA166UA

<sup>(1)</sup> **Status:** For more details on status, see our [product life cycle](#).

<sup>(2)</sup> **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

<sup>(3)</sup> **RoHS values:** Yes, No, RoHS Exempt. See the [TI RoHS Statement](#) for additional information and value definition.

<sup>(4)</sup> **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

<sup>(5)</sup> **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

<sup>(6)</sup> **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.



## TAPE AND REEL INFORMATION



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
INA166UA/2K5	SOIC	D	14	2500	330.0	16.4	6.5	9.0	2.1	8.0	16.0	Q1

## TAPE AND REEL BOX DIMENSIONS



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
INA166UA/2K5	SOIC	D	14	2500	353.0	353.0	32.0

## IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
Copyright © 2025, Texas Instruments Incorporated